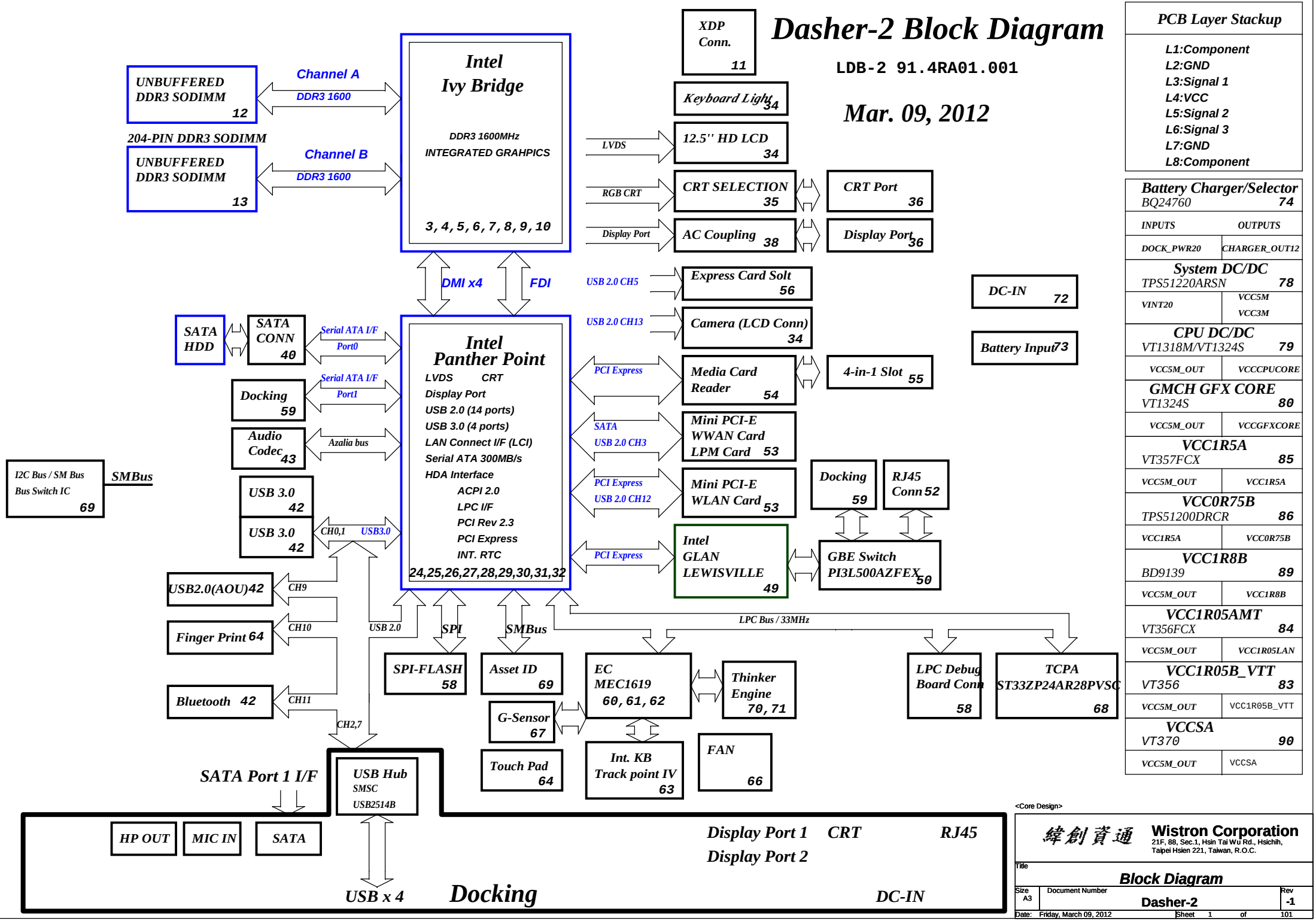
Dasher-2 Block Diagram

LDB-2 91.4RA01.001

Mar. 09, 2012



PCB Layer Stackup

L1:Component
L2:GND
L3:Signal 1
L4:VCC
L5:Signal 2
L6:Signal 3
L7:GND
L8:Component

Battery Charger/Selector BQ24760 — 74

INPUTS	OUTPUTS
DOCK_PWR20	CHARGER_OUT12

System DC/DC TPS51220ARSN — 78

VINT20	VCC5M VCC3M

CPU DC/DC VT1318M/VT1324S — 79

VCC5M_OUT	VCCCPUCORE

GMCH GFX CORE VT1324S — 80

VCC5M_OUT	VCCGFXCORE

VCC1R5A VT357FCX — 85

VCC5M_OUT	VCC1R5A

VCC0R75B TPS51200DRCR — 86

VCC1R5A	VCC0R75B

VCC1R8B BD9139 — 89

VCC5M_OUT	VCC1R8B

VCC1R05AMT VT356FCX — 84

VCC5M_OUT	VCC1R05LAN

VCC1R05B_VTT VT356 — 83

VCC5M_OUT	VCC1R05B_VTT

VCCSA VT370 — 90

VCC5M_OUT	VCCSA

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Taipei Hsien 221, Taiwan, R.O.C.

Title: Block Diagram

Size: A3 | Document Number: Dasher-2 | Rev: -1

Date: Friday, March 09, 2012 | Sheet 1 of 101

RESISTOR

Symbol name	Value	Tolerance (J: 5%, F: 1%, D: 0.5%, B: 0.1 %)	Rating 0402=> 1/16W, 25V 0603 => 1/16W, 75V 0805 => 1/10W, 100V	Size 2=>0402, 3=>0603, 5=>0805, 6=>1206, 0=>1210

The naming rule is value + R + size + tolerance
For the value, it can be read by the number before R. (R means resistor)
For the tolerance, it can be read from the last letter.
For the rating, we don't show on the symbol name.
For the size, R2=>0402, R3=>0603, R5=>0805,....

CAPACITOR

Symbol name	Value	Tolerance (M: +/-20, K: +/-10, Z: +80/-20)	Rating	Size 2=>0402, 3=>0603, 5=>0805, 6=>1206, 0=>1210

The naming rule is
Capacitor type + value + rating + size + tolerance + material
SCD1U10V2MX-1
SC=> SMT Ceremic, TC=> POS cap or SP cap
D1U => 0.1uF
10V => the voltage rating is 10V
2=> 0402, 3=>0603, 5=>0805
M=>tolerance M, K, Z
X=> X7R/X5R, Y=> Y5V
-1 => symbol version, nonsense to EE characteristic

PLANAR_ID[3..0]

IBEXPEAK-M	39	38	48	49	Planar ID Version	Planar PCB Version
PLANAR_IDn	3	2	1	0		
	0	0	0	0	Dasher-2 initial	N/A
	0	0	0	1	Dasher-2 PreDV	SA
	0	0	1	0	Dasher-2 SDV	SA
	0	0	1	1	Dasher-2 FVT	SB
	0	1	0	0	Dasher-2 PreSIT	SC
	0	1	0	1	Dasher-2 SIT	SD
	0	1	1	0	Dasher-2 SVT	-1
	0	1	1	1		
	1	0	0	0		
	1	0	0	1		
	1	0	1	0		

EC HISTORY

Stage	Date	EC No.	Page	Note

<Core Design>

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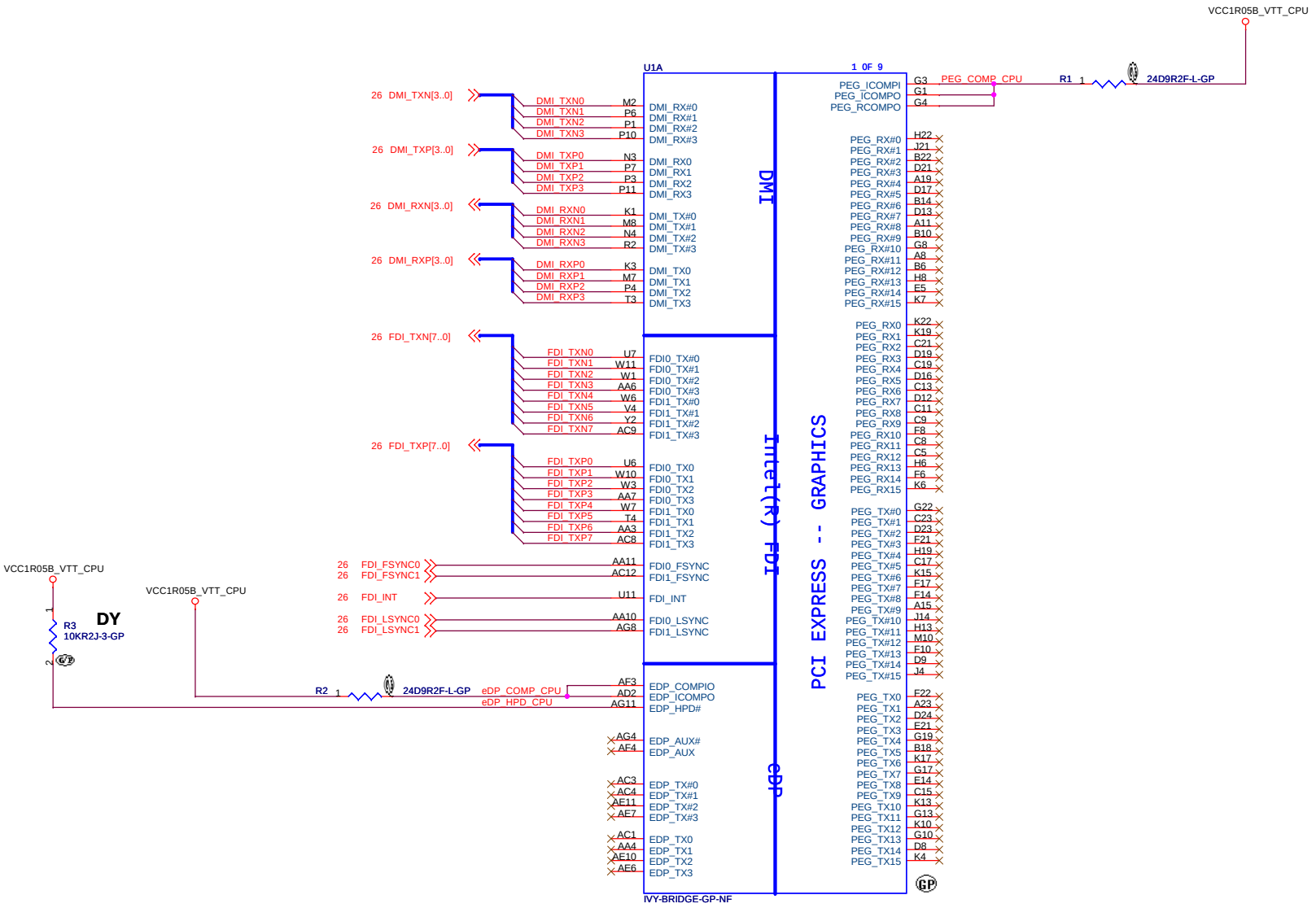
Title: *Reference*

Size: A3 | Document Number: Dasher-2 | Rev: -1

Date: Tuesday, February 21, 2012 | Sheet 2 of 101



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Taipei Hsien 221, Taiwan, R.O.C.

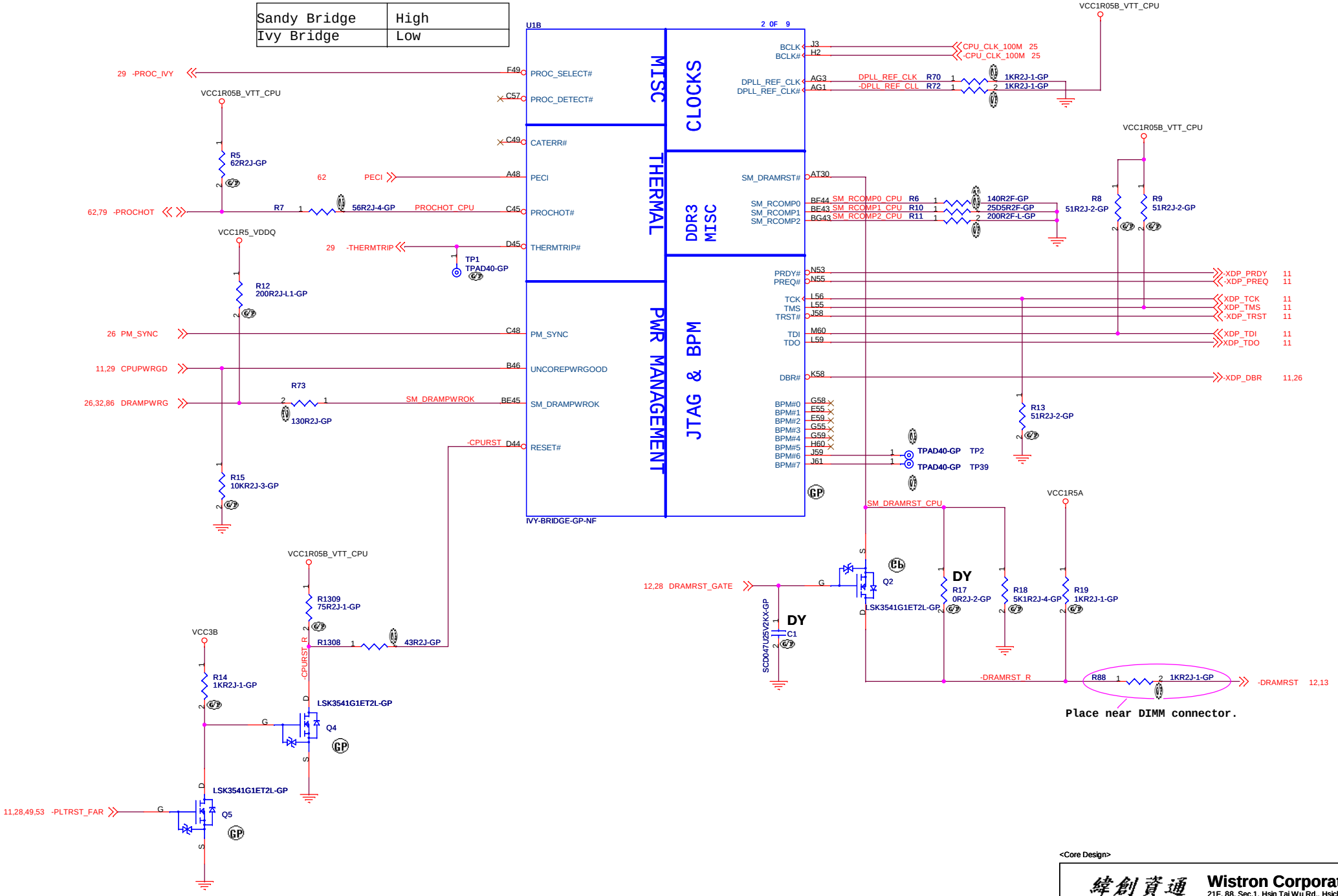
Title: **CPU(1/8):DMI/EDP/PEG/FDI**

Size	Document Number	Rev
A3	Dasher-2	-1

Date: Friday, March 09, 2012 Sheet 3 of 101

Table -PROC_SELECT(-PROC_IVY)

Sandy Bridge	High
Ivy Bridge	Low



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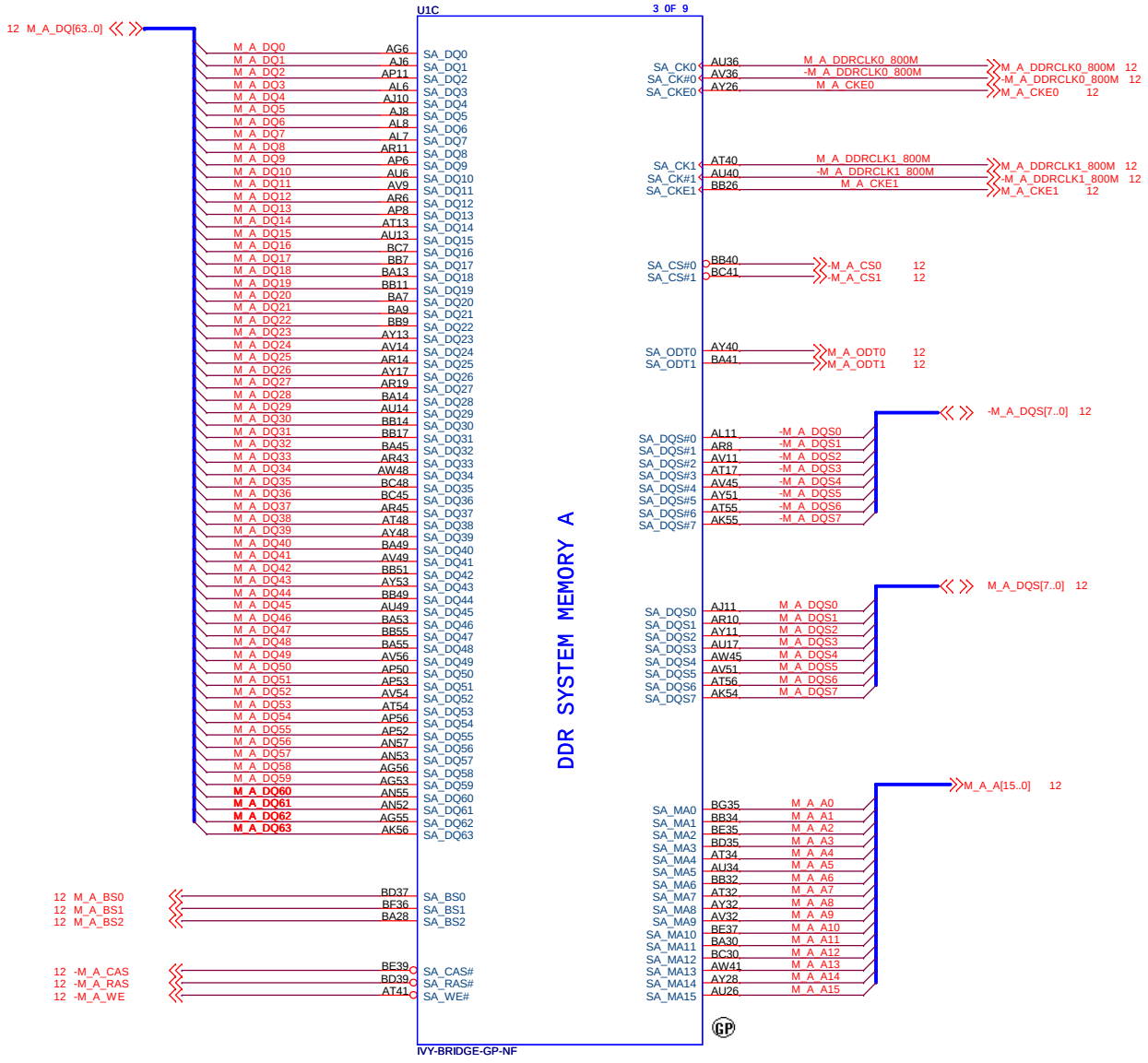
Taipei Hsien 221, Taiwan, R.O.C.

Title	CPU(2/8):CLK/MISC/JTAG	
Size A3	Document Number Dasher-2	Rev -1
Date: Friday, March 09, 2012	Sheet 4 of 101	



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21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
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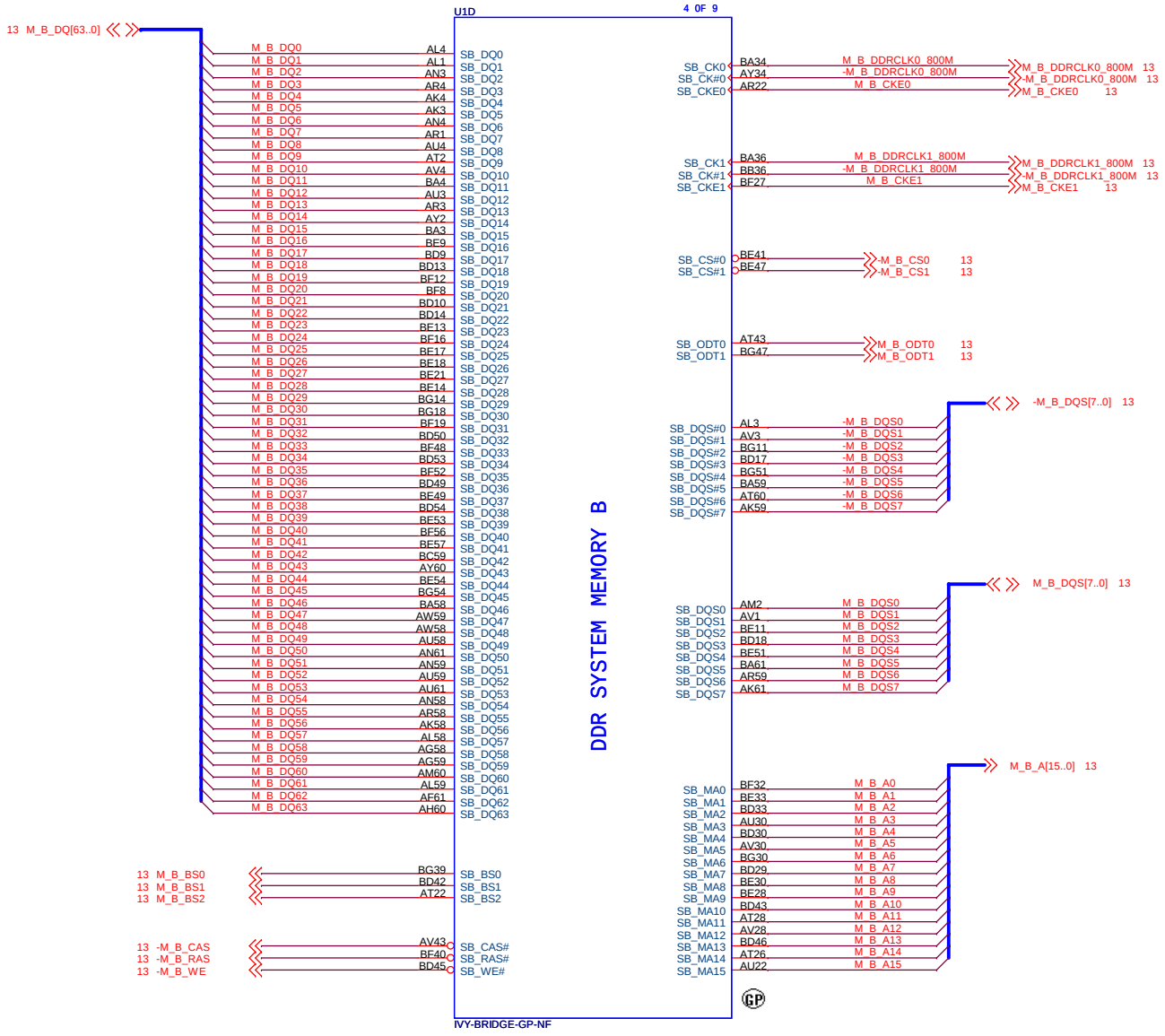
Title: **CPU(3/8):DDR3 Channel-A**

Size	Document Number	Rev
A3	Dasher-2	-1

Date: Friday, March 09, 2012 Sheet 5 of 101



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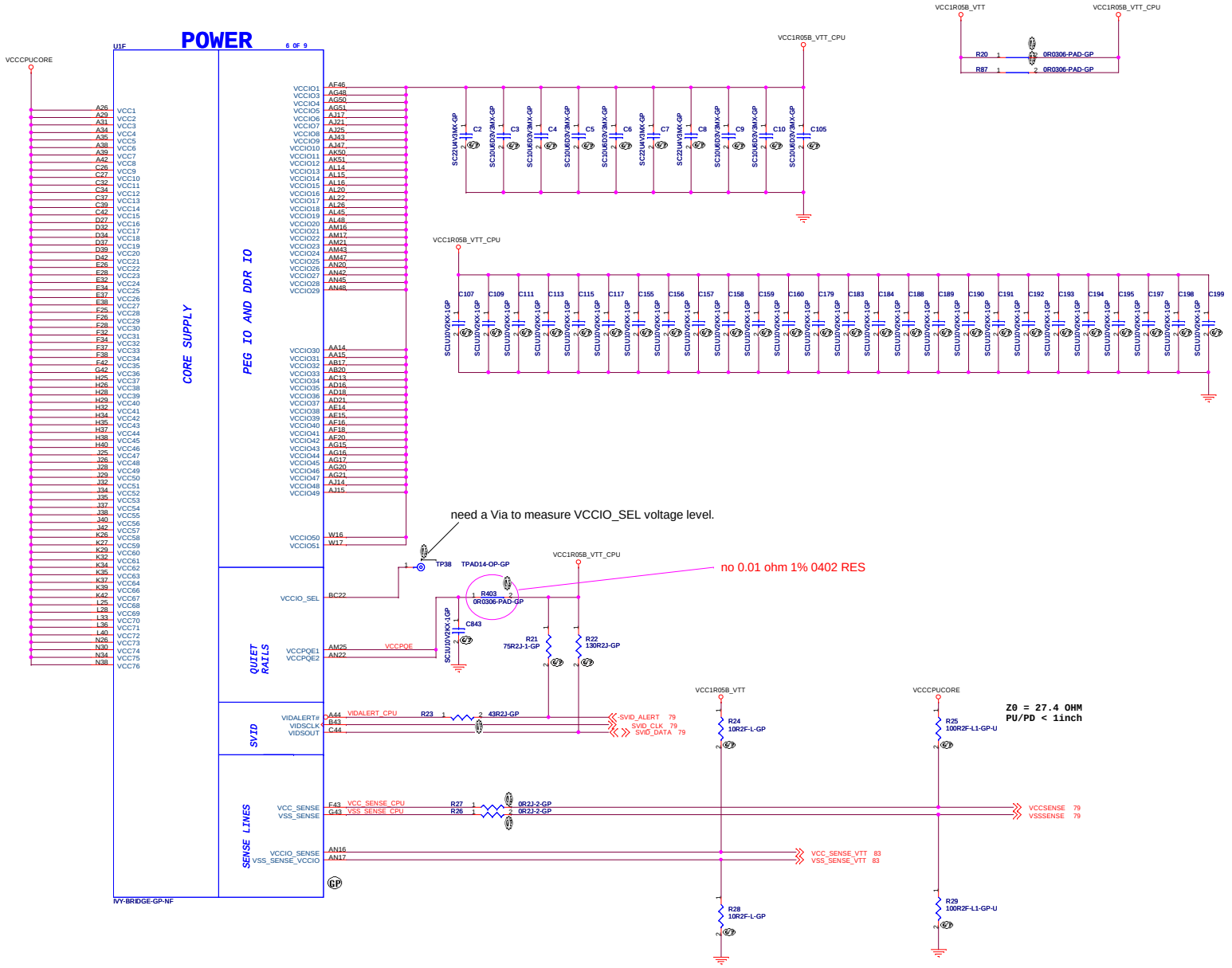
Title: **CPU(4/8):DDR3 Channel-B**

Size	Document Number	Rev
A3	Dasher-2	-1

Date: Friday, March 09, 2012 Sheet 6 of 101



<Core Design>

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Title: **CPU(5/8):Processor Power**

Size	Document Number	Rev
A2	Dasher-2	-1

Date: Friday, March 09, 2012 Sheet 7 of 101

POWER

7 OF 9

U1G

GRAPHICS

VCCGFXCORE_I

VAXG1 VAXG2 VAXG3 VAXG4 VAXG5 VAXG6 VAXG7 VAXG8 VAXG9 VAXG10 VAXG11 VAXG12 VAXG13 VAXG14 VAXG15 VAXG16 VAXG17 VAXG18 VAXG19 VAXG20 VAXG21 VAXG22 VAXG23 VAXG24 VAXG25 VAXG26 VAXG27 VAXG28 VAXG29 VAXG30 VAXG31 VAXG32 VAXG33 VAXG34 VAXG35 VAXG36 VAXG37 VAXG38 VAXG39 VAXG40 VAXG41 VAXG42 VAXG43 VAXG44 VAXG45 VAXG46 VAXG47 VAXG48 VAXG49 VAXG50 VAXG51 VAXG52 VAXG53 VAXG54 VAXG55 VAXG56

C933 C934 C20 C21 C22 C23 — SC22U4V3MX-GP / SC10U6D3V3MX-GP

C200 C201 C217 C218 C295 C368 C369 C370 C371 C844 C845 — SC1U10V2KX-1GP

As for placement, please follow
"Huron River Platform Power Delivery Design Guide Rev1.0"
(Intel DocNo.439028)

DDR3 - 1.5V RAILS

VCC1R5_VDDQ

VDDQ1 VDDQ2 VDDQ3 VDDQ4 VDDQ5 VDDQ6 VDDQ7 VDDQ8 VDDQ9 VDDQ10 VDDQ11 VDDQ12 VDDQ13 VDDQ14 VDDQ15 VDDQ16 VDDQ17 VDDQ18 VDDQ19 VDDQ20 VDDQ21 VDDQ22 VDDQ23 VDDQ24 VDDQ25 VDDQ26

AJ28 AJ33 AJ36 AJ40 AL30 AL34 AL38 AL42 AM33 AM36 AM40 AN30 AN34 AN38 AR26 AR28 AR30 AR32 AR34 AR36 AR40 AV41 AW26 BA40 BB28 BG33

VREF

SM_VREF — AY43

SA_DIMM_VREFDQ — BE7 — DDR3_VREF_DQ_SA_M3 12

SB_DIMM_VREFDQ — BG7 — DDR3_VREF_DQ_SB_M3 12

DY R668 1KR2F-3-GP

DY R669 1KR2F-3-GP

DY C24 DY C25 — SCD1U10V2KX-4GP / SC2D2U6D3V2MX-GP

NEED SC2D2U4V2MX-1GP

Place near CPU

VCC1R5_VDDQ

R41 1KR2F-3-GP

R42 1KR2F-3-GP

VCC1R5_VDDQ

Place under CPU

C26 C27 C28 C29 C30 C31 C32 C36 C35 C37 — SC1U10V2KX-1GP

As for placement, please follow
"Huron River Platform Power Delivery Design Guide Rev1.0"
(Intel DocNo.439028)

SENSE LINES

VAXG_SENSE — F45 — VAXG_SENSE_CPU

VSSAXG_SENSE — G45 — VSSAXG_SENSE_CPU

VCCGFXCORE_I

R30 100R2F-L1-GP-U

79 VCCGFX_SENSE_I — R31 0R2J-2-GP

79 VSSGFX_SENSE_I — R32 0R2J-2-GP

R33 100R2F-L1-GP-U

QUIET RAILS

VCCDQ1 VCCDQ2

AM28 AN26 — VCCDQ

VCC1R5_VDDQ

R90 0R0306-PAD-GP

C846 — SC1U10V2KX-1GP

1.8V RAIL

VCC1R8B

R34 0R0306-PAD-GP

VCC1R8B_CPU — BB3 BC1 BC4 — VCCPLL1 VCCPLL2 VCCPLL3

C33 C34 DY C103 DY C154 — SC1U10V2KX-1GP / SC2D2U6D3V2MX-GP

SENSE LINES

VDDQ_SENSE — BC43

VSS_SENSE_VDDQ — BA43

PN12 PWRNC

PN13 PWRNC

SA RAIL

VCCSA

L17 L21 N16 N20 N22 P17 P20 R16 R18 R21 U15 V16 V17 V18 V21 W20 — VCCSA1 VCCSA2 VCCSA3 VCCSA4 VCCSA5 VCCSA6 VCCSA7 VCCSA8 VCCSA9 VCCSA10 VCCSA11 VCCSA12 VCCSA13 VCCSA14 VCCSA15 VCCSA16

C834 C835 C836 C837 C838 — SC1U10V2KX-1GP

VCCSA_SENSE — U10 — VCCSA_SENSE 90

VCCSA VID lines

VCCSA_VID0 — D48 — VCCSA_VID0_CPU — R1318 0R2J-2-GP

VCCSA_VID1 — D49 — VCCSA_VID1_CPU — R36 0R2J-2-GP

VCC1R05B_VTT_CPU

DY R1317 10KR2J-3-GP

DY R35 10KR2J-3-GP

R1319 10KR2J-3-GP

R38 10KR2J-3-GP

VCCSA_SEL0 90

VCCSA_SEL1 90

IVY-BRIDGE-GP-NF

	VCCSA_SEL0	VCCSA_SEL1	VCCSA
Sandy Bridge	LOW	0 1	0.90V 0.80V
Ivy Bridge	High	0 1	0.725V 0.675V

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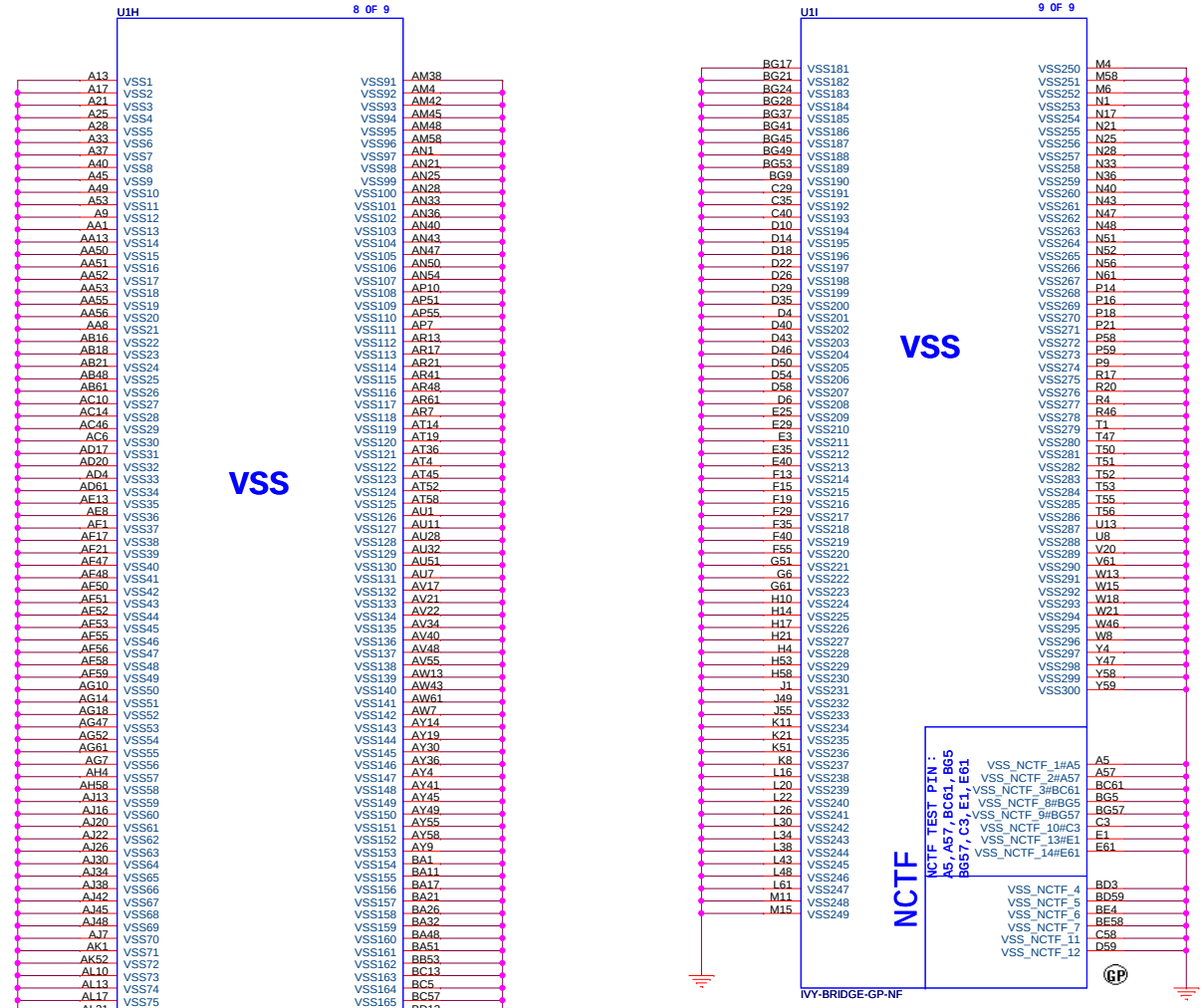
Title: CPU(6/8):Graphics Power

Size: A3 | Document Number: Dasher-2 | Rev: -1

Date: Friday, March 09, 2012 | Sheet 8 of 101



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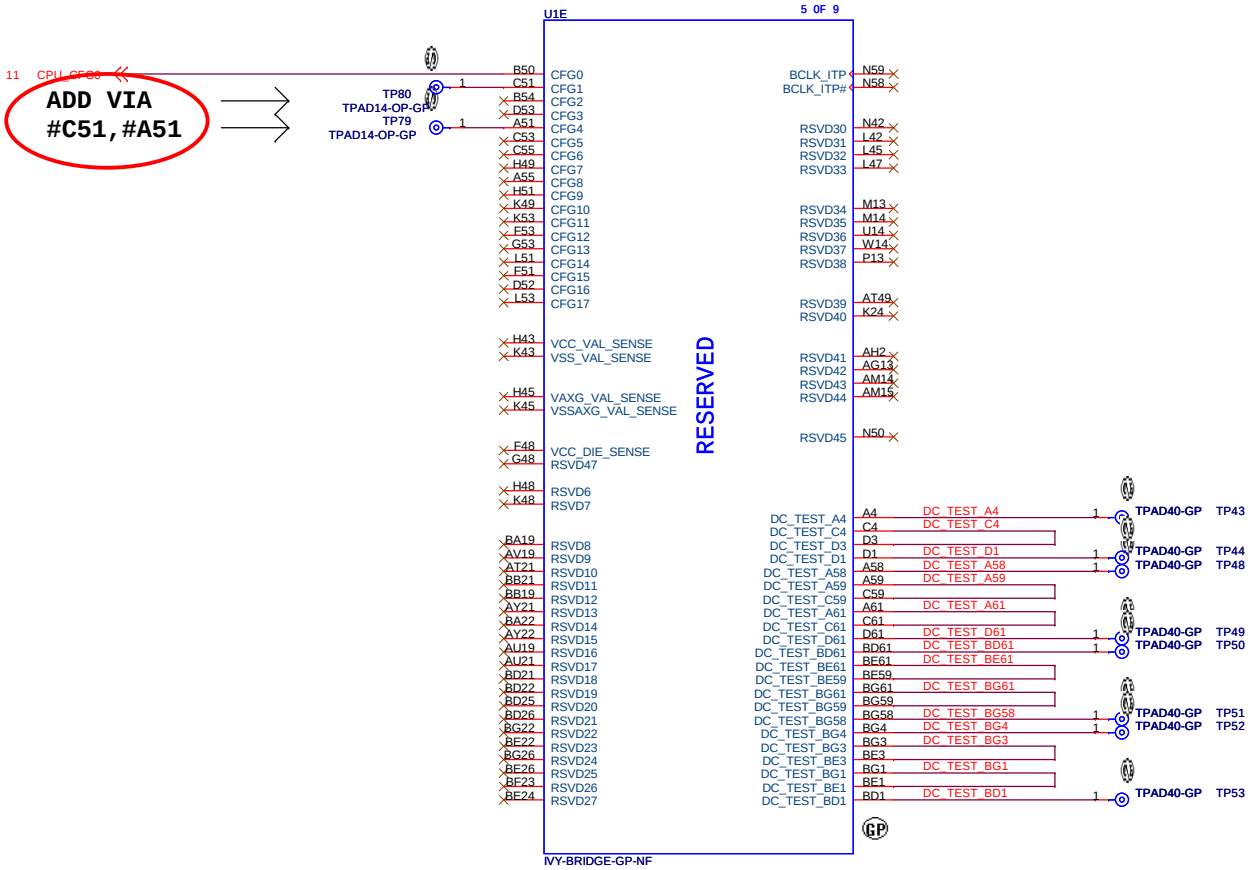
Taipei Hsien 221, Taiwan, R.O.C.

Title: CPU(7/8):GND

Size	Document Number	Rev
Custom	Dasher-2	-1

Date: Tuesday, February 21, 2012 Sheet 9 of 101



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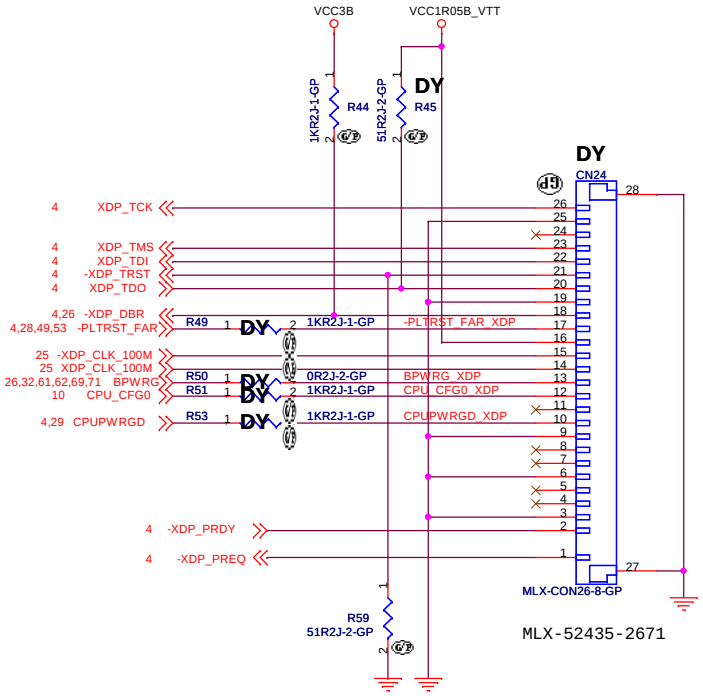
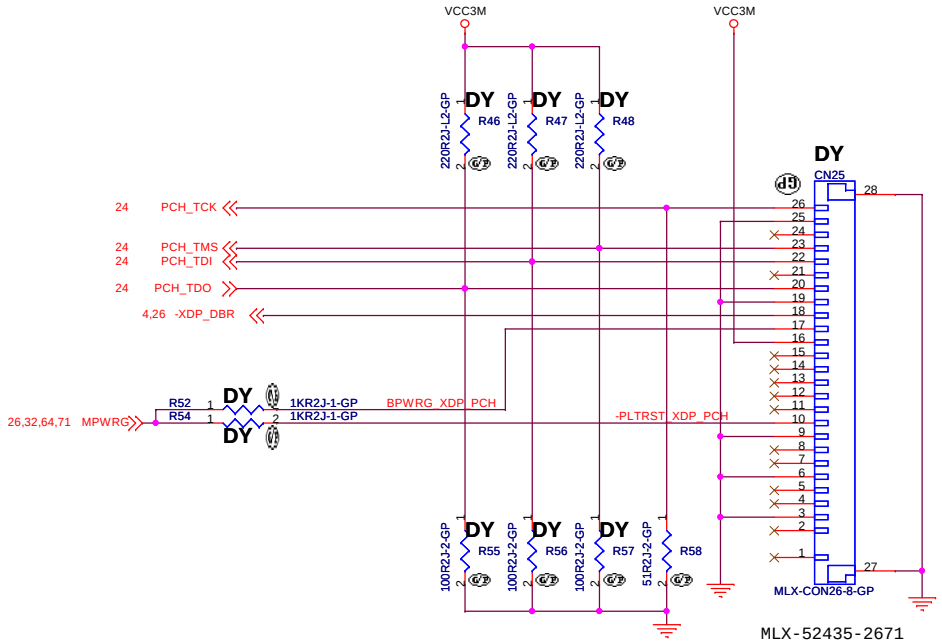
Title: **CPU(8/8):CFG/Reserved**

Size	Document Number	Rev
A3	Dasher-2	-1

Date: Friday, March 09, 2012 Sheet 10 of 101



DEBUG Interface for Processor.



DEBUG Interface for PCH.

XDP1 NOTE:"ASM" FOR PDV/SDV ONLY

		ENABLE	DISABLE
TDO	R45	ASM	DY
TRST#	R59	ASM	ASM
DBRST#	R44	ASM	ASM
RESET#	R49	ASM	DY
CFG0	R51	ASM	DY
PWRGD	R53	ASM	DY
BPWRG	R50	ASM	DY
	CN24	ASM	DY

FVT Logic

XDP2 NOTE:"ASM" FOR PDV/SDV ONLY

		ENABLE	DISABLE
TDO	R46	220	DY
	R55	100	DY
TMS	R48	220	DY
	R57	100	DY
TDI	R47	220	DY
	R56	100	DY
TCK	R58	51	51
MPWRG	R52	ASM	DY
	R54	ASM	DY
	CN25	ASM	DY

FVT Logic

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Title: ***XDP Connector***

Size	Document Number	Rev
A3	Dasher-2	-1

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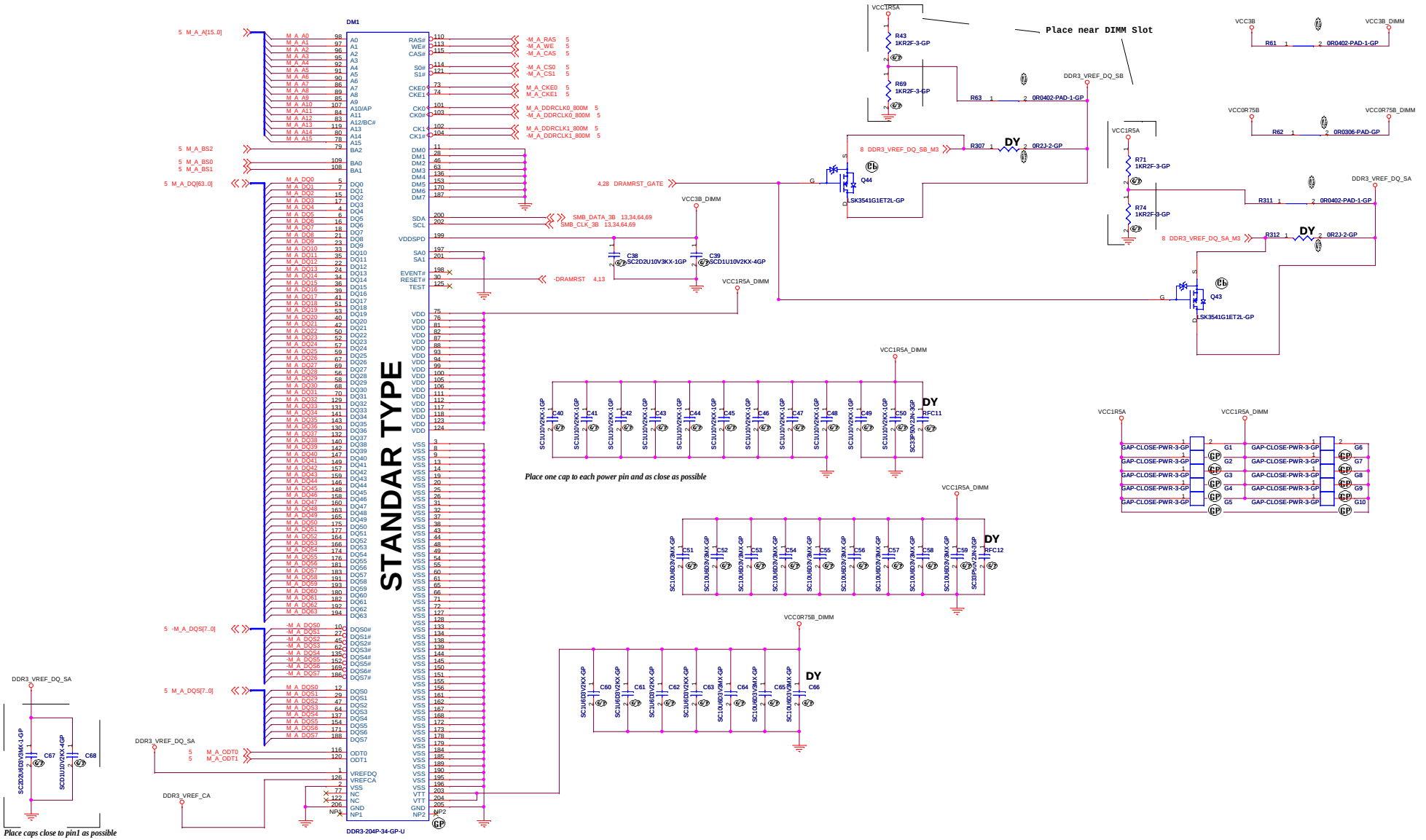

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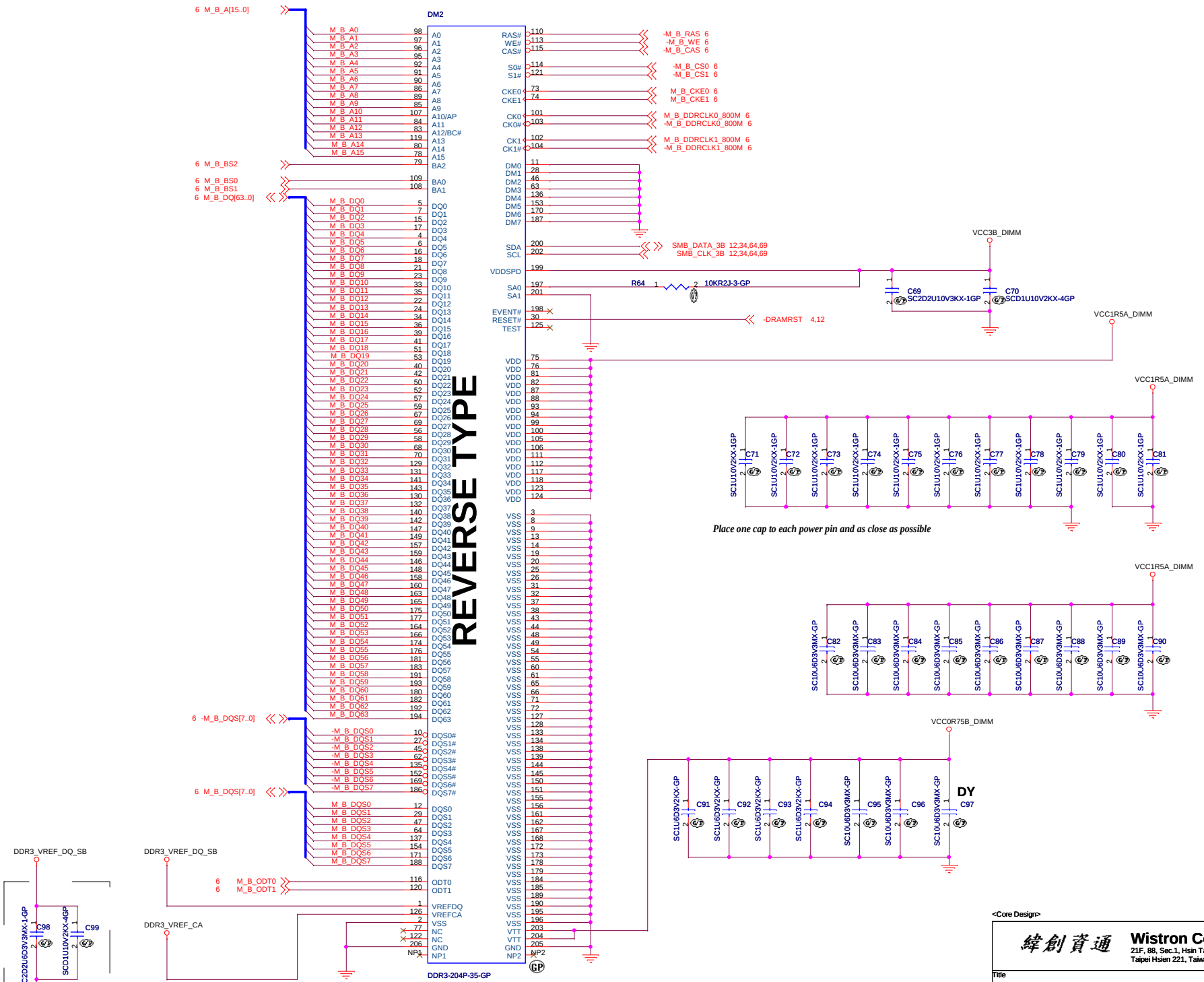
Title: **DDR3 SODIMM-A**

Size	Document Number	Rev
A2	Dasher-2	-1

Date: Friday, March 09, 2012 Sheet 12 of 101



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Title: DDR3 SODIMM-B

Size	Document Number	Rev
Custom	Dasher-2	-1

Date: Friday, March 09, 2012 Sheet 13 of 101

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Size	Document Number	Rev
A3	Dasher-2	-1

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Size	Document Number	Rev
A3	Dasher-2	-1

Date: Tuesday, February 21, 2012 Sheet 15 of 101

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Size	Document Number	Rev
A3	Dasher-2	-1

Date: Tuesday, February 21, 2012 Sheet 16 of 101

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Title: ***BLANK***

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A3	Dasher-2	-1

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B
L
A
N
K

<Core Design>

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Title: ***BLANK***

Size	Document Number	Rev
A3	**Dasher-2**	-1

Date: Tuesday, February 21, 2012 Sheet 18 of 101

B
L
A
N
K

<Core Design>

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Size	Document Number	Rev
A3	**Dasher-2**	-1

Date: Tuesday, February 21, 2012 Sheet 19 of 101

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Size	Document Number	Rev
A3	Dasher-2	-1

Date: Tuesday, February 21, 2012 Sheet 20 of 101

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Title: ***BLANK***

Size	Document Number	Rev
A3	Dasher-2	-1

Date: Tuesday, February 21, 2012 Sheet 21 of 101

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Size	Document Number	Rev
A3	Dasher-2	-1

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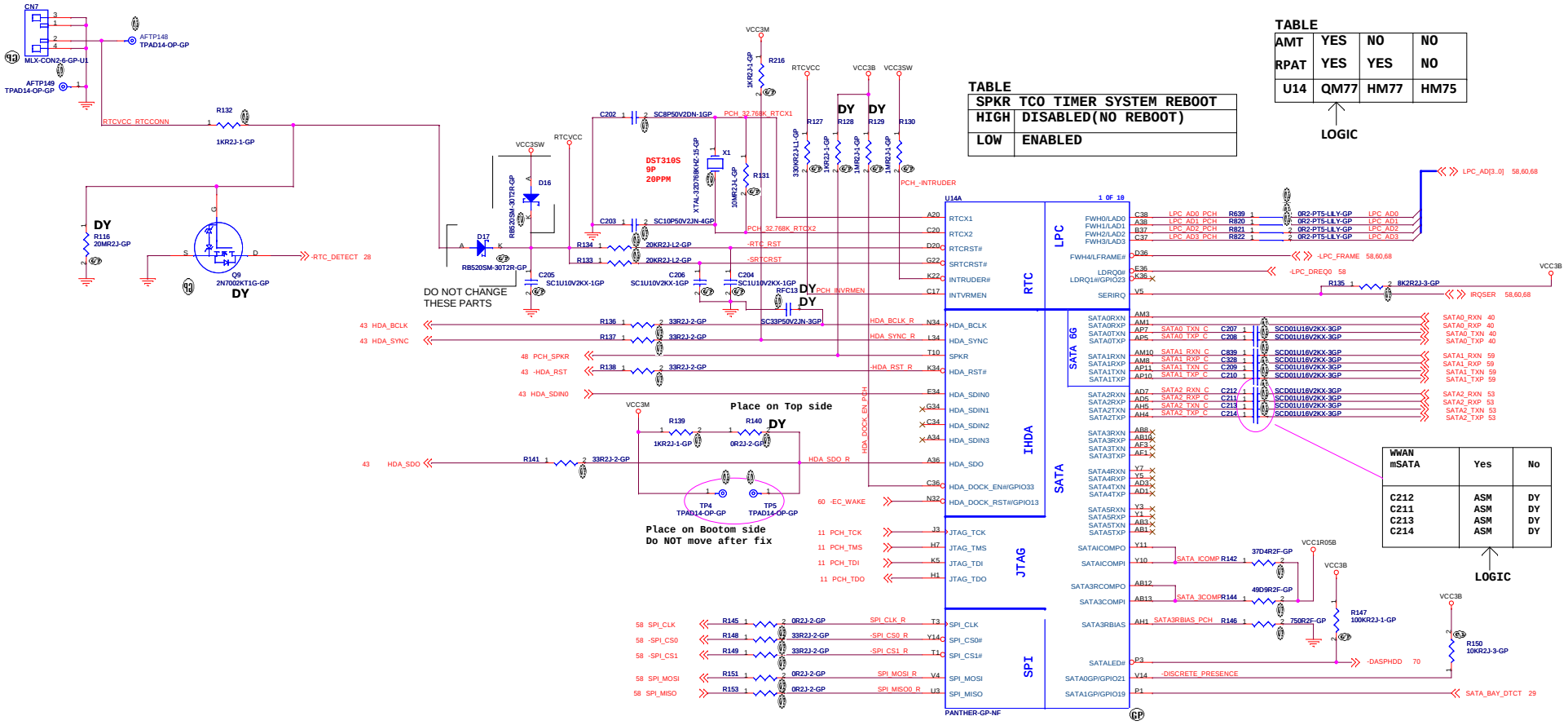
Date: Tuesday, February 21, 2012	Sheet 23 of 101	



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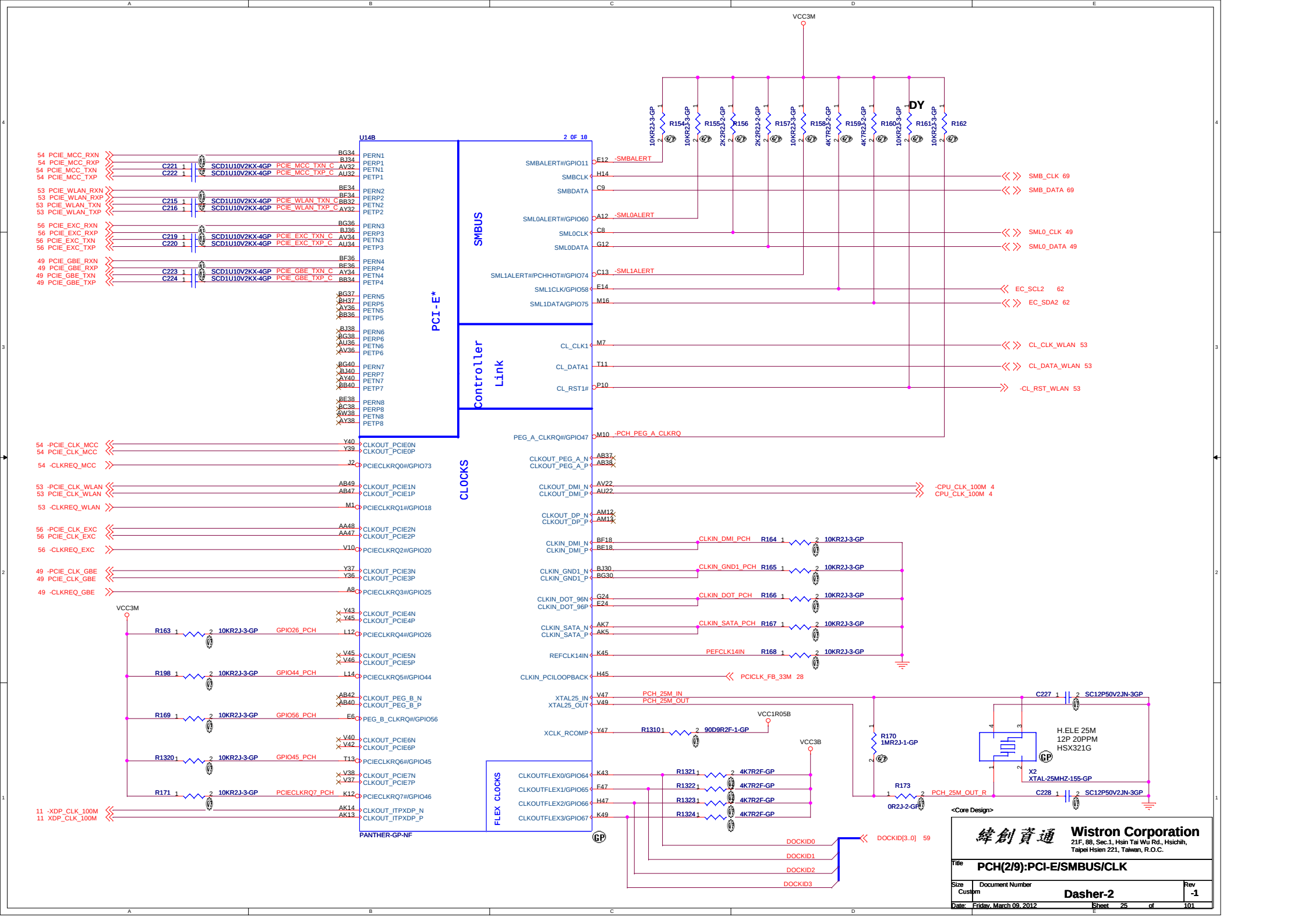
Title: PCH(1/9):HDA/JTAG/SPI/SATA

Size: A2 | Document Number: Dasher-2 | Rev: -1

Date: Friday, March 09, 2012 | Sheet 24 of 101


VCC3M
U14B
2 OF 10
PCI-E*
SMBUS
Controller Link
CLOCKS
FLEX CLOCKS
PANTHER-GP-NF
DY
SMBALERT#/GPIO11
SMBCLK
SMBDATA
SML0ALERT#/GPIO60
SML0CLK
SML0DATA
SML1ALERT#/PCHHOT#/GPIO74
SML1CLK/GPIO58
SML1DATA/GPIO75
CL_CLK1
CL_DATA1
CL_RST1#
PEG_A_CLKRQ#/GPIO47
CLKOUT_PEG_A_N
CLKOUT_PEG_A_P
CLKOUT_DMI_N
CLKOUT_DMI_P
CLKOUT_DP_N
CLKOUT_DP_P
CLKIN_DMI_N
CLKIN_DMI_P
CLKIN_GND1_N
CLKIN_GND1_P
CLKIN_DOT_96N
CLKIN_DOT_96P
CLKIN_SATA_N
CLKIN_SATA_P
REFCLK14IN
CLKIN_PCILOOPBACK
XTAL25_IN
XTAL25_OUT
XCLK_RCOMP
CLKOUTFLEX0/GPIO64
CLKOUTFLEX1/GPIO65
CLKOUTFLEX2/GPIO66
CLKOUTFLEX3/GPIO67
DOCKID0
DOCKID1
DOCKID2
DOCKID3
DOCKID[3..0] 59
-SMBALERT
SMB_CLK 69
SMB_DATA 69
-SML0ALERT
SML0_CLK 49
SML0_DATA 49
-SML1ALERT
EC_SCL2 62
EC_SDA2 62
CL_CLK_WLAN 53
CL_DATA_WLAN 53
-CL_RST_WLAN 53
-PCH_PEG_A_CLKRQ
-CPU_CLK_100M 4
CPU_CLK_100M 4
CLKIN_DMI_PCH
CLKIN_GND1_PCH
CLKIN_DOT_PCH
CLKIN_SATA_PCH
PEFCLK14IN
PCICLK_FB_33M 28
PCH_25M_IN
PCH_25M_OUT
PCH_25M_OUT_R
H.ELE 25M
12P 20PPM
HSX321G
X2
XTAL-25MHZ-155-GP
<Core Design>
54 PCIE_MCC_RXN
54 PCIE_MCC_RXP
54 PCIE_MCC_TXN
54 PCIE_MCC_TXP
C221
C222
SCD1U10V2KX-4GP PCIE_MCC_TXN_C
SCD1U10V2KX-4GP PCIE_MCC_TXP_C
53 PCIE_WLAN_RXN
53 PCIE_WLAN_RXP
53 PCIE_WLAN_TXN
53 PCIE_WLAN_TXP
C215
C216
SCD1U10V2KX-4GP PCIE_WLAN_TXN_C
SCD1U10V2KX-4GP PCIE_WLAN_TXP_C
56 PCIE_EXC_RXN
56 PCIE_EXC_RXP
56 PCIE_EXC_TXN
56 PCIE_EXC_TXP
C219
C220
SCD1U10V2KX-4GP PCIE_EXC_TXN_C
SCD1U10V2KX-4GP PCIE_EXC_TXP_C
49 PCIE_GBE_RXN
49 PCIE_GBE_RXP
49 PCIE_GBE_TXN
49 PCIE_GBE_TXP
C223
C224
SCD1U10V2KX-4GP PCIE_GBE_TXN_C
SCD1U10V2KX-4GP PCIE_GBE_TXP_C
54 -PCIE_CLK_MCC
54 PCIE_CLK_MCC
54 -CLKREQ_MCC
53 -PCIE_CLK_WLAN
53 PCIE_CLK_WLAN
53 -CLKREQ_WLAN
56 -PCIE_CLK_EXC
56 PCIE_CLK_EXC
56 -CLKREQ_EXC
49 -PCIE_CLK_GBE
49 PCIE_CLK_GBE
49 -CLKREQ_GBE
11 -XDP_CLK_100M
11 XDP_CLK_100M
R163
R198
R169
R1320
R171
10KR2J-3-GP
GPIO26_PCH
GPIO44_PCH
GPIO56_PCH
GPIO45_PCH
PCIECLKRQ7_PCH
R154
R155
R156
R157
R158
R159
R160
R161
R162
10KR2J-3-GP
2K2R2J-2-GP
4K7R2J-2-GP
R164
R165
R166
R167
R168
10KR2J-3-GP
R1310
90D9R2F-1-GP
VCC1R05B
VCC3B
R1321
R1322
R1323
R1324
4K7R2F-GP
R170
1MR2J-1-GP
R173
0R2J-2-GP
C227
C228
SC12P50V2JN-3GP
Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.
Title
PCH(2/9):PCI-E/SMBUS/CLK
Size
Custom
Document Number
Dasher-2
Rev
-1
Date: Friday, March 09, 2012
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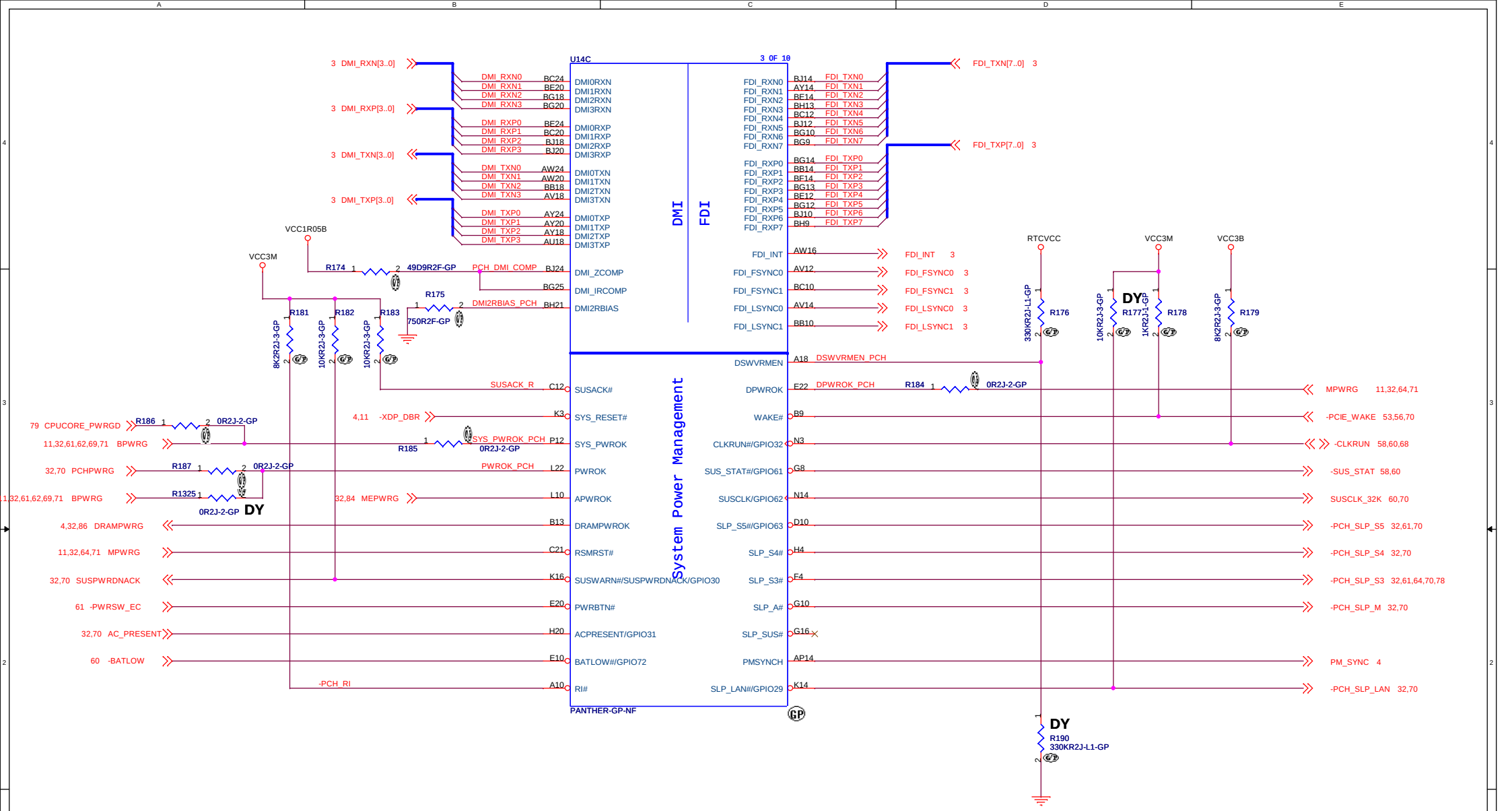

<Core Design>

緯創資通 Wistron Corporation
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Taipei Hsien 221, Taiwan, R.O.C.

Title	PCH(3/9):DMI/FDI/PM	
Size A3	Document Number Dasher-2	Rev -1
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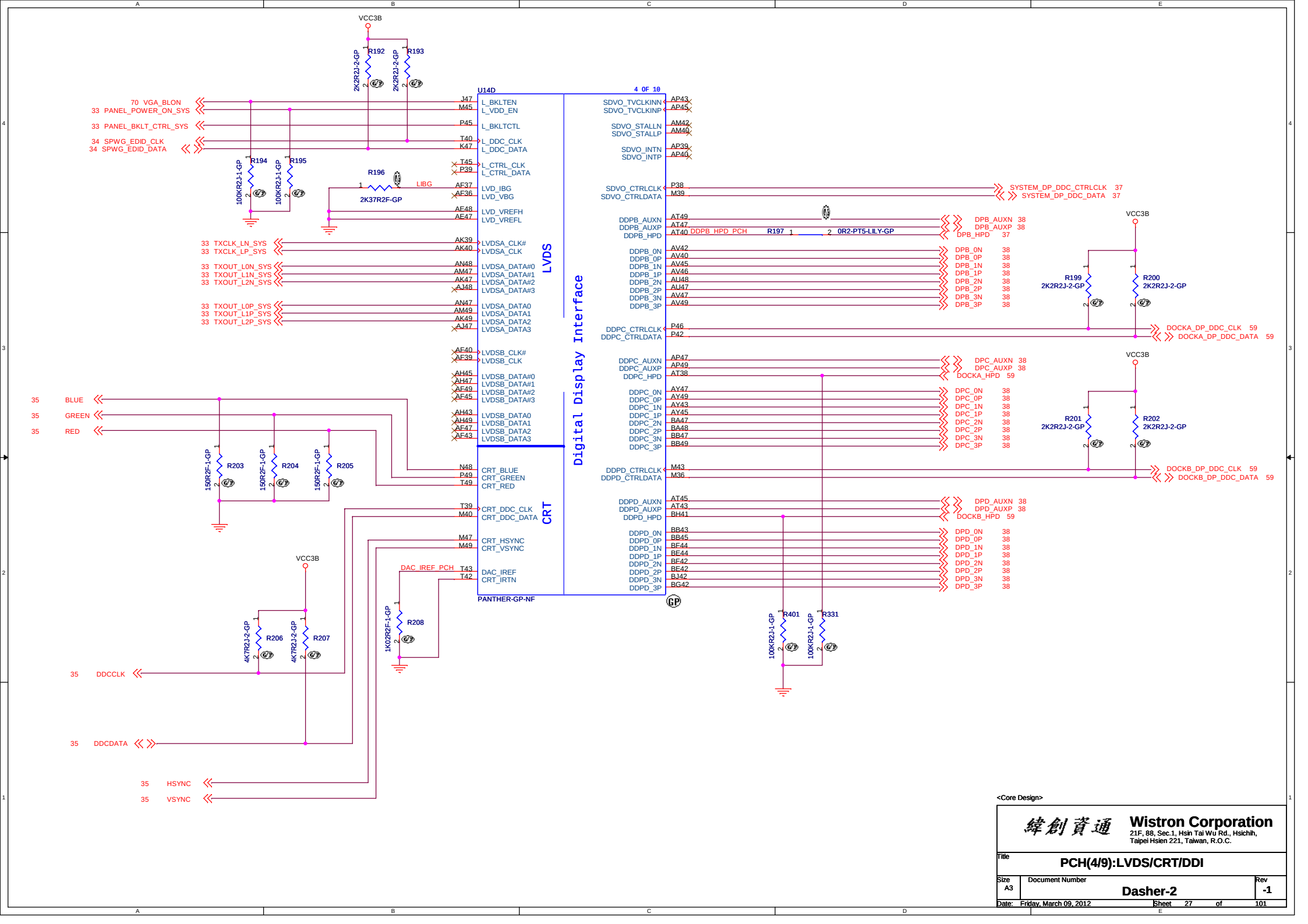
VCC3B
R192 2K2R2J-2-GP
R193 2K2R2J-2-GP
U14D
4 OF 10
70 VGA_BLON
33 PANEL_POWER_ON_SYS
33 PANEL_BKLT_CTRL_SYS
34 SPWG_EDID_CLK
34 SPWG_EDID_DATA
R194 100KR2J-1-GP
R195 100KR2J-1-GP
R196 2K37R2F-GP
LIBG
J47 L_BKLTEN
M45 L_VDD_EN
P45 L_BKLTCTL
T40 L_DDC_CLK
K47 L_DDC_DATA
T45 L_CTRL_CLK
P39 L_CTRL_DATA
AF37 LVD_IBG
AF36 LVD_VBG
AE48 LVD_VREFH
AE47 LVD_VREFL
LVDS
33 TXCLK_LN_SYS
33 TXCLK_LP_SYS
AK39 LVDSA_CLK#
AK40 LVDSA_CLK
33 TXOUT_L0N_SYS
33 TXOUT_L1N_SYS
33 TXOUT_L2N_SYS
AN48 LVDSA_DATA#0
AM47 LVDSA_DATA#1
AK47 LVDSA_DATA#2
AJ48 LVDSA_DATA#3
33 TXOUT_L0P_SYS
33 TXOUT_L1P_SYS
33 TXOUT_L2P_SYS
AN47 LVDSA_DATA0
AM49 LVDSA_DATA1
AK49 LVDSA_DATA2
AJ47 LVDSA_DATA3
AF40 LVDSB_CLK#
AF39 LVDSB_CLK
AH45 LVDSB_DATA#0
AH47 LVDSB_DATA#1
AF49 LVDSB_DATA#2
AF45 LVDSB_DATA#3
AH43 LVDSB_DATA0
AH49 LVDSB_DATA1
AF47 LVDSB_DATA2
AF43 LVDSB_DATA3
Digital Display Interface
35 BLUE
35 GREEN
35 RED
150R2F-1-GP R203
150R2F-1-GP R204
150R2F-1-GP R205
N48 CRT_BLUE
P49 CRT_GREEN
T49 CRT_RED
CRT
T39 CRT_DDC_CLK
M40 CRT_DDC_DATA
M47 CRT_HSYNC
M49 CRT_VSYNC
DAC_IREF_PCH
T43 DAC_IREF
T42 CRT_IRTN
PANTHER-GP-NF
SDVO_TVCLKINN AP43
SDVO_TVCLKINP AP45
SDVO_STALLN AM42
SDVO_STALLP AM40
SDVO_INTN AP39
SDVO_INTP AP40
SDVO_CTRLCLK P38
SDVO_CTRLDATA M39
SYSTEM_DP_DDC_CTRLCLK 37
SYSTEM_DP_DDC_DATA 37
DDPB_AUXN AT49
DDPB_AUXP AT47
DDPB_HPD AT40
DDPB_HPD_PCH
R197 0R2-PT5-LILY-GP
DPB_AUXN 38
DPB_AUXP 38
DPB_HPD 37
DDPB_0N AV42
DDPB_0P AV40
DDPB_1N AV45
DDPB_1P AV46
DDPB_2N AU48
DDPB_2P AU47
DDPB_3N AV47
DDPB_3P AV49
DPB_0N 38
DPB_0P 38
DPB_1N 38
DPB_1P 38
DPB_2N 38
DPB_2P 38
DPB_3N 38
DPB_3P 38
VCC3B
R199 2K2R2J-2-GP
R200 2K2R2J-2-GP
DDPC_CTRLCLK P46
DDPC_CTRLDATA P42
DOCKA_DP_DDC_CLK 59
DOCKA_DP_DDC_DATA 59
DDPC_AUXN AP47
DDPC_AUXP AP49
DDPC_HPD AT38
DPC_AUXN 38
DPC_AUXP 38
DOCKA_HPD 59
DDPC_0N AY47
DDPC_0P AY49
DDPC_1N AY43
DDPC_1P AY45
DDPC_2N BA47
DDPC_2P BA48
DDPC_3N BB47
DDPC_3P BB49
DPC_0N 38
DPC_0P 38
DPC_1N 38
DPC_1P 38
DPC_2N 38
DPC_2P 38
DPC_3N 38
DPC_3P 38
VCC3B
R201 2K2R2J-2-GP
R202 2K2R2J-2-GP
DDPD_CTRLCLK M43
DDPD_CTRLDATA M36
DOCKB_DP_DDC_CLK 59
DOCKB_DP_DDC_DATA 59
DDPD_AUXN AT45
DDPD_AUXP AT43
DDPD_HPD BH41
DPD_AUXN 38
DPD_AUXP 38
DOCKB_HPD 59
DDPD_0N BB43
DDPD_0P BB45
DDPD_1N BF44
DDPD_1P BE44
DDPD_2N BF42
DDPD_2P BE42
DDPD_3N BJ42
DDPD_3P BG42
DPD_0N 38
DPD_0P 38
DPD_1N 38
DPD_1P 38
DPD_2N 38
DPD_2P 38
DPD_3N 38
DPD_3P 38
R401 100KR2J-1-GP
R331 100KR2J-1-GP
VCC3B
R206 4K7R2J-2-GP
R207 4K7R2J-2-GP
R208 1K02R2F-1-GP
35 DDCCLK
35 DDCDATA
35 HSYNC
35 VSYNC
<Core Design>
緯創資通
Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.
Title
PCH(4/9):LVDS/CRT/DDI
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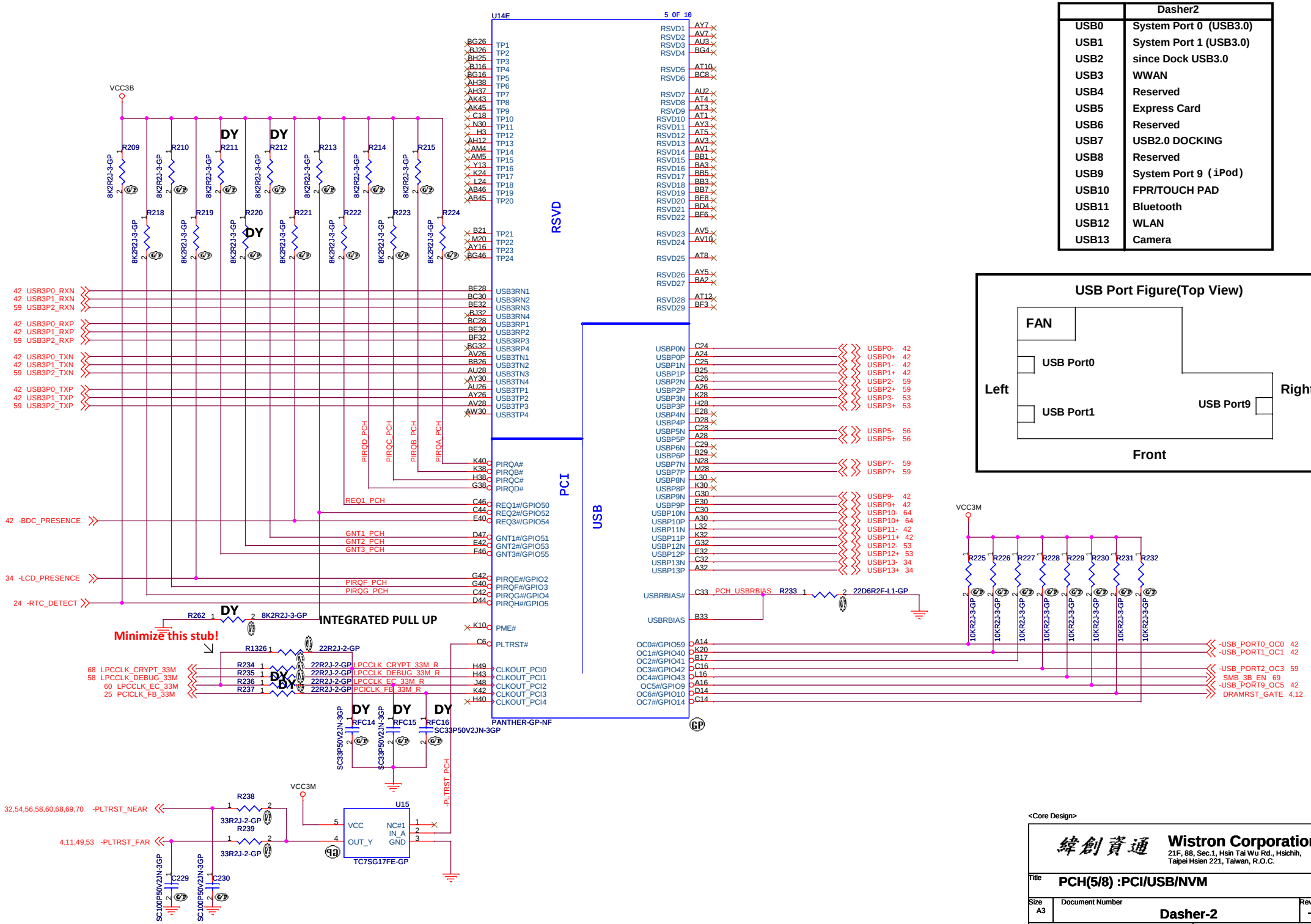
	Dasher2
USB0	System Port 0 (USB3.0)
USB1	System Port 1 (USB3.0)
USB2	since Dock USB3.0
USB3	WWAN
USB4	Reserved
USB5	Express Card
USB6	Reserved
USB7	USB2.0 DOCKING
USB8	Reserved
USB9	System Port 9 (iPod)
USB10	FPR/TOUCH PAD
USB11	Bluetooth
USB12	WLAN
USB13	Camera



<Core Design>

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Taipei Hsien 221, Taiwan, R.O.C.

Title	PCH(5/8) :PCI/USB/NVM	
Size A3	Document Number Dasher-2	Rev -1
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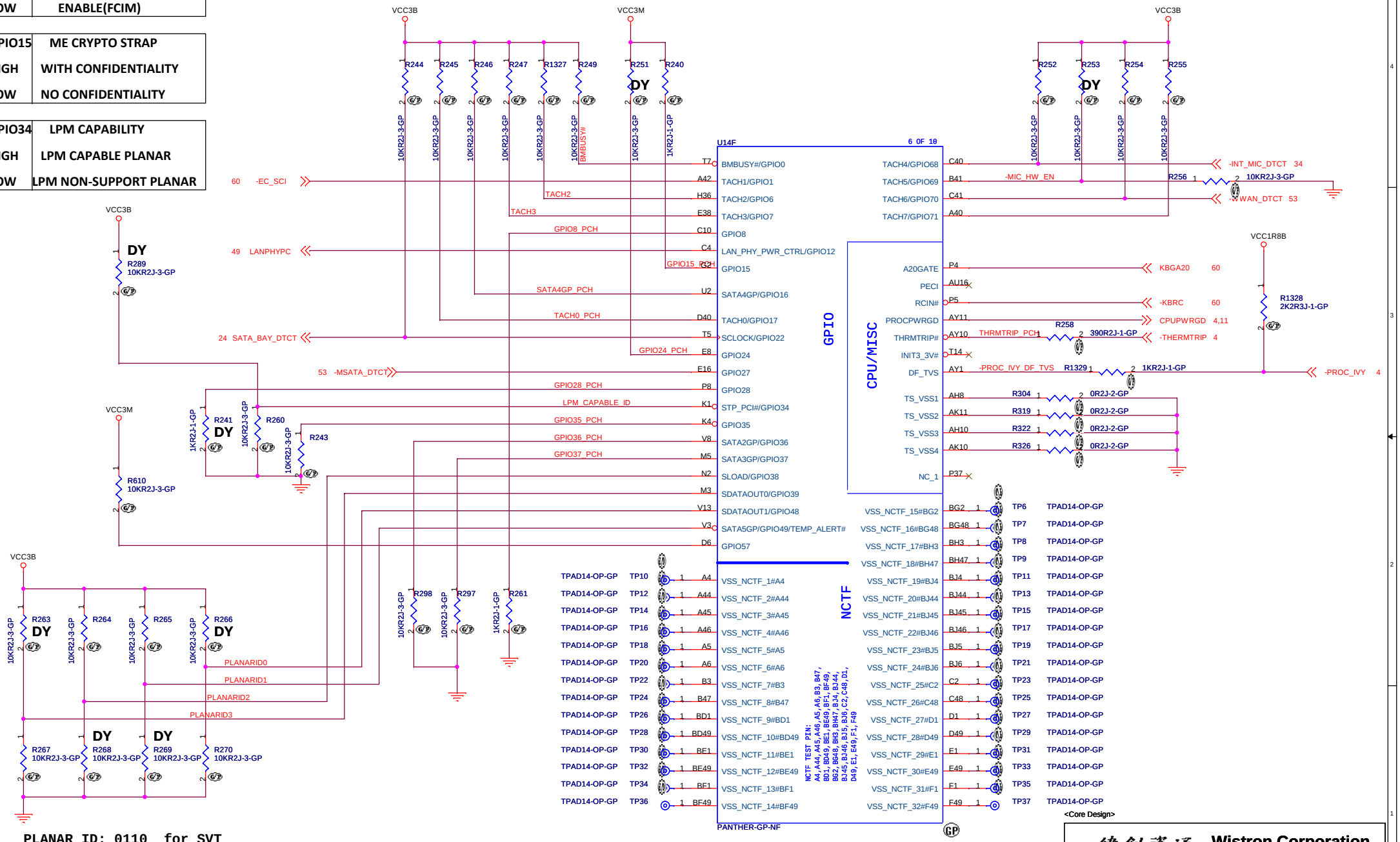
GPIO8	INTEGRATED CLOCKING
HIGH	DISABLE(BTM)
LOW	ENABLE(FCIM)

GPIO15	ME CRYPTO STRAP
HIGH	WITH CONFIDENTIALITY
LOW	NO CONFIDENTIALITY

GPIO34	LPM CAPABILITY
HIGH	LPM CAPABLE PLANAR
LOW	LPM NON-SUPPORT PLANAR



PLANAR ID: 0110 for SVT

<Core Design>

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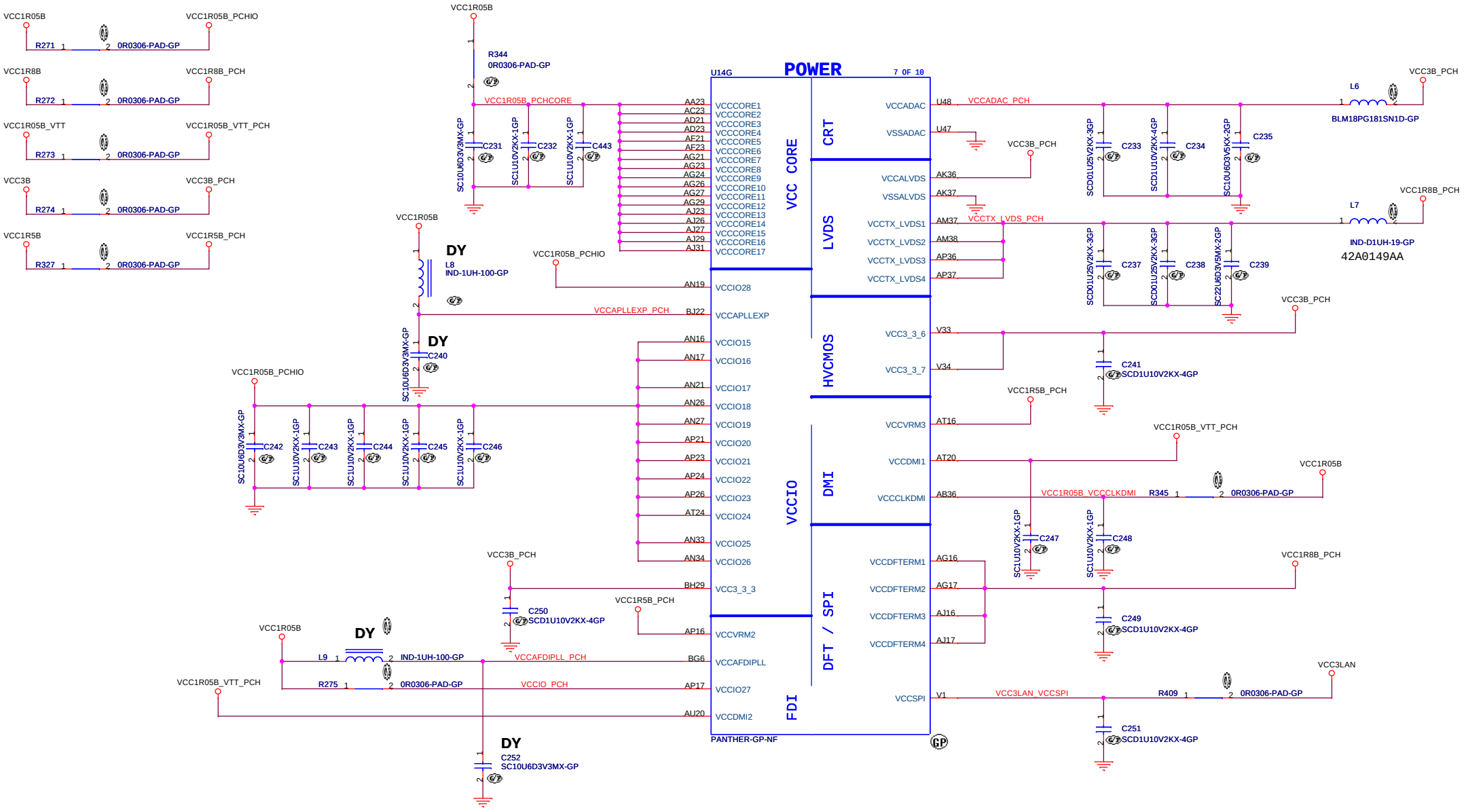
Taipei Hsien 221, Taiwan, R.O.C.

Title: PCH(6/9):GPIO/NCTF/RSVD

Size: A3 | Document Number: Dasher-2 | Rev: -1

Date: Friday, March 09, 2012 | Sheet 29 of 101



<Core Design>

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Taipei Hsien 221, Taiwan, R.O.C.

Title	PCH(7/9):Power	
Size A3	Document Number Dasher-2	Rev -1
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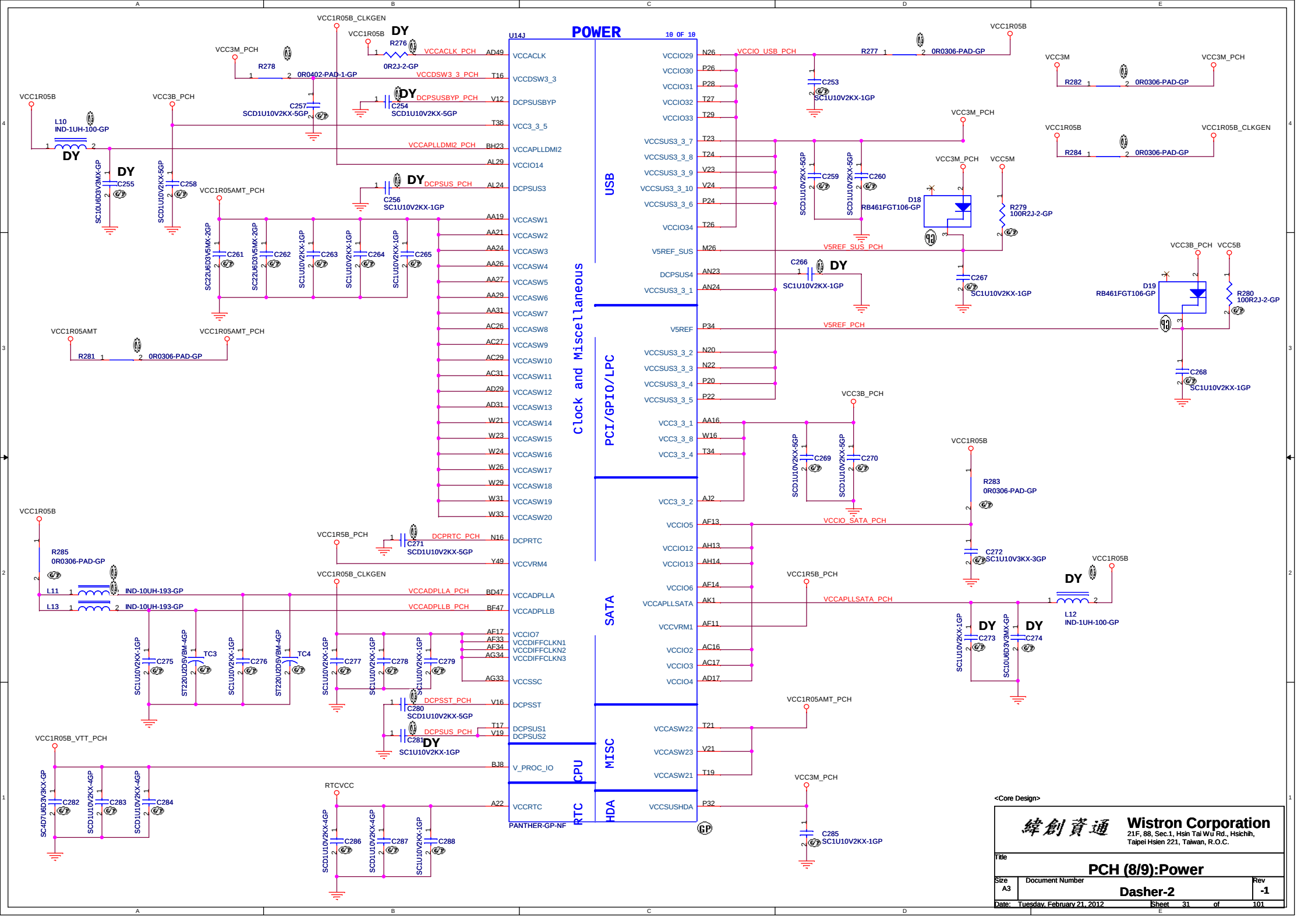
POWER
10 OF 10
U14J
Clock and Miscellaneous
USB
PCI/GPIO/LPC
SATA
MISC
CPU
RTC
HDA
PANTHER-GP-NF
VCCACLK
VCCDSW3_3
DCPSUSBYP
VCC3_3_5
VCCAPLLDMI2
VCCIO14
DCPSUS3
VCCASW1
VCCASW2
VCCASW3
VCCASW4
VCCASW5
VCCASW6
VCCASW7
VCCASW8
VCCASW9
VCCASW10
VCCASW11
VCCASW12
VCCASW13
VCCASW14
VCCASW15
VCCASW16
VCCASW17
VCCASW18
VCCASW19
VCCASW20
DCPRTC
VCCVRM4
VCCADPLLA
VCCADPLLB
VCCIO7
VCCDIFFCLKN1
VCCDIFFCLKN2
VCCDIFFCLKN3
VCCSSC
DCPSST
DCPSUS1
DCPSUS2
V_PROC_IO
VCCRTC
VCCIO29
VCCIO30
VCCIO31
VCCIO32
VCCIO33
VCCSUS3_3_7
VCCSUS3_3_8
VCCSUS3_3_9
VCCSUS3_3_10
VCCSUS3_3_6
VCCIO34
V5REF_SUS
DCPSUS4
VCCSUS3_3_1
V5REF
VCCSUS3_3_2
VCCSUS3_3_3
VCCSUS3_3_4
VCCSUS3_3_5
VCC3_3_1
VCC3_3_8
VCC3_3_4
VCC3_3_2
VCCIO5
VCCIO12
VCCIO13
VCCIO6
VCCAPLLSATA
VCCVRM1
VCCIO2
VCCIO3
VCCIO4
VCCASW22
VCCASW23
VCCASW21
VCCSUSHDA
VCCIO_USB_PCH
V5REF_SUS_PCH
V5REF_PCH
VCCIO_SATA_PCH
VCCAPLLSATA_PCH
VCCACLK_PCH
VCCDSW3_3_PCH
DCPSUSBYP_PCH
DCPSUS_PCH
DCPRTC_PCH
DCPSST_PCH
VCCADPLLA_PCH
VCCADPLLB_PCH
VCC1R05B
VCC1R05B_CLKGEN
VCC3M_PCH
VCC3B_PCH
VCC1R05AMT
VCC1R05AMT_PCH
VCC1R5B_PCH
VCC1R05B_VTT_PCH
RTCVCC
VCC5M
VCC5B
R276 0R2J-2-GP
R278 0R0402-PAD-1-GP
R277 0R0306-PAD-GP
R279 100R2J-2-GP
R280 100R2J-2-GP
R281 0R0306-PAD-GP
R282 0R0306-PAD-GP
R283 0R0306-PAD-GP
R284 0R0306-PAD-GP
R285 0R0306-PAD-GP
L10 IND-1UH-100-GP
L11 IND-10UH-193-GP
L13 IND-10UH-193-GP
L12 IND-1UH-100-GP
D18 RB461FGT106-GP
D19 RB461FGT106-GP
DY
<Core Design>
Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.
Title
PCH (8/9):Power
Size A3
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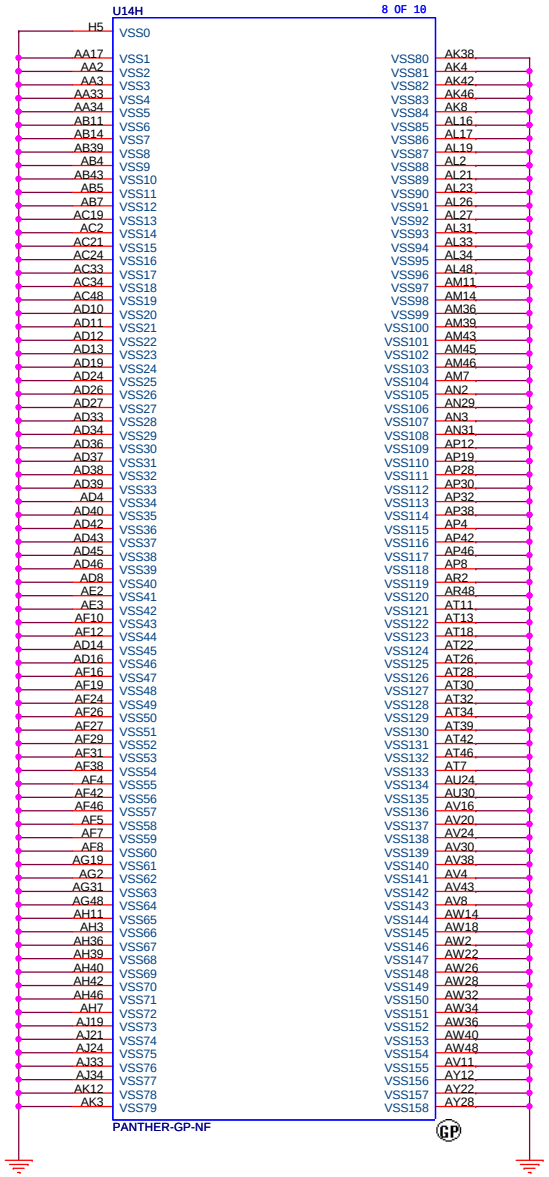
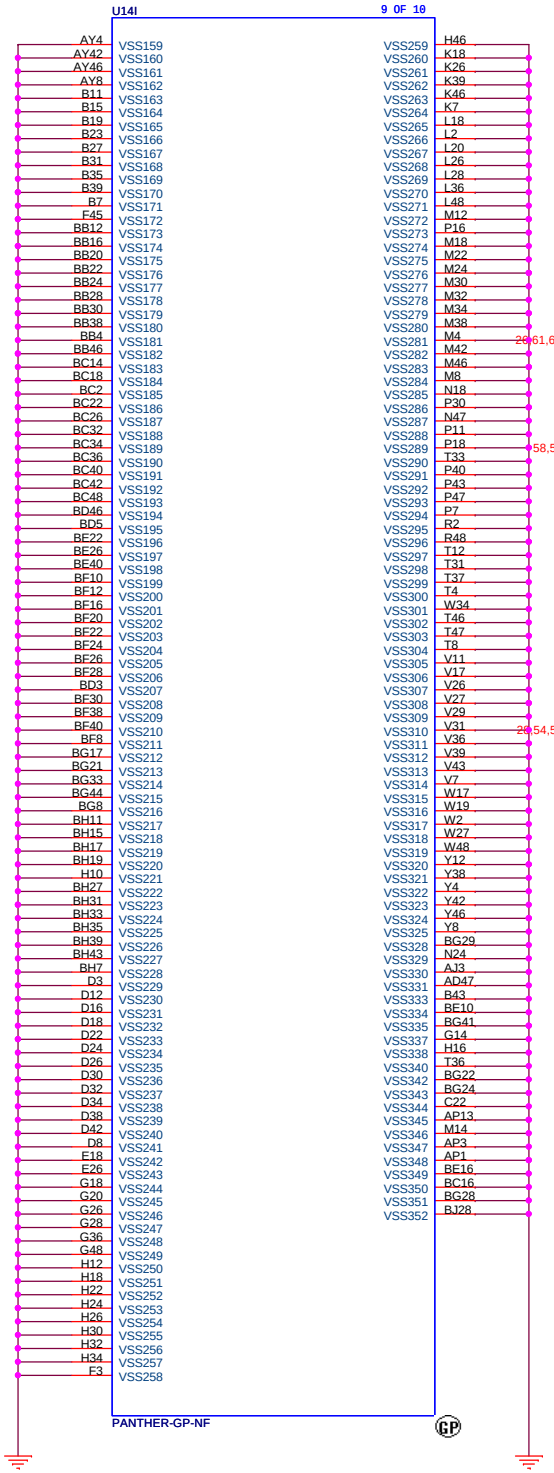
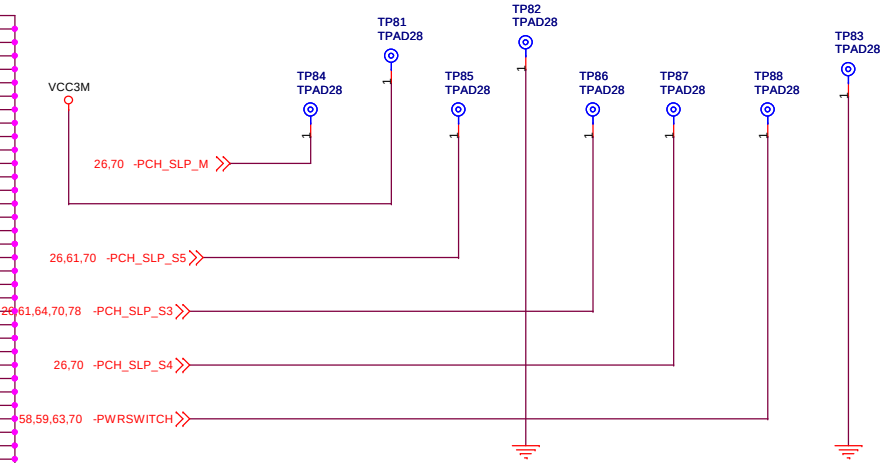
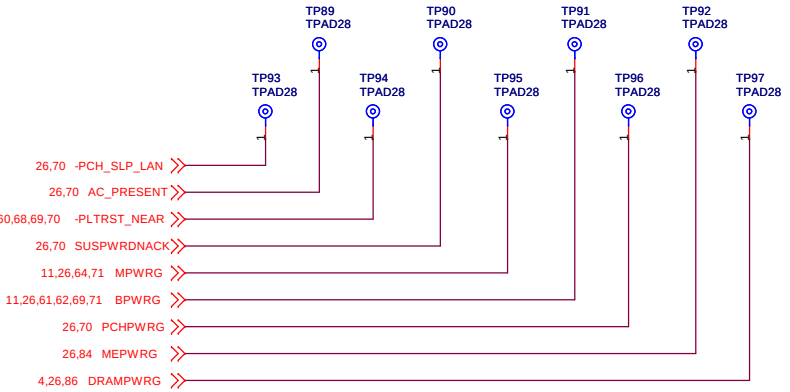
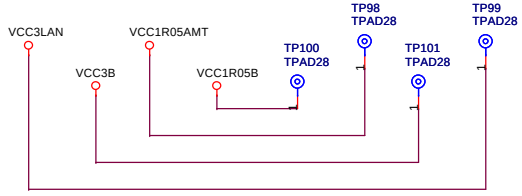
TEST PAD FOR METS/APS











<Core Design>

緯創資通 **Wistron Corporation**
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title: **PCH (9/9):GND**

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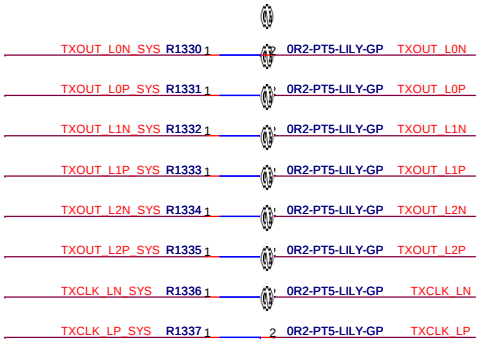
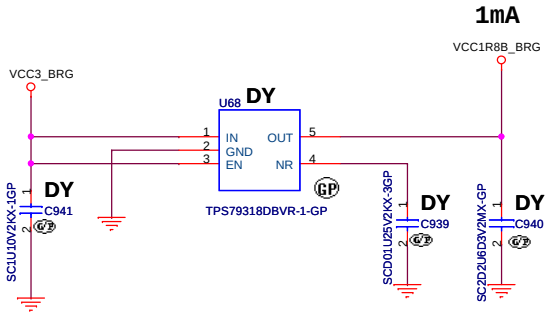
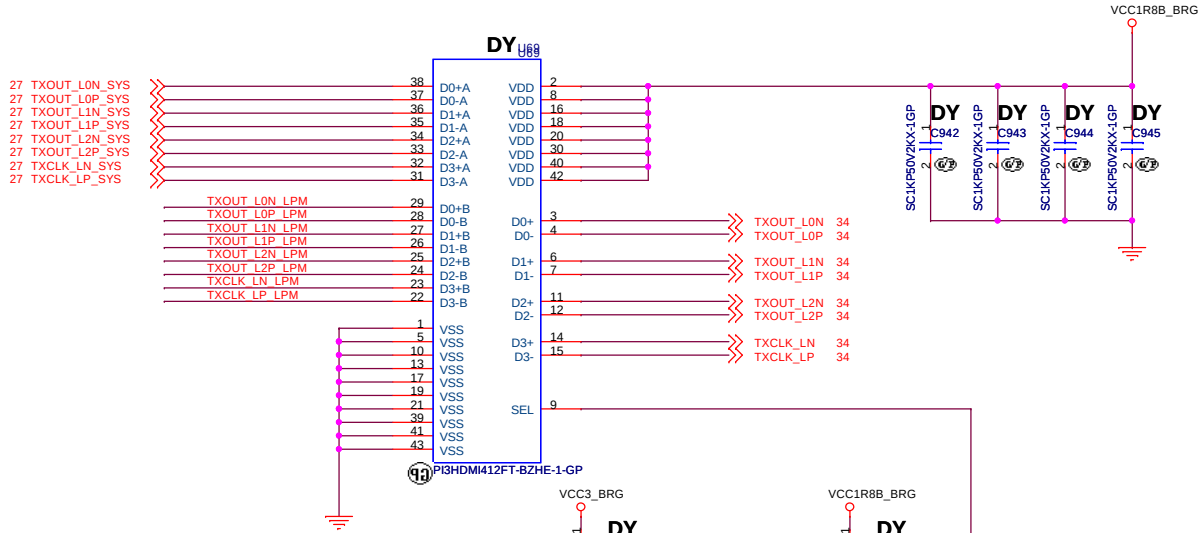
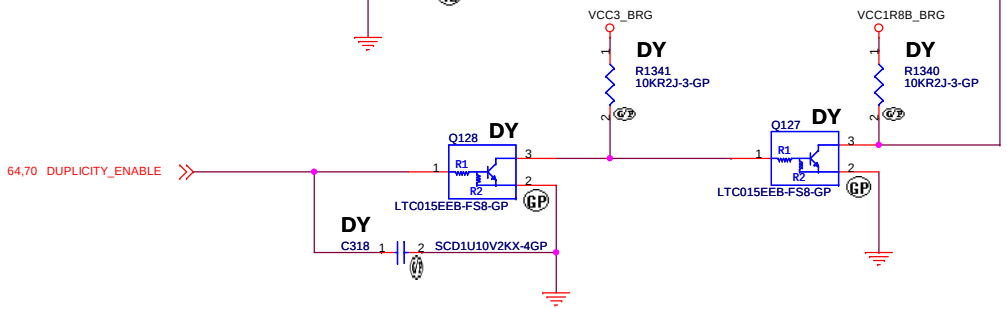
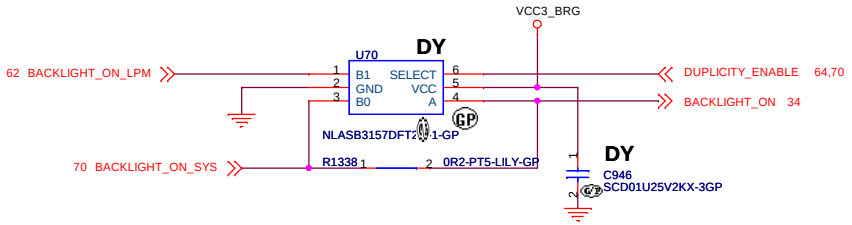
supplier	Vendor P/N	Wistron P/N
Pericom	PIHDMI412-BZHE	71.03412.B0G
TI	TS3DV421RUAR	71.03421.003

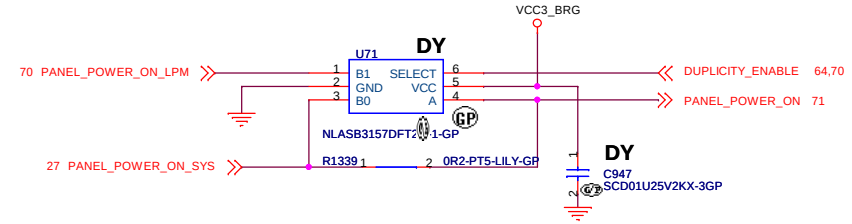


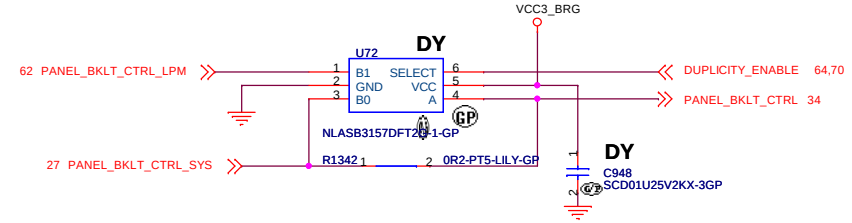


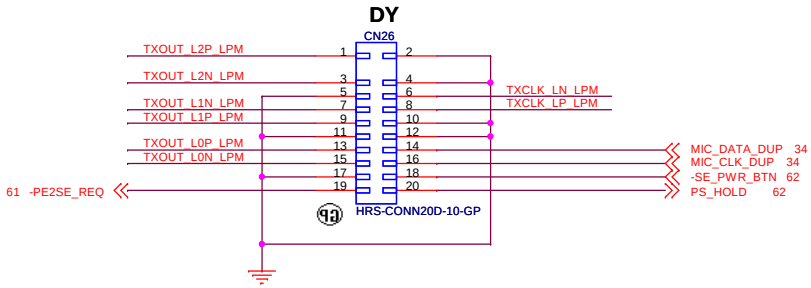












<Core Design>

Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title: LPM LVDS SW/Connector

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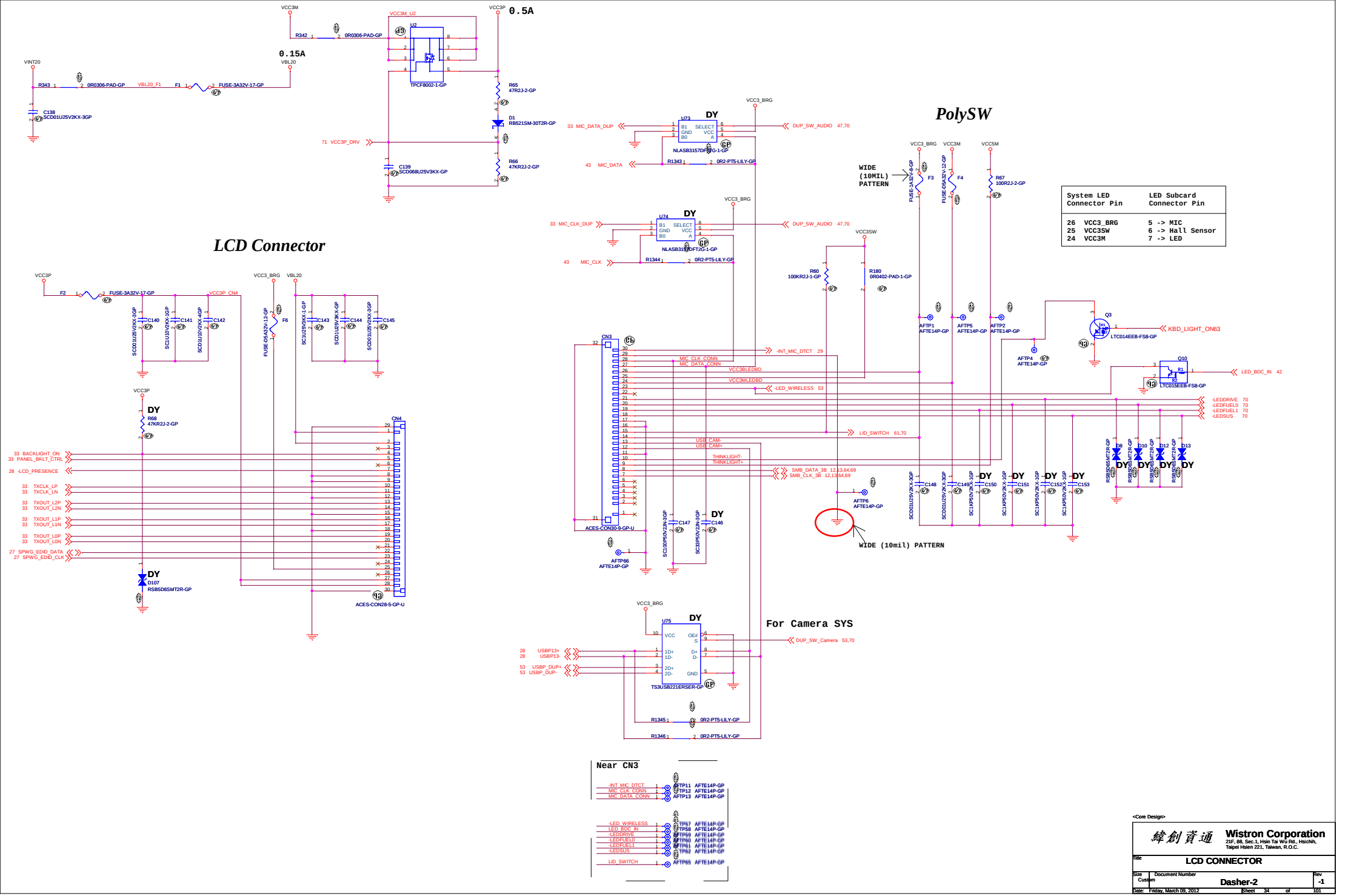
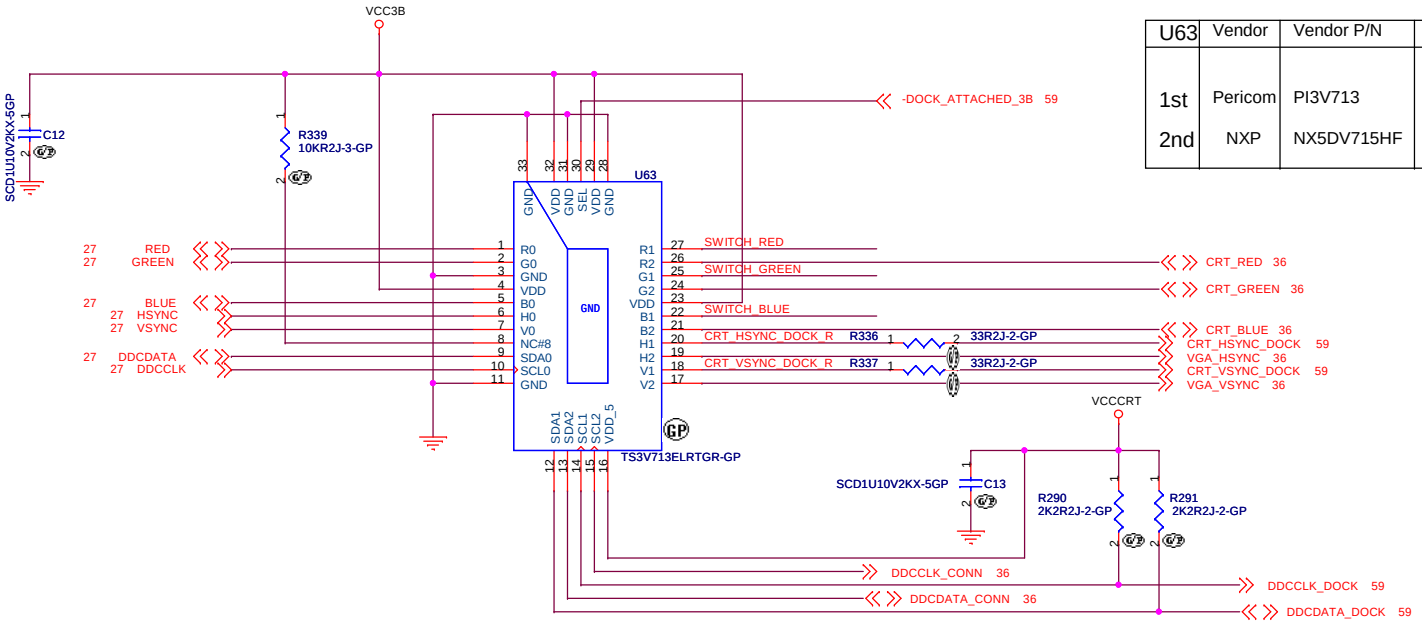
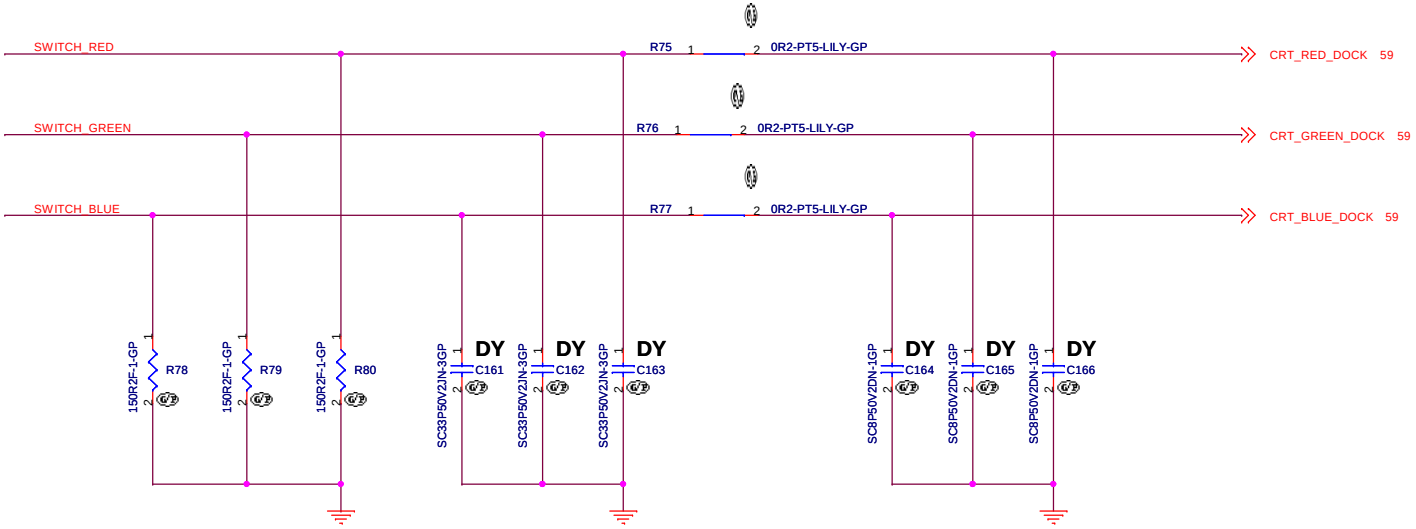

LCD Connector
PolySW
For Camera SYS
Near CN3
WIDE (10MIL) PATTERN
WIDE (10mil) PATTERN
System LED Connector Pin
LED Subcard Connector Pin
26 VCC3_BRG
25 VCC3SW
24 VCC3M
5 -> MIC
6 -> Hall Sensor
7 -> LED
0.5A
0.15A
VCC3M
VCC3P
VCC3_BRG
VCC3SW
VCC5M
VBL20
VINT20
CN3
CN4
U2
U73
U74
U75
Q3
Q10
D1
D107
DY
TPCF8002-1-GP
NLASB3157DFT2G-1-GP
ACES-CON30-9-GP-U
ACES-CON28-5-GP-U
TS3USB221ERSER-GP
LTC014EEB-FS8-GP
LTC015EEB-FS8-GP
RSB5D6SMT2R-GP
SCD01U25V2KX-3GP
SC1U10V2KX-1GP
SC1U25V3KX-1GP
SCD1U25V3KX-3GP
SCD01U25V2KX-3GP
SC100P50V2JN-3GP
SC38P50V2JN-3GP
SC1KP50V2KX-1GP
SC1KP50V2KX-3GP
FUSE-3A32V-17-GP
FUSE-05A32V-12-GP
FUSE-1A32V-9-GP
AFTE14P-GP
AFTP1
AFTP2
AFTP4
AFTP5
AFTP6
AFTP66
AFTP11
AFTP12
AFTP13
AFTP57
AFTP58
AFTP59
AFTP60
AFTP61
AFTP62
AFTP65
0R0306-PAD-GP
0R2-PT5-LILY-GP
47KR2J-2-GP
100KR2J-1-GP
0R0402-PAD-1-GP
SCD068U25V3KX-GP
SCD001U25V2KX-3GP
RB521SM-30T2R-GP
VBL20_F1
F1
F2
F3
F4
F6
R342
R343
R65
R66
R67
R68
R1343
R1344
R1345
R1346
R60
R180
C138
C139
C140
C141
C142
C143
C144
C145
C146
C147
C148
C149
C150
C151
C152
C153
D8
D10
D12
D13
33 MIC_DATA_DUP
43 MIC_DATA
33 MIC_CLK_DUP
43 MIC_CLK
71 VCC3P_DRV
DUP_SW_AUDIO 47,70
DUP_SW_Camera 53,70
-INT_MIC_DTCT 29
MIC_CLK_CONN
MIC_DATA_CONN
VCC3BLEDBD
VCC3MLEDBD
-LED_WIRELESS 53
LID_SWITCH 61,70
USB_CAM-
USB_CAM+
THINKLIGHT-
THINKLIGHT+
SMB_DATA_3B 12,13,64,69
SMB_CLK_3B 12,13,64,69
KBD_LIGHT_ON63
LED_BDC_IN 42
-LEDDRIVE 70
-LEDFUEL0 70
-LEDFUEL1 70
-LEDSUS 70
33 BACKLIGHT_ON
33 PANEL_BKLT_CTRL
28 -LCD_PRESENCE
33 TXCLK_LP
33 TXCLK_LN
33 TXOUT_L2P
33 TXOUT_L2N
33 TXOUT_L1P
33 TXOUT_L1N
33 TXOUT_L0P
33 TXOUT_L0N
27 SPWG_EDID_DATA
27 SPWG_EDID_CLK
28 USBP13+
28 USBP13-
53 USBP_DUP+
53 USBP_DUP-
<Core Design>
Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.
Title
LCD CONNECTOR
Size: Custom
Document Number
Dasher-2
Rev
-1
Date: Friday, March 09, 2012
Sheet 34 of 101



U63	Vendor	Vendor P/N	Wistron P/N
1st	Pericom	PI3V713	73.3V713.A0I
2nd	NXP	NX5DV715HF	73.05715.003



<Core Design>

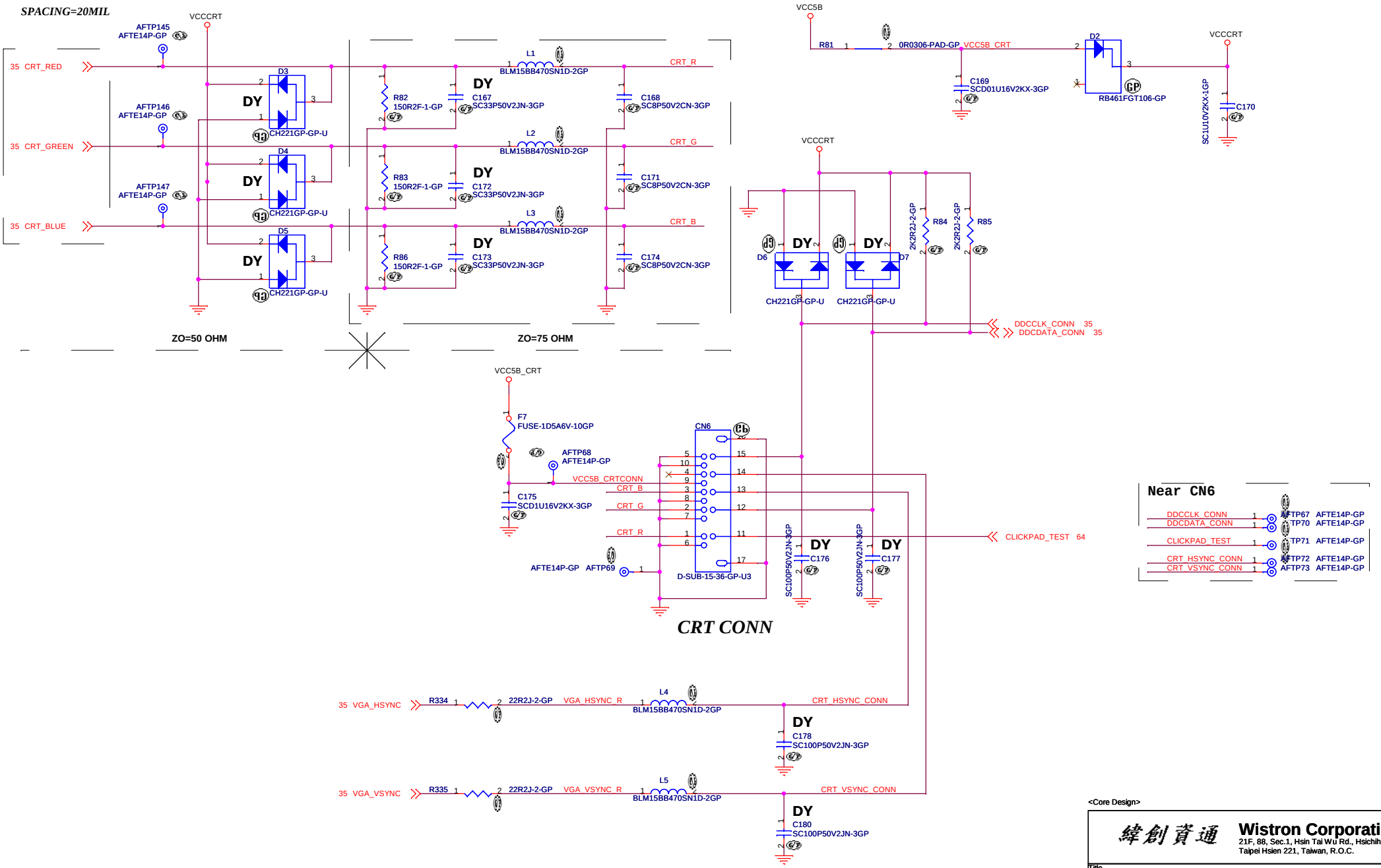
Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title: CRT SELECTOR

Size: A3 | Document Number: Dasher-2 | Rev: -1

Date: Friday, March 09, 2012 | Sheet 35 of 101

GND GUARDING
EACH SIGNAL WIDTH DEPENDS ON ZO(TRACE IMPEDANCE)
SPACING=20MIL



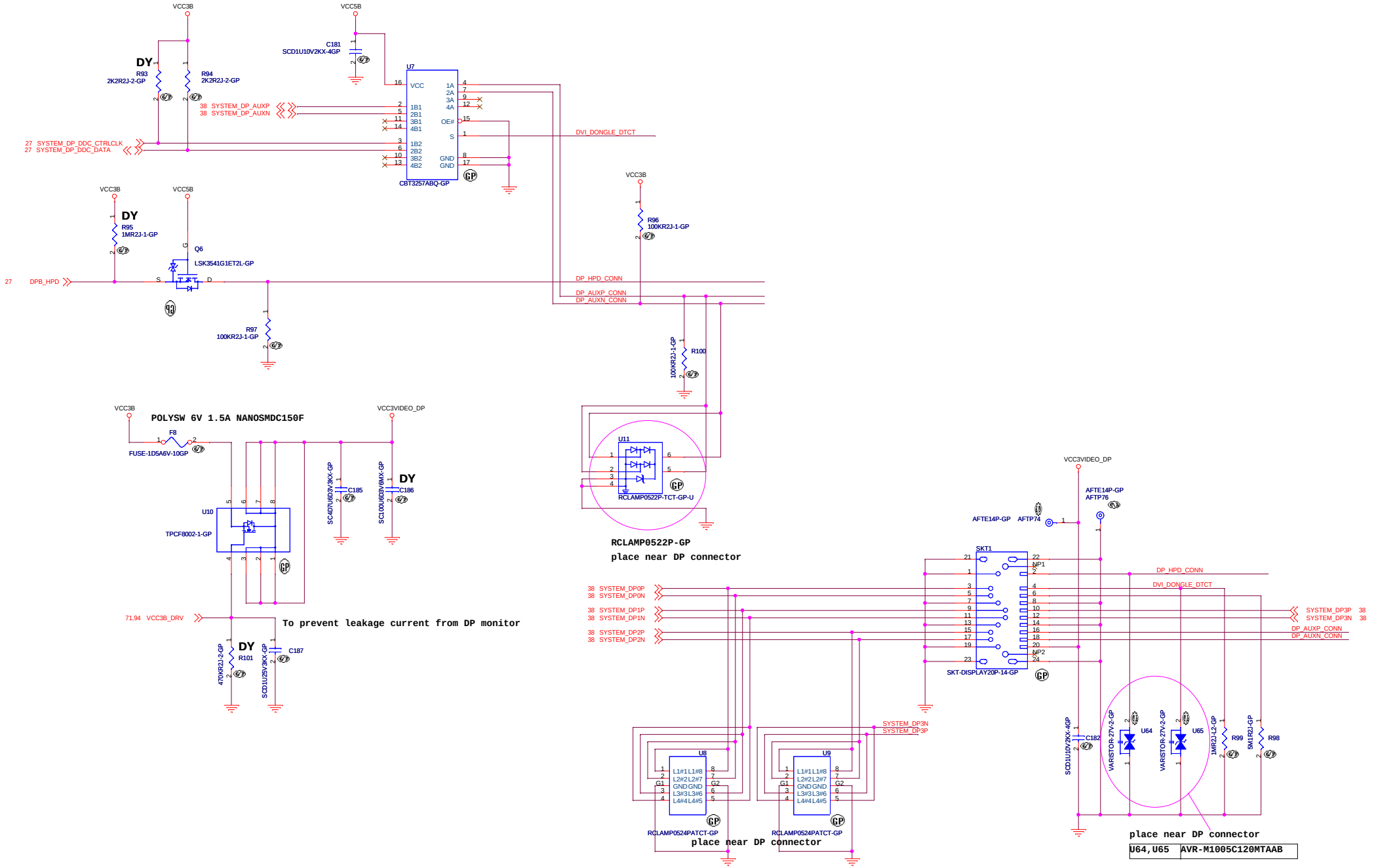
<Core Design>

緯創資通 Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title: **Ext CRT Interface**

Size	Document Number	Rev
A3	Dasher-2	-1

Date: Friday, March 09, 2012 Sheet 36 of 101



<Core Design>

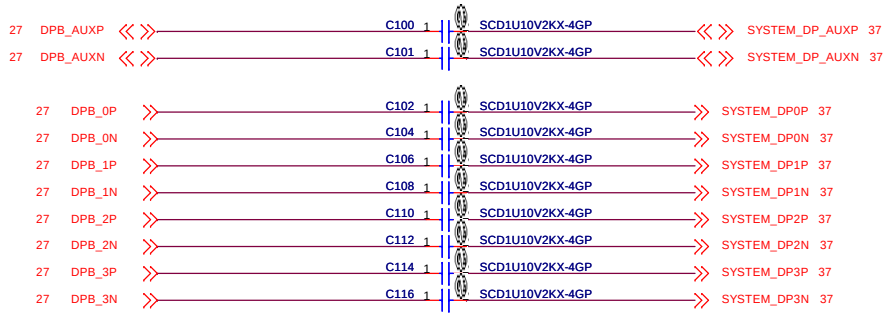
Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title: **Display Port Connector**

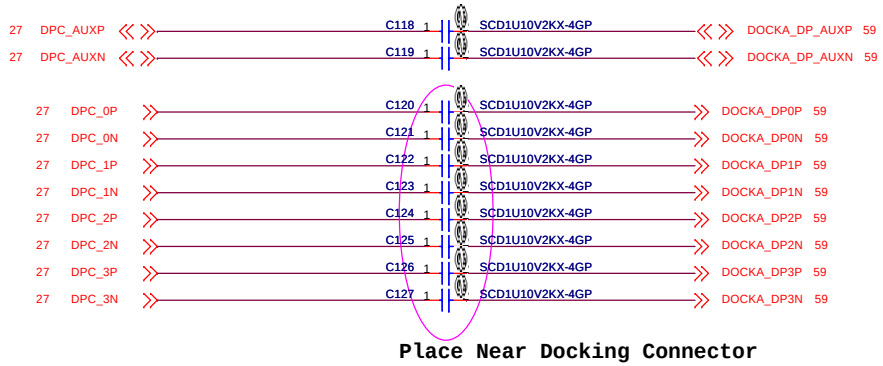
Size	Document Number	Rev
Custom	Dasher-2	-1

Date: Friday, March 09, 2012 Sheet 37 of 101

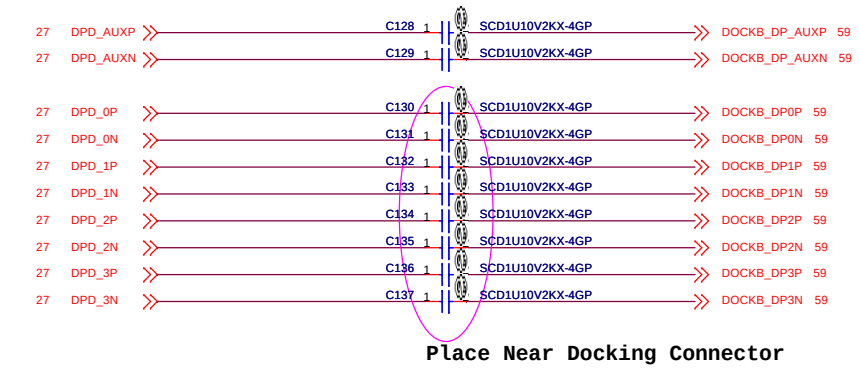
System DP Connector



Docking DP Connector A



Docking DP Connector B



<Core Design>

緯創資通 **Wistron Corporation**
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title: ***Display Port AC Coupling***

Size	Document Number	Rev
A3	Dasher-2	-1

Date: Friday, March 09, 2012 Sheet 38 of 101

BLANK

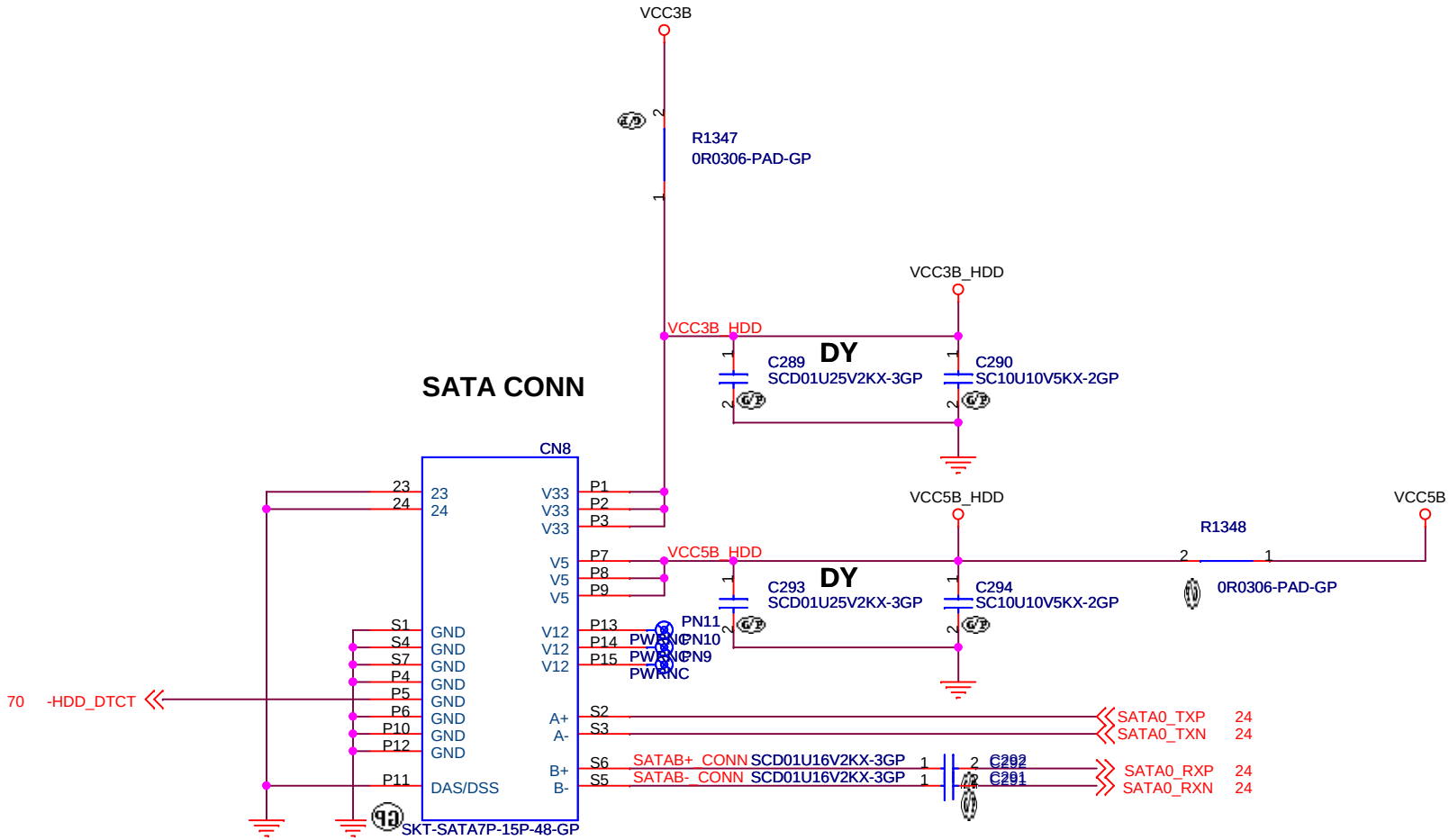
<Core Design>

緯創資通 **Wistron Corporation**
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title: ***BLANK***

Size: A3 | Document Number: **Dasher-2** | Rev: **-1**

Date: Tuesday, February 21, 2012 | Sheet 39 of 101



<Core Design>

緯創資通 **Wistron Corporation**
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title: ***SATA Connector***

Size	Document Number	Rev
A4	**Dasher-2**	-1

Date: Friday, March 09, 2012 Sheet 40 of 101

B
L
A
N
K

<Core Design>

緯創資通 **Wistron Corporation**
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title: ***BLANK***

Size	Document Number	Rev
A3	Dasher-2	-1

Date: Tuesday, February 21, 2012 Sheet 41 of 101

USB3.0 port0

U23,U18		
USB3.0 Enable	TI TPS2069DGN GMT G548A1F51U	74.02069.079 74.00548.A79

L35,L38,L41,L42		
1st:TDK 2nd:Murata	TCM1210S-500-2P DLP11SA350HL2L	68.01210.20E 68.11350.201

Dual Layout

Should use non-discharge part.

WIDE PATTERN (MIN 500MA)
PLACE NEAR USB CONNECTOR

USB3.0 CONNECTOR

USB3.0 port1

Near J1

Should use non-discharge part.

WIDE PATTERN (MIN 500MA)
PLACE NEAR USB CONNECTOR

USB3.0 CONNECTOR

Dual Layout

U25,U77	
1st:Infineon ESD3V3U4ULC	83.3V3U4.0A0
2nd:NXP IP4294CZ10-TBR	83.04294.0A0
3rd:AMC AZ1045-04F DFN2510P 10E	83.01045.AA0

USB port9(Always On USB)

Near SKT3

C299, C303,TC5			
1st	NEC Tokin	TEPSLB20J157M	77.C1571.09L
2nd	SANYO	6TPE150MAZB	77.21571.111

Always on port

WIDE PATTERN (MIN 500MA)
PLACE NEAR USB CONNECTOR

Should use discharge part.

U76,U22,U19	
1st:AOS AOZ8904CIL	83.08904.0AE
2nd:Little Fuse SP3002-04HTG	84.03200.0AF

Always On USB Part List (U20)		
TI	~SDV	FVT~
	TPS2541RTER (74.02541.073)	TPS2541RTER(74.02541.073)
		TPS2541A(74.02541. A73)

Bluetooth Module

Near CN9

PSIT Config.

	PSIT-1	PSIT-2	PSIT-3	PSIT-4	PSIT-5	PSIT-6
L35,L41	No ASM	No ASM	No ASM	No ASM	No ASM	No ASM
L38,L42	TDK	No ASM	No ASM	TDK	No ASM	Murata
U25,U77	Infineon	NXP	AMC	NXP	Infineon	AMC
R152,287,323,324	ASM	ASM	ASM	ASM	ASM	ASM
R320,321,325,328	No ASM	ASM	ASM	No ASM	ASM	No ASM

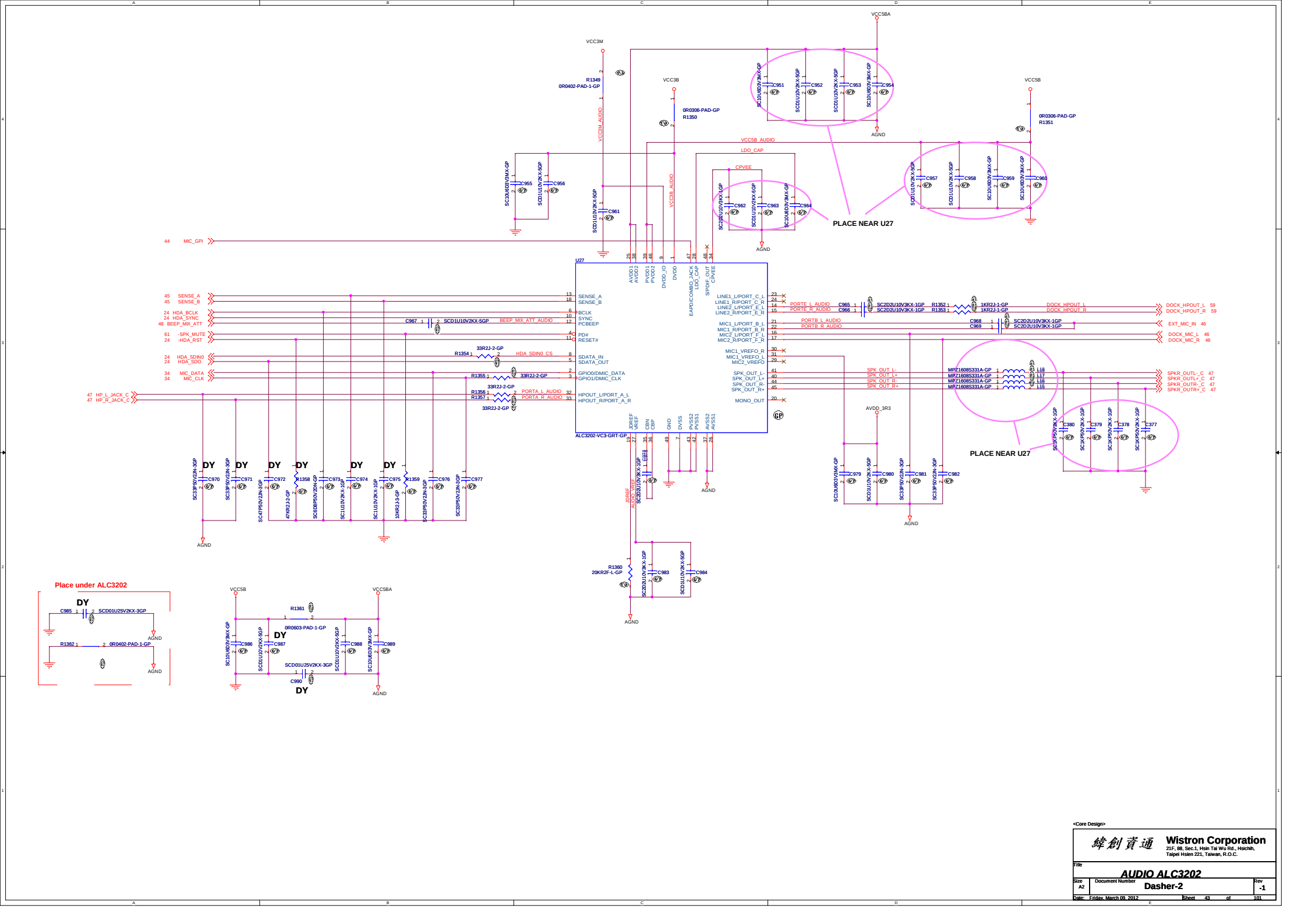
<Core Design>

Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title: USB Connector

Size: Custom | Document Number: Dasher-2 | Rev: -1

Date: Friday, March 09, 2012 | Sheet 42 of 101


VCC5BA
VCC3M
VCC3B
VCC5B
VCC5B_AUDIO
LDO_CAP
CPVEE
PLACE NEAR U27
U27
ALC3202-VC3-GRT-GP
SENSE_A
SENSE_B
BCLK
SYNC
PCBEEP
PD#
RESET#
SDATA_IN
SDATA_OUT
GPIO0/DMIC_DATA
GPIO1/DMIC_CLK
HPOUT_L/PORT_A_L
HPOUT_R/PORT_A_R
MONO_OUT
MIC_GPI
HDA_BCLK
HDA_SYNC
BEEP_MIX_ATT
-SPK_MUTE
-HDA_RST
HDA_SDIN0
HDA_SDO
MIC_DATA
MIC_CLK
HP_L_JACK_C
HP_R_JACK_C
BEEP_MIX_ATT_AUDIO
HDA_SDIN0_CS
PORTA_L_AUDIO
PORTA_R_AUDIO
PORTE_L_AUDIO
PORTE_R_AUDIO
PORTB_L_AUDIO
PORTB_R_AUDIO
DOCK_HPOUT_L
DOCK_HPOUT_R
EXT_MIC_IN
DOCK_MIC_L
DOCK_MIC_R
SPK_OUT_L-
SPK_OUT_L+
SPK_OUT_R-
SPK_OUT_R+
SPKR_OUTL-_C
SPKR_OUTL+_C
SPKR_OUTR-_C
SPKR_OUTR+_C
AVDD_3R3
AGND
PLACE NEAR U27
Place under ALC3202
DY
<Core Design>
Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.
Title
AUDIO ALC3202
Size A2
Document Number Dasher-2
Rev -1
Date: Friday, March 09, 2012
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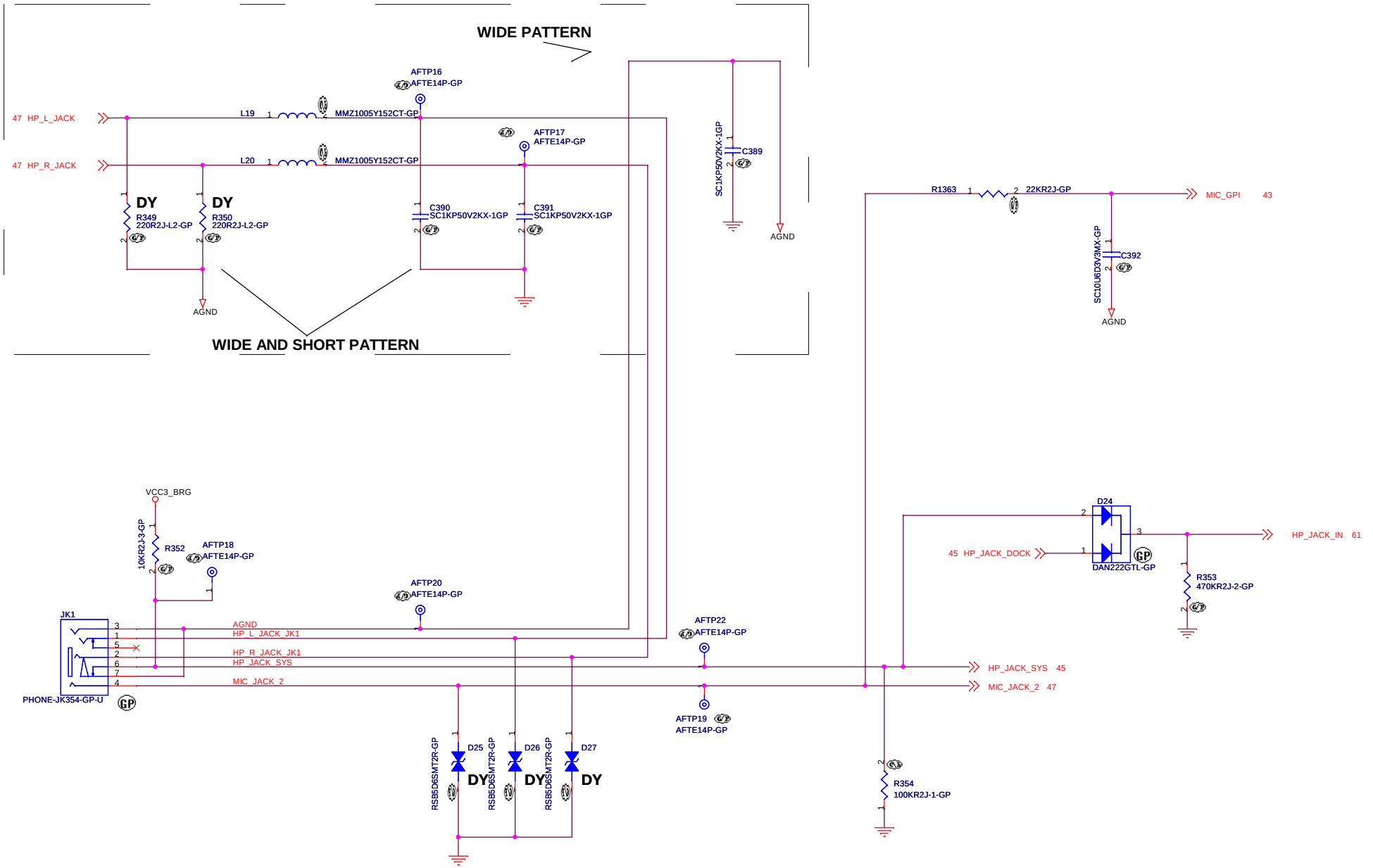
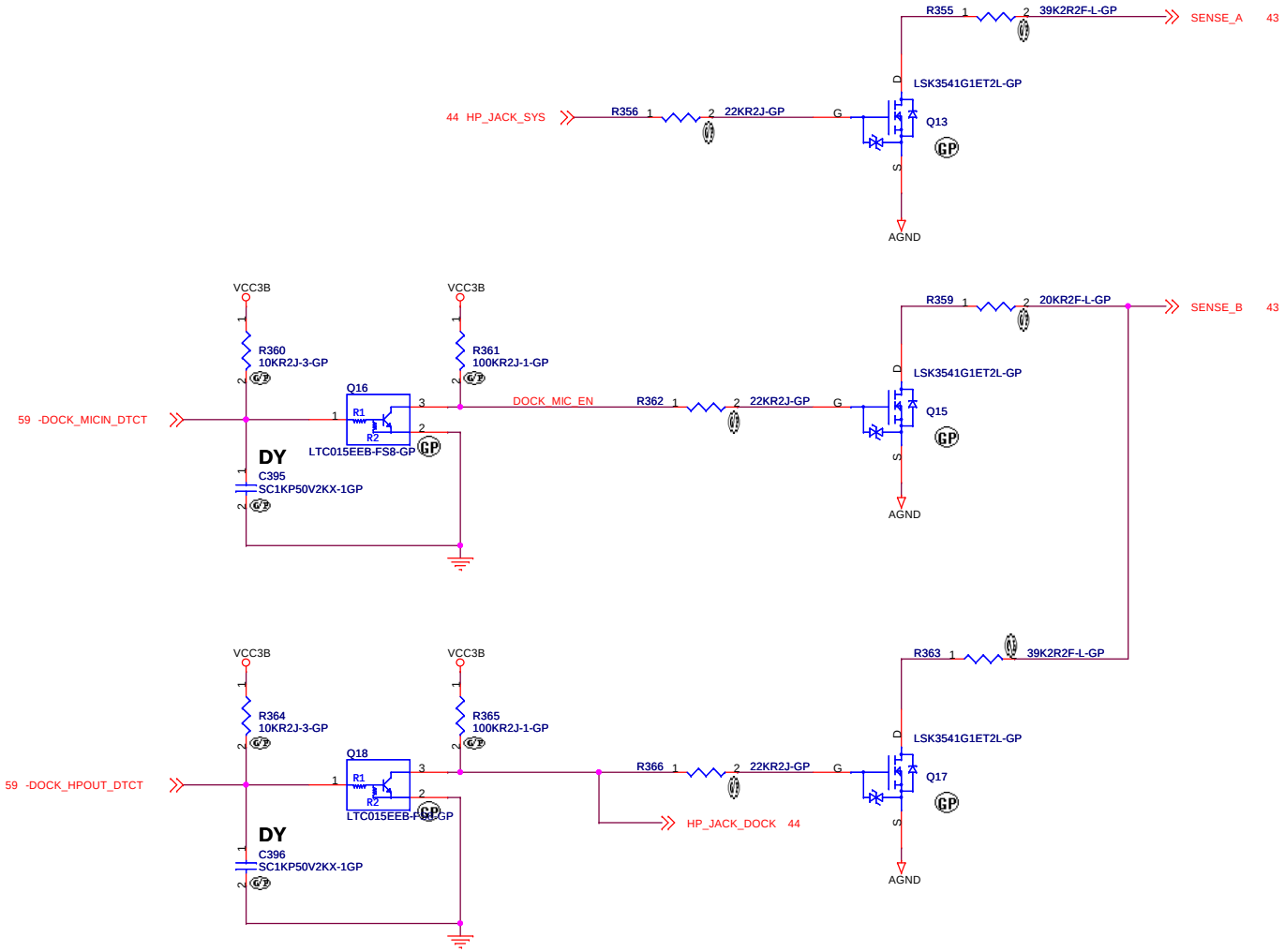
NEAR HEADPHONE CONN



<Core Design>

緯創資通	**Wistron Corporation** 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title	***Audio Connector***	
Size A3	Document Number **Dasher-2**	Rev **-1**
Date: Friday, March 09, 2012	Sheet 44 of 101	



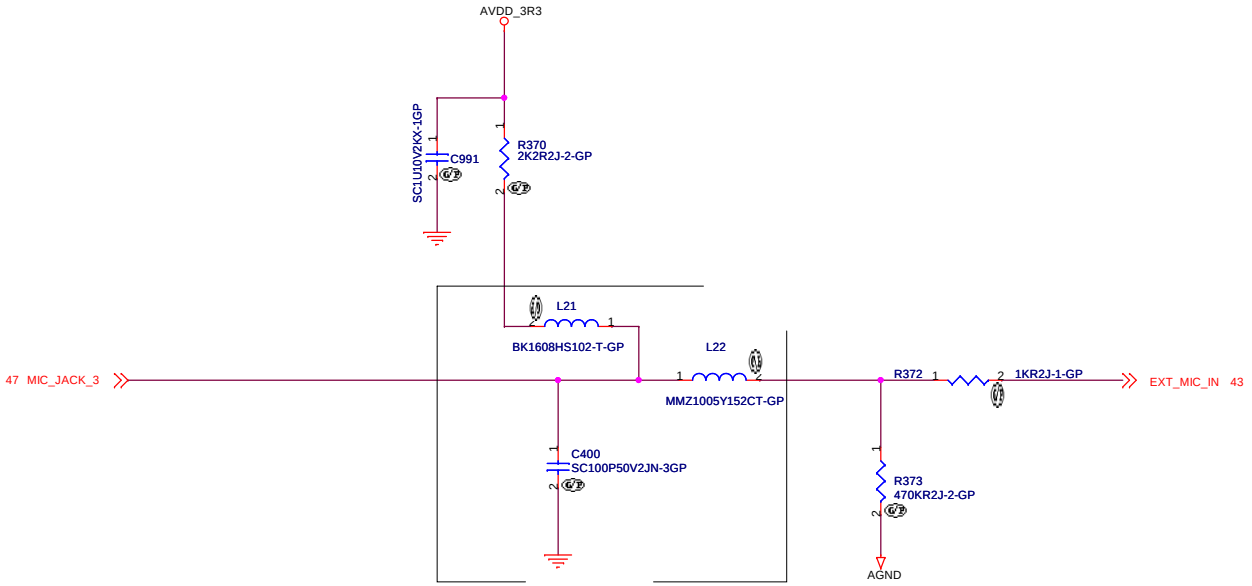
<Core Design>

緯創資通 **Wistron Corporation**
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

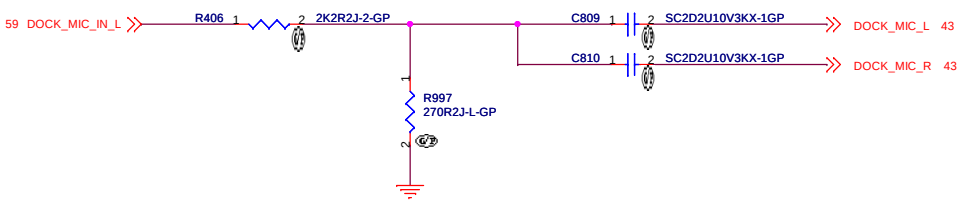
Title: ***Audio Jack Sense***

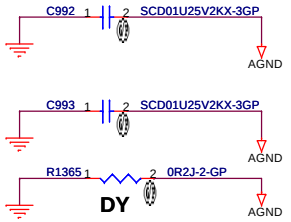
Size	Document Number	Rev
A3	Dasher-2	-1

Date: Friday, March 09, 2012 Sheet 45 of 101



NEAR EXT MIC CONN

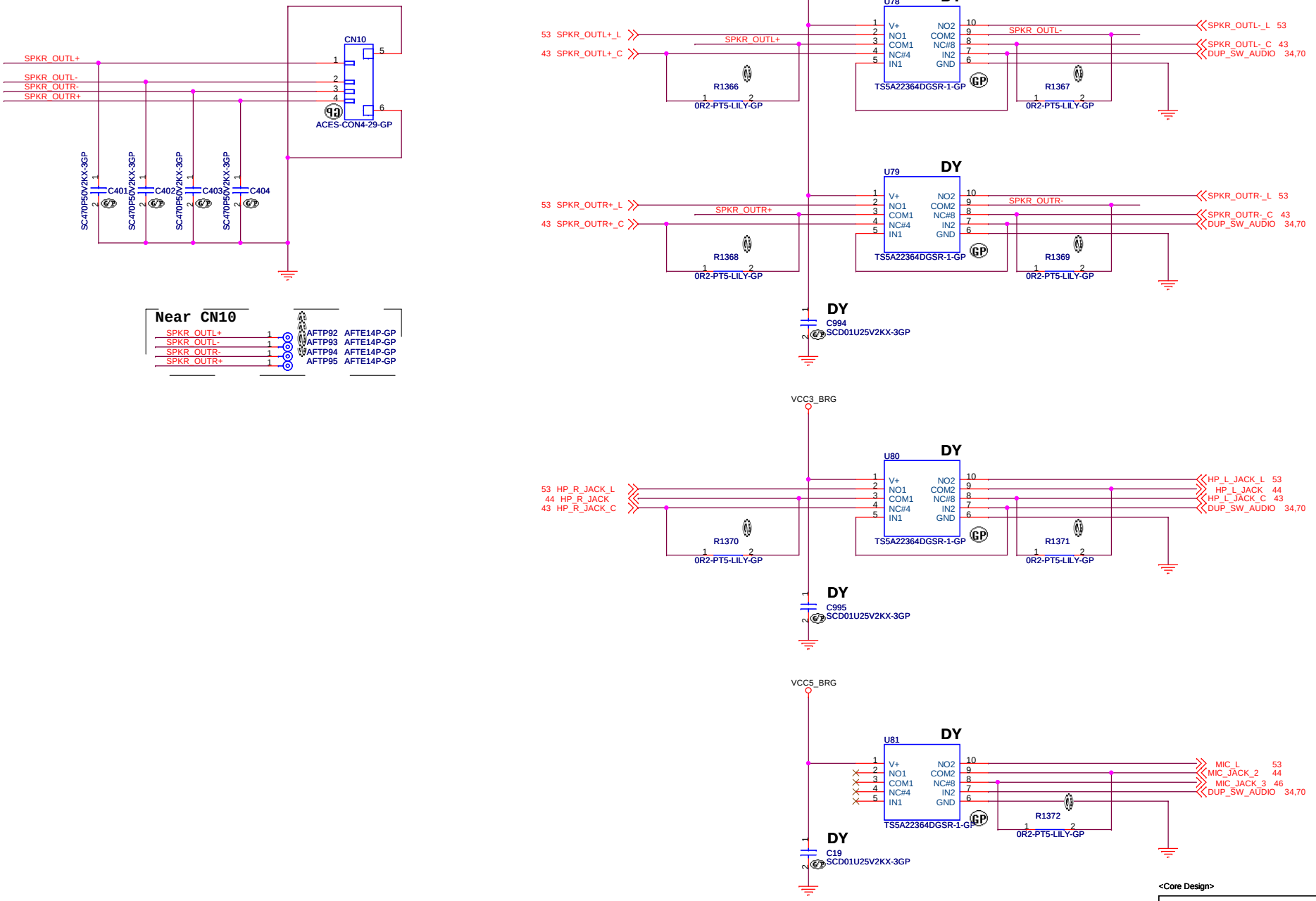




<Core Design>

緯創資通 **Wistron Corporation**
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title	***Audio Ext MIC I/F***	
Size A3	Document Number **Dasher-2**	Rev -1
Date: Friday, March 09, 2012	Sheet 46 of 101	



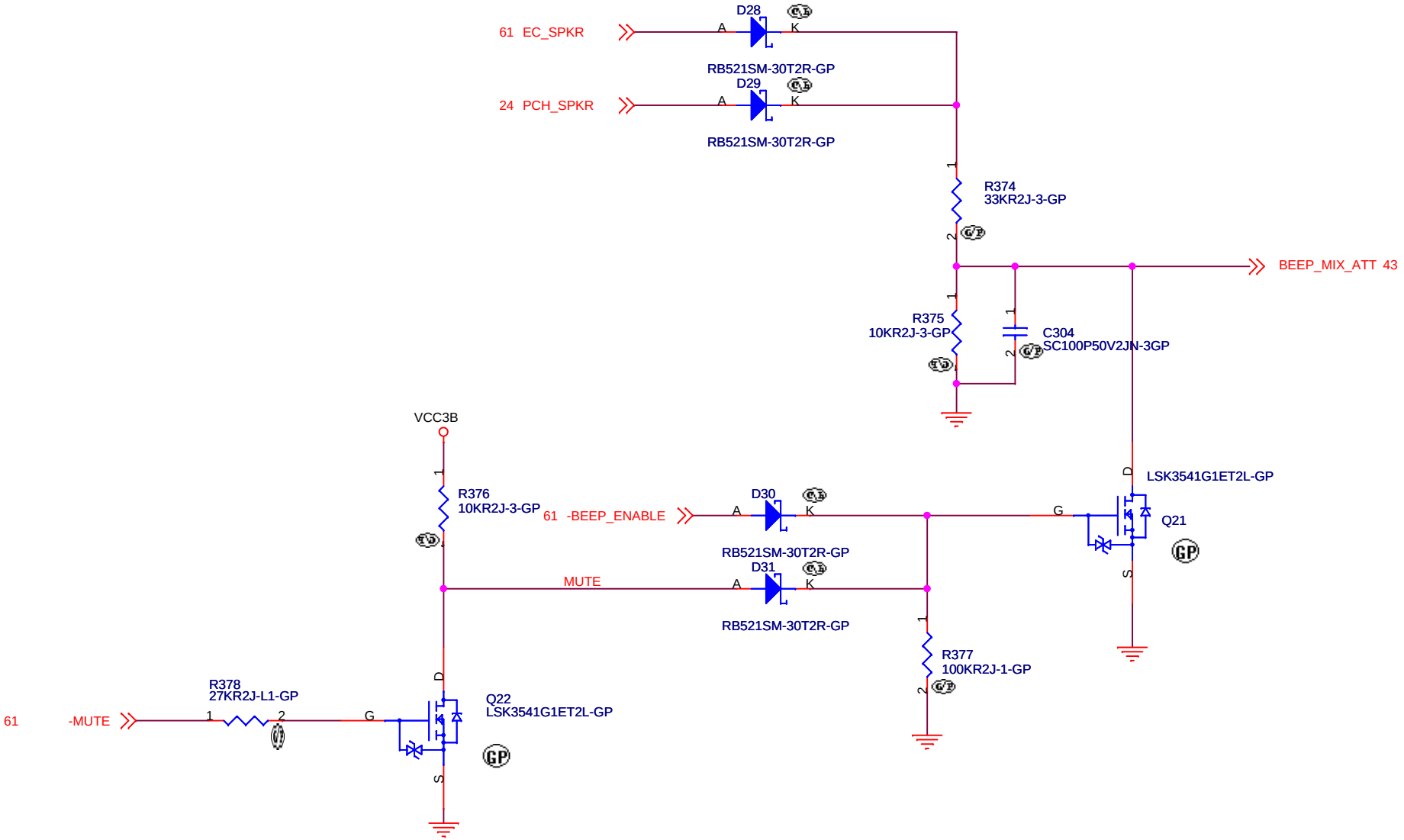
<Core Design>

緯創資通 **Wistron Corporation**
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title: ***AUDIO SPEAKER***

Size	Document Number	Rev
A3	Dasher-2	-1

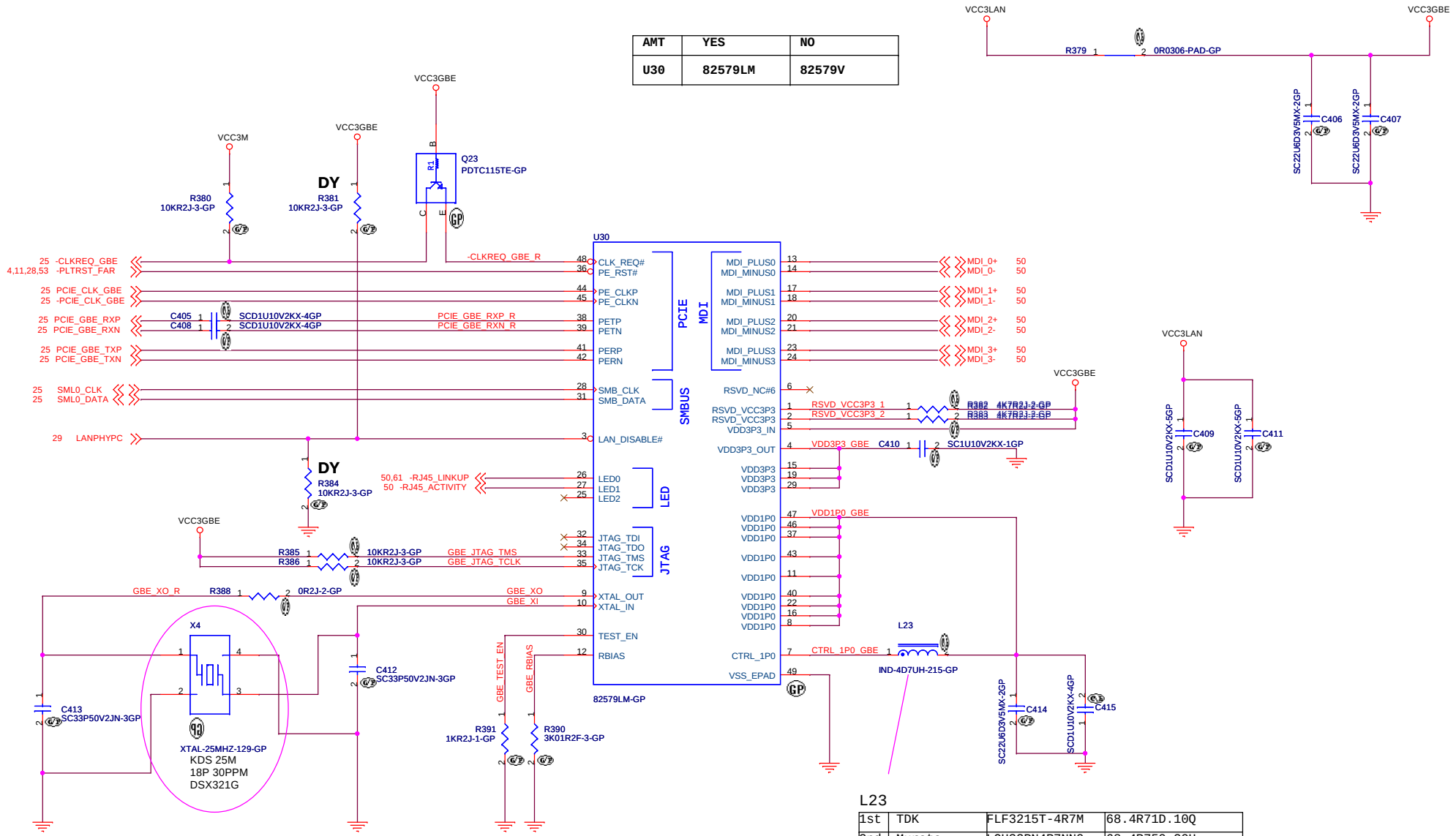
Date: Friday, March 09, 2012 Sheet 47 of 101



<Core Design>

緯創資通	**Wistron Corporation** 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title	***Audio BEEP***	
Size A4	Document Number **Dasher-2**	Rev **-1**
Date: Friday, March 09, 2012	Sheet 48 of	101

AMT	YES	NO
U30	82579LM	82579V



L23

1st	TDK	FLF3215T-4R7M	68.4R71D.10Q
2nd	Murata	LQH32PN4R7NN0	68.4R750.20H
3rd	Taiyo Yuden	BRL3225T4R7M	68.4R71E.10I

X4

1st	KDS	DSX321G 25M 18P 30PPM	82.30020.B11
2nd	H.ELE	HSX321S 25M 18P 30PPM	82.30020.B21
3rd	TXC	7V25020001 25M 18P 30PPM	41U6141AA

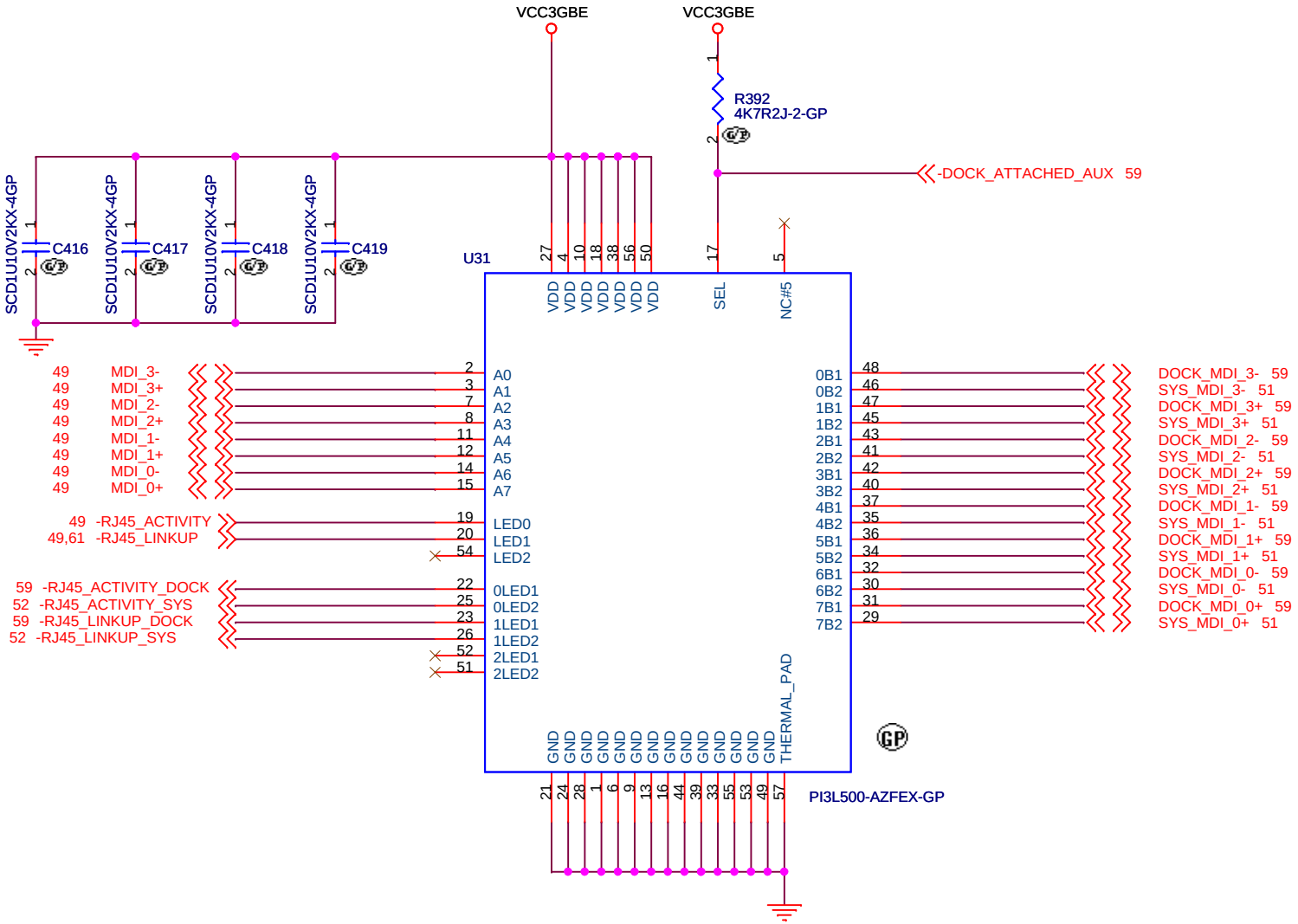
<Core Design>

緯創資通 Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title: GBE Lewisville

Size	Document Number	Rev
A3	Dasher-2	-1

Date: Friday, March 09, 2012 Sheet 49 of 101



		Vendor P/N	Wistron P/N
1st	Pericom	PI3L500AZFEX	73.3L500.003
2nd	TI	TS3L500AERHUR	73.3L500.A0V
3rd	ST	STMUX1800LQTR	73.01800.003

<Core Design>

緯創資通

Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title

GBE LAN Switch

Size	Document Number	Rev
A4	Dasher-2	-1

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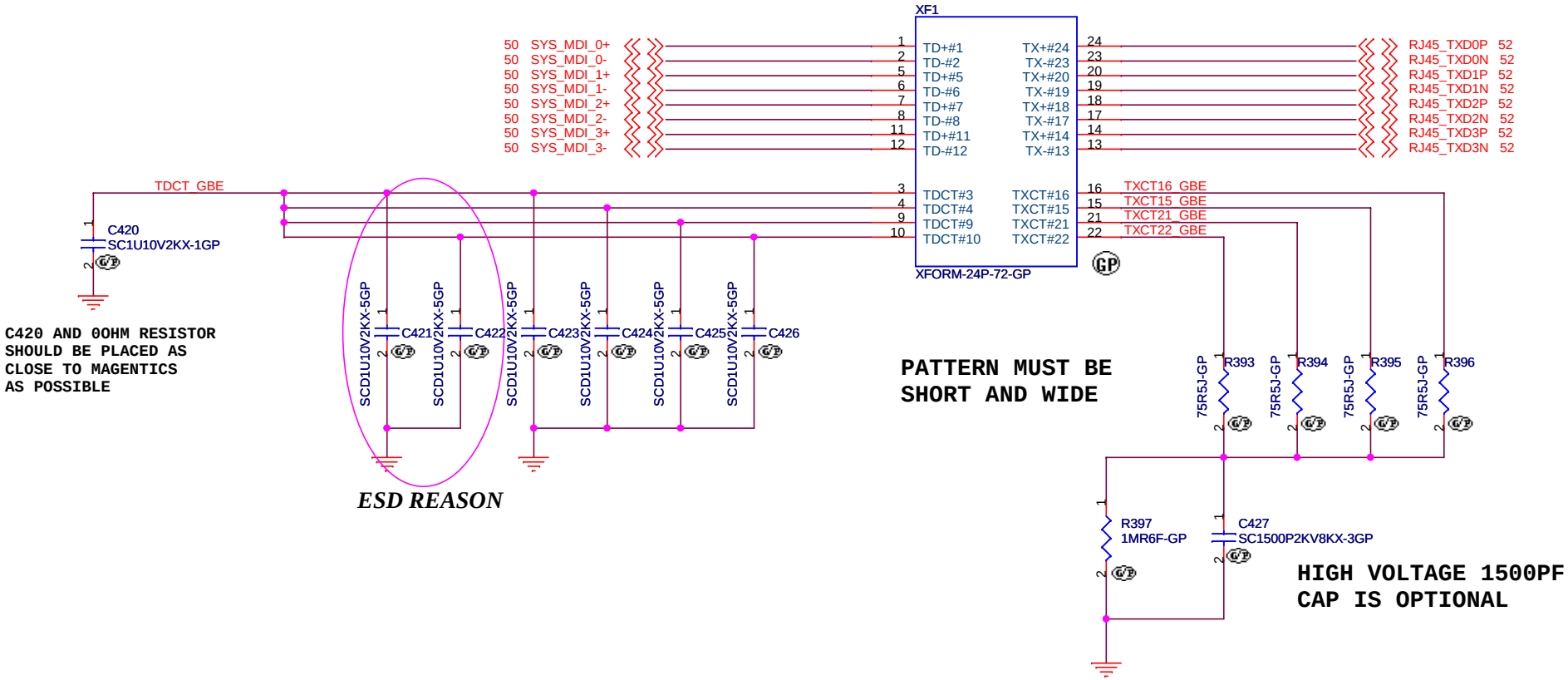

<Core Design>

緯創資通 **Wistron Corporation**
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title: ***GBE MAGNETICS***

Size	Document Number	Rev
Custom	Dasher-2	-1

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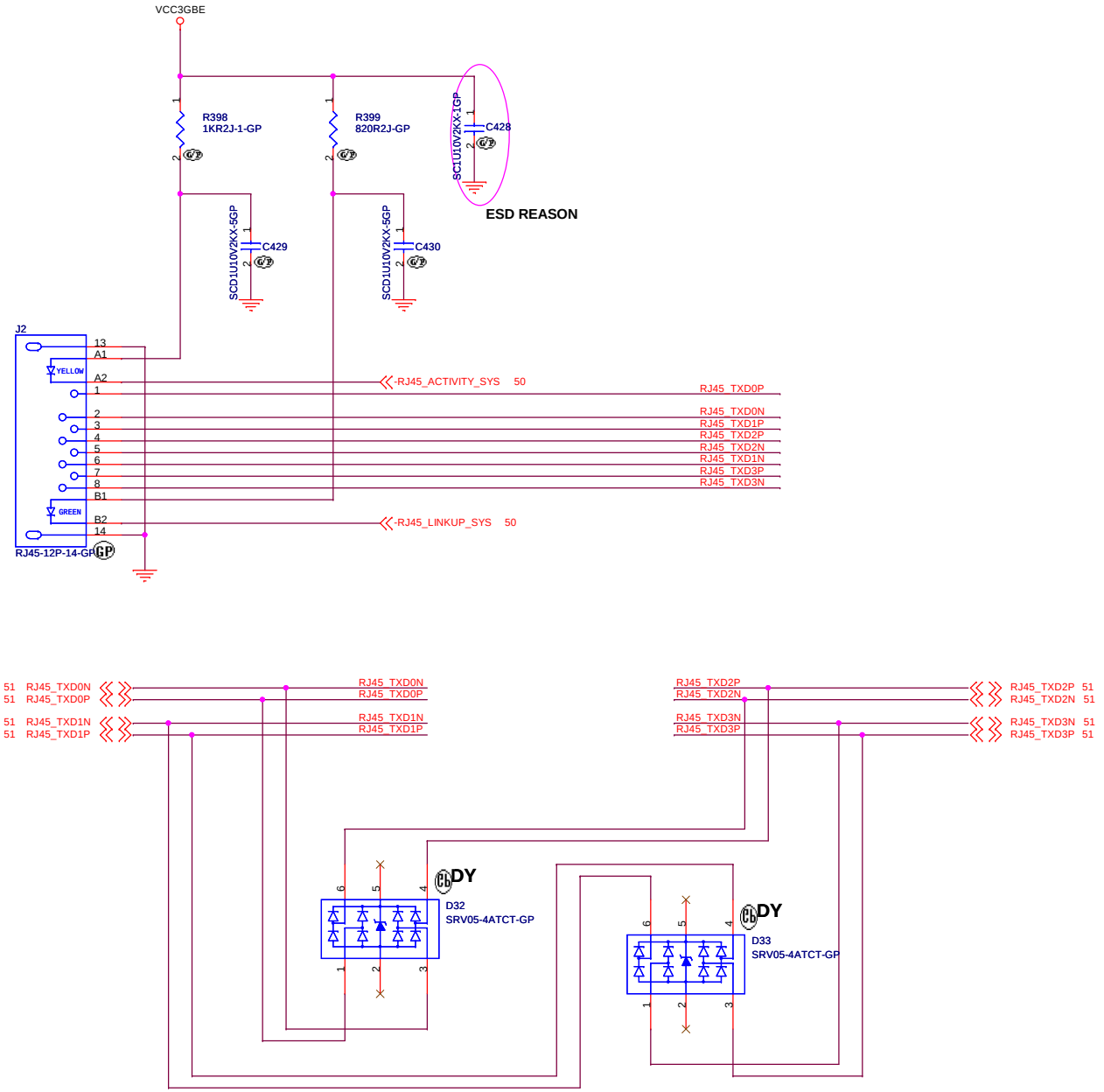

<Core Design>

緯創資通 **Wistron Corporation**
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title: ***RJ45 CONNECTOR***

Size	Document Number	Rev
A3	**Dasher-2**	-1

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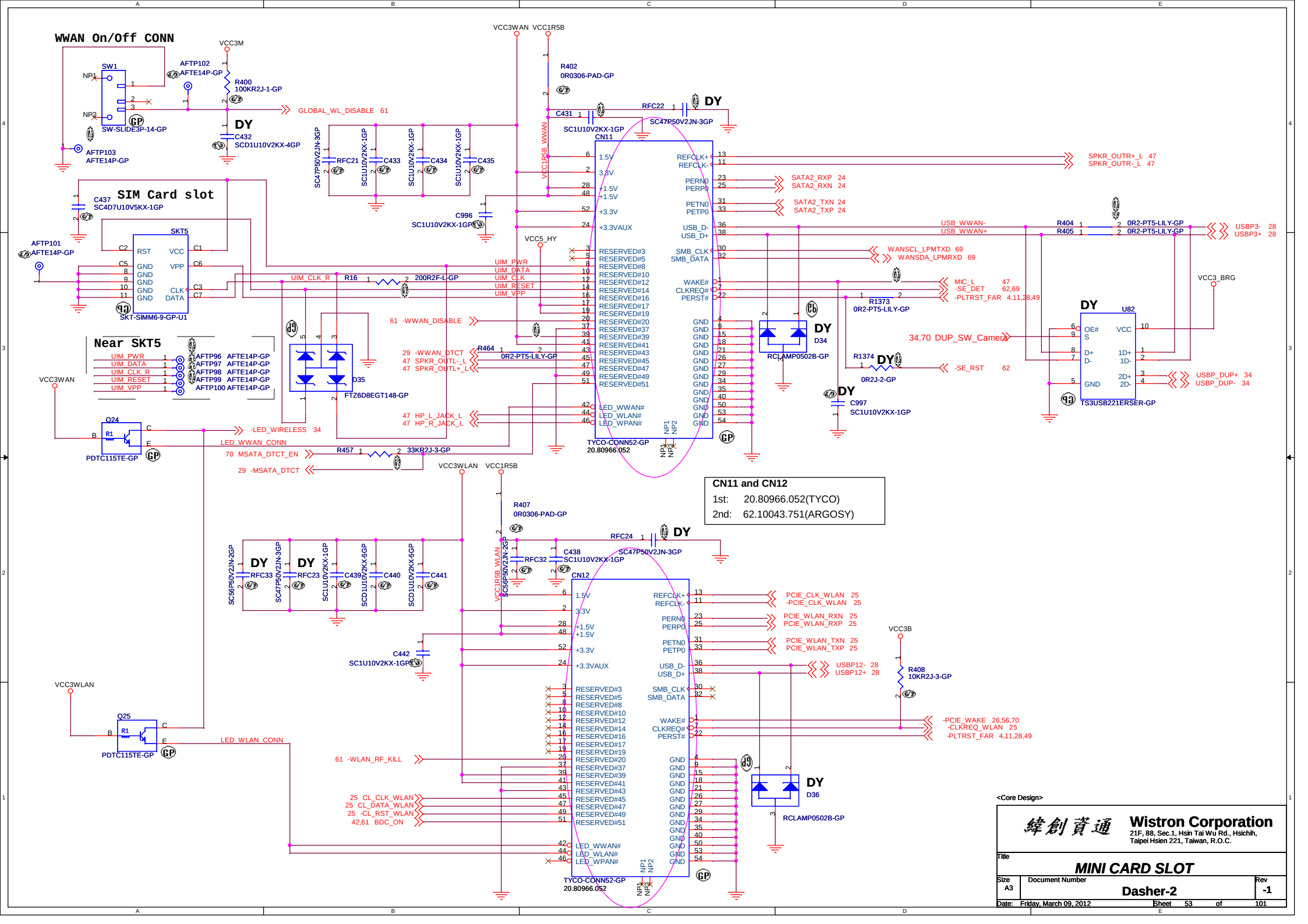
WWAN On/Off CONN
SIM Card slot
Near SKT5
CN11 and CN12
1st: 20.80966.052(TYCO)
2nd: 62.10043.751(ARGOSY)
TYCO-CONN52-GP
20.80966.052
SKT-SIMM6-9-GP-U1
PDTC115TE-GP
FTZ6D8EGT148-GP
RCLAMP0502B-GP
TS3USB221ERSER-GP
SC1U10V2KX-1GP
SC47P50V2JN-3GP
0R0306-PAD-GP
0R2-PT5-LILY-GP
200R2F-L-GP
33KR2J-3-GP
0R2J-2-GP
10KR2J-3-GP
100KR2J-1-GP
SW-SLIDE3P-14-GP
AFTE14P-GP
SC4D7U10V5KX-1GP
SCD1U10V2KX-4GP
SC1U10V2KX-1GP
SC47P50V2JN-3GP
SC56P50V2JN-2GP
SC1U10V2KX-5GP
SCD1U10V2KX-5GP
SC56P50V2JN-3GP
SC47P50V2JN-3GP
VCC3M
VCC3WAN
VCC1R5B
VCC5_HY
VCC3WLAN
VCC3_BRG
VCC3B
GLOBAL_WL_DISABLE 61
61 -WWAN_DISABLE
61 -WLAN_RF_KILL
-LED_WIRELESS 34
LED_WWAN_CONN
LED_WLAN_CONN
70 MSATA_DTCT_EN
29 -MSATA_DTCT
29 -WWAN_DTCT
47 -SPKR_OUTL-_L
47 -SPKR_OUTL+_L
47 HP_L_JACK_L
47 HP_R_JACK_L
UIM_PWR
UIM_DATA
UIM_CLK
UIM_RESET
UIM_VPP
UIM_CLK_R
SPKR_OUTR+_L 47
SPKR_OUTR-_L 47
SATA2_RXP 24
SATA2_RXN 24
SATA2_TXN 24
SATA2_TXP 24
USB_WWAN-
USB_WWAN+
USBP3- 28
USBP3+ 28
WANSCL_LPMTXD 69
WANSDA_LPMRXD 69
MIC_L 47
-SE_DET 62,69
-PLTRST_FAR 4,11,28,49
34,70 DUP_SW_Camera
-SE_RST 62
USBP_DUP+ 34
USBP_DUP- 34
PCIE_CLK_WLAN 25
-PCIE_CLK_WLAN 25
PCIE_WLAN_RXN 25
PCIE_WLAN_RXP 25
PCIE_WLAN_TXN 25
PCIE_WLAN_TXP 25
USBP12- 28
USBP12+ 28
-PCIE_WAKE 26,56,70
-CLKREQ_WLAN 25
-PLTRST_FAR 4,11,28,49
25 CL_CLK_WLAN
25 CL_DATA_WLAN
25 -CL_RST_WLAN
42,61 BDC_ON
<Core Design>
Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.
MINI CARD SLOT
Dasher-2
Rev -1
Date: Friday, March 09, 2012
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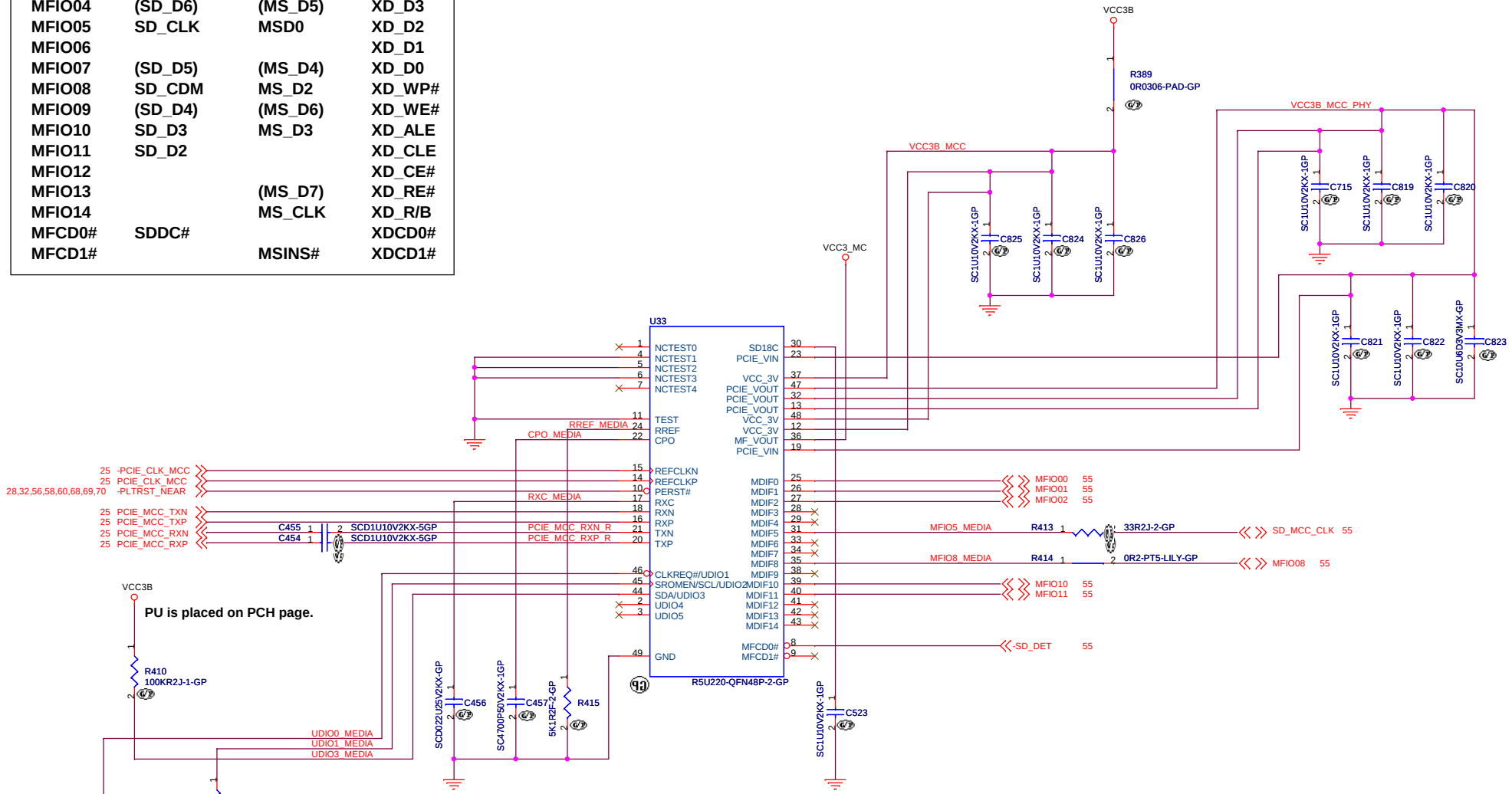
MEDIA I/F	SD/MMC	MEMORYSTICK	XD
MFIO00	SDWP#	MSBS	XD_D7
MFIO01	SD_D1		XD_D6
MFIO02	SD_D0	MS_D1	XD_D5
MFIO03	(SD_D7)		XD_D4
MFIO04	(SD_D6)	(MS_D5)	XD_D3
MFIO05	SD_CLK	MSD0	XD_D2
MFIO06			XD_D1
MFIO07	(SD_D5)	(MS_D4)	XD_D0
MFIO08	SD_CDM	MS_D2	XD_WP#
MFIO09	(SD_D4)	(MS_D6)	XD_WE#
MFIO10	SD_D3	MS_D3	XD_ALE
MFIO11	SD_D2		XD_CLE
MFIO12			XD_CE#
MFIO13		(MS_D7)	XD_RE#
MFIO14		MS_CLK	XD_R/B
MFCD0#	SDDC#		XDCD0#
MFCD1#		MSINS#	XDCD1#



<Core Design>

Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title: MEDIA CARD CONTROLLER

Size	Document Number	Rev
A3	Dasher-2	-1

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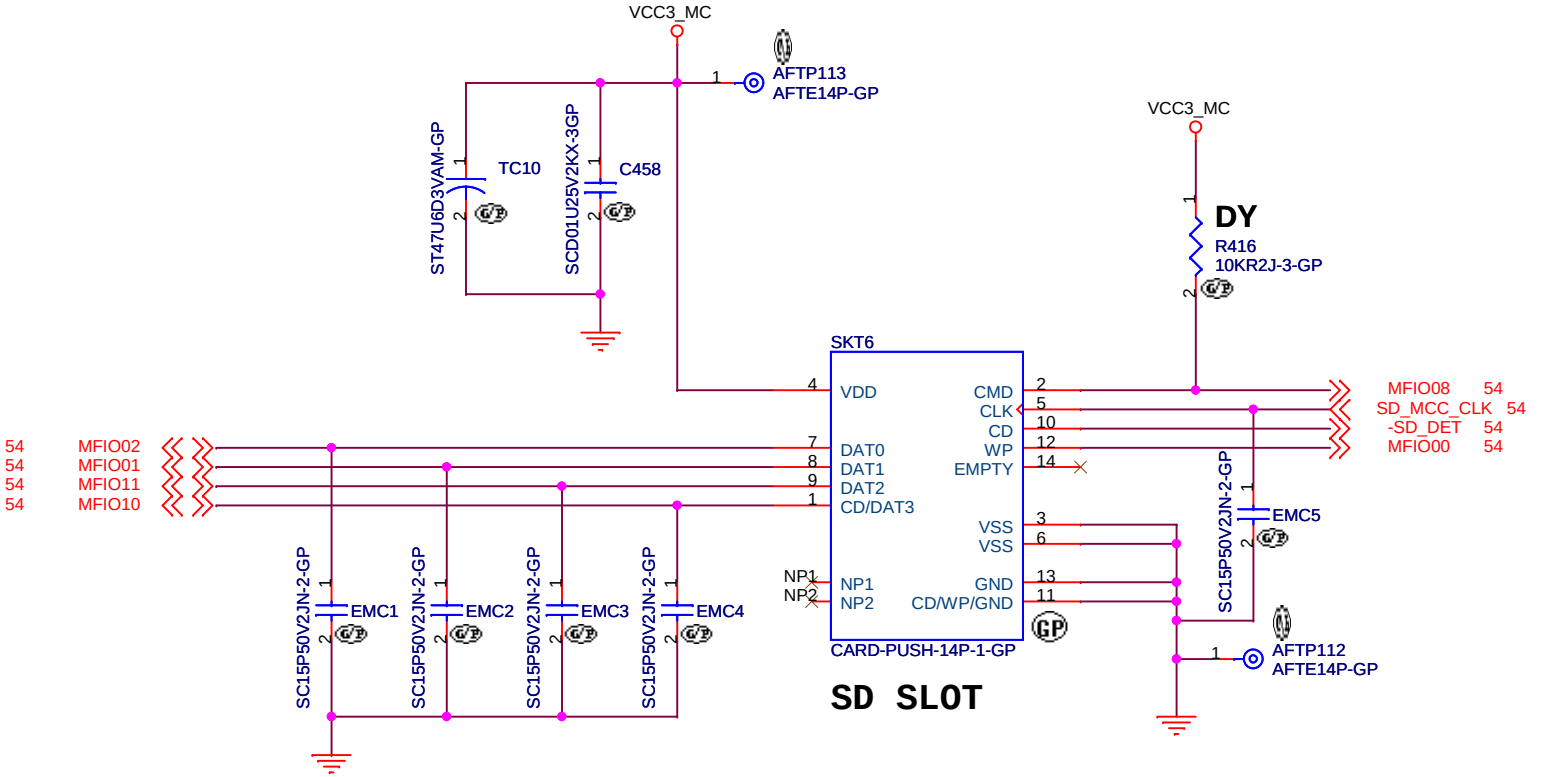
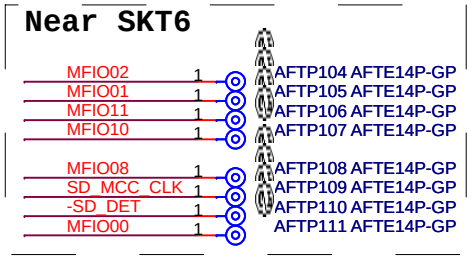




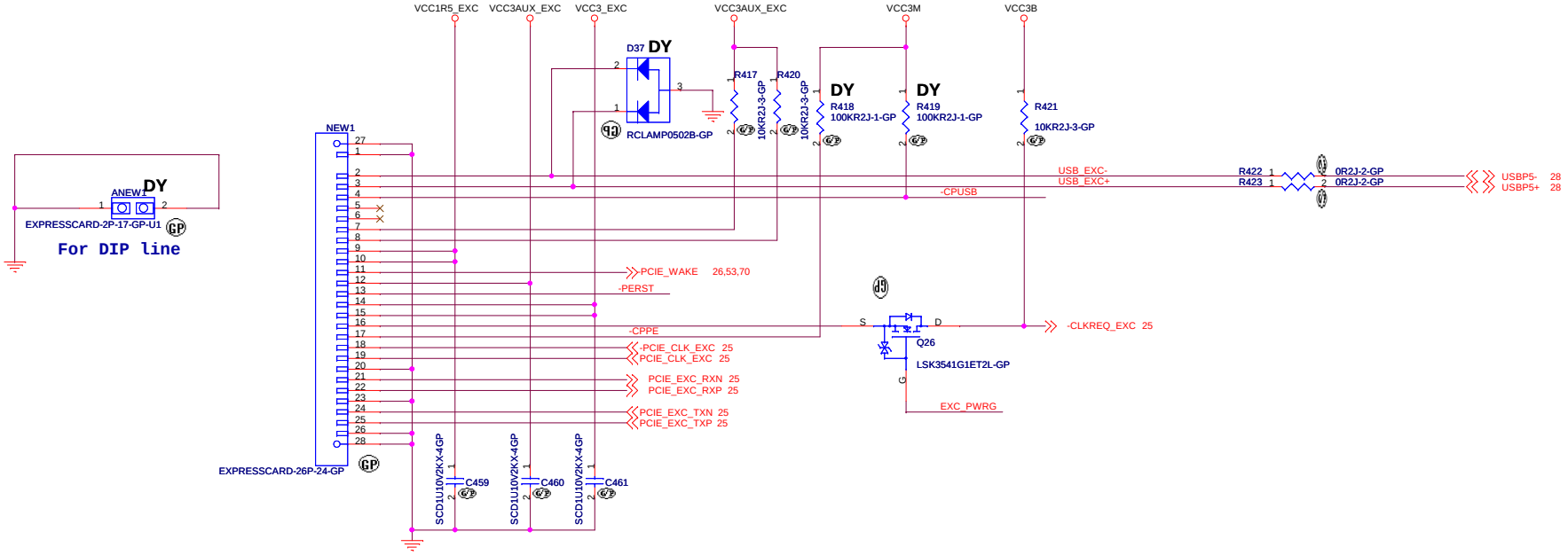
<Core Design>

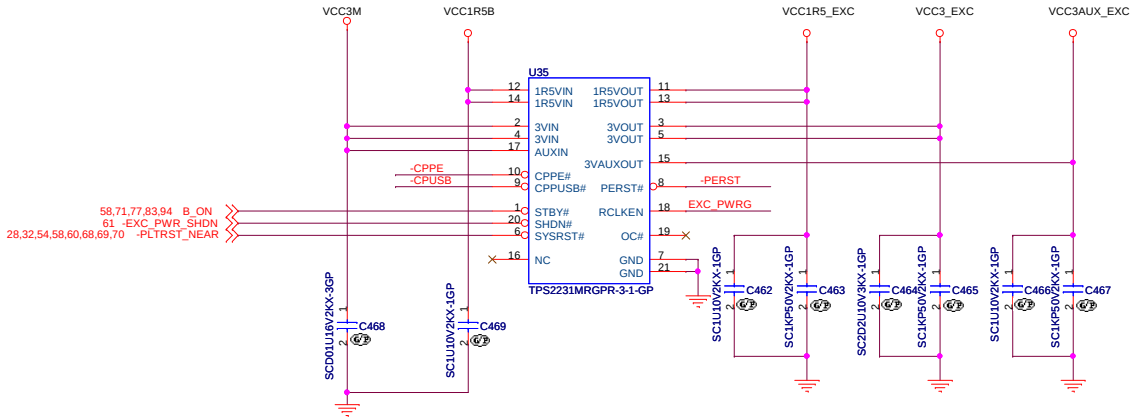
緯創資通 **Wistron Corporation**
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title: ***Media Card Slot***

Size	Document Number	Rev
A4	Dasher-2	-1

Date: Friday, March 09, 2012 Sheet 55 of 101





U35	TI	TPS2231MRGP-3	45K0234BA
	Rohm	BD4156MUV-GTR	74.04156.A73
	Rohm	BD4157MUV-GE2	74.04157.A73

<Core Design>

緯創資通 Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title: Express Card Connector

Size	Document Number	Rev
Custom	Dasher-2	-1

Date: Friday, March 09, 2012 Sheet 56 of 101

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<Core Design>

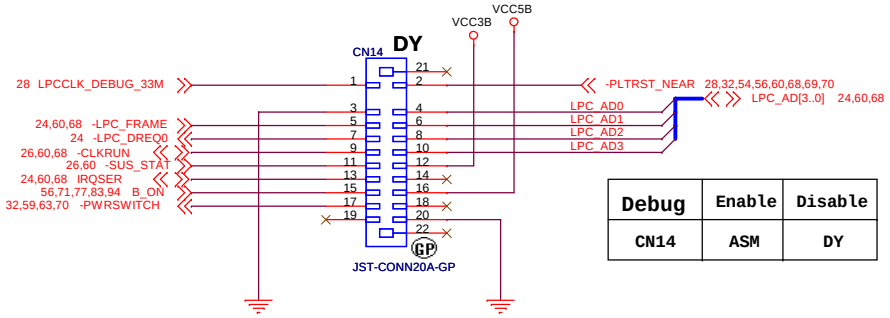
緯創資通

Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title: ***BLANK***		
Size: A	Document Number: **Dasher-2**	Rev: **-1**
Date: Tuesday, February 21, 2012	Sheet 57 of 101	

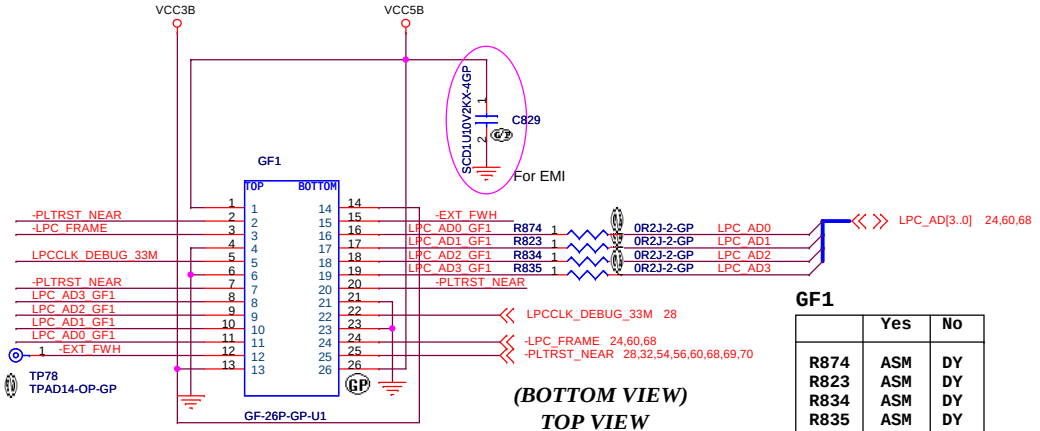
Golden Finger for Debug Board

Debug card connector



Debug	Enable	Disable
CN14	ASM	DY

Put "easy-to -access" place



GF1

	Yes	No
R874	ASM	DY
R823	ASM	DY
R834	ASM	DY
R835	ASM	DY
R235	ASM	DY

Logic

(BOTTOM VIEW)

TOP VIEW

(26) (25)····(15) (14)

13 12 ··· 2 1

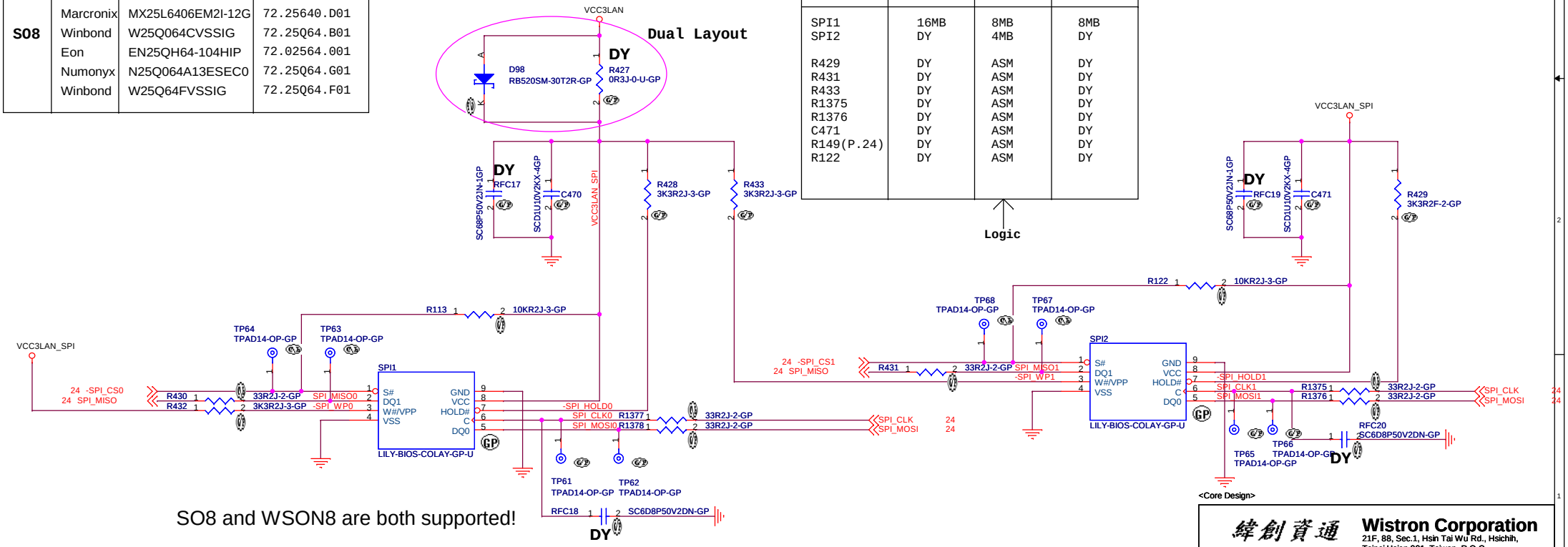
4MB			
S08	Marcronix	MX25L3206EM2I-12G	72.25320.C01
	Winbond	W25Q032BVSSIG	72.25Q32.A01
	Eon	EN25QH32-104HIP	72.02532.B01
	Numonyx	N25Q032A13ESEC0	72.25Q32.C01
8MB			
S08	Marcronix	MX25L6406EM2I-12G	72.25640.D01
	Winbond	W25Q064CVSSIG	72.25Q64.B01
	Eon	EN25QH64-104HIP	72.02564.001
	Numonyx	N25Q064A13ESEC0	72.25Q64.G01
	Winbond	W25Q64FVSSIG	72.25Q64.F01

Configuration Table

	CONFIG-1 16MB	CONFIG-2 12MB	CONFIG-3 8MB
SPI1	16MB	8MB	8MB
SPI2	DY	4MB	DY
R429	DY	ASM	DY
R431	DY	ASM	DY
R433	DY	ASM	DY
R1375	DY	ASM	DY
R1376	DY	ASM	DY
C471	DY	ASM	DY
R149(P.24)	DY	ASM	DY
R122	DY	ASM	DY

Logic

Dual Layout



SO8 and WSON8 are both supported!

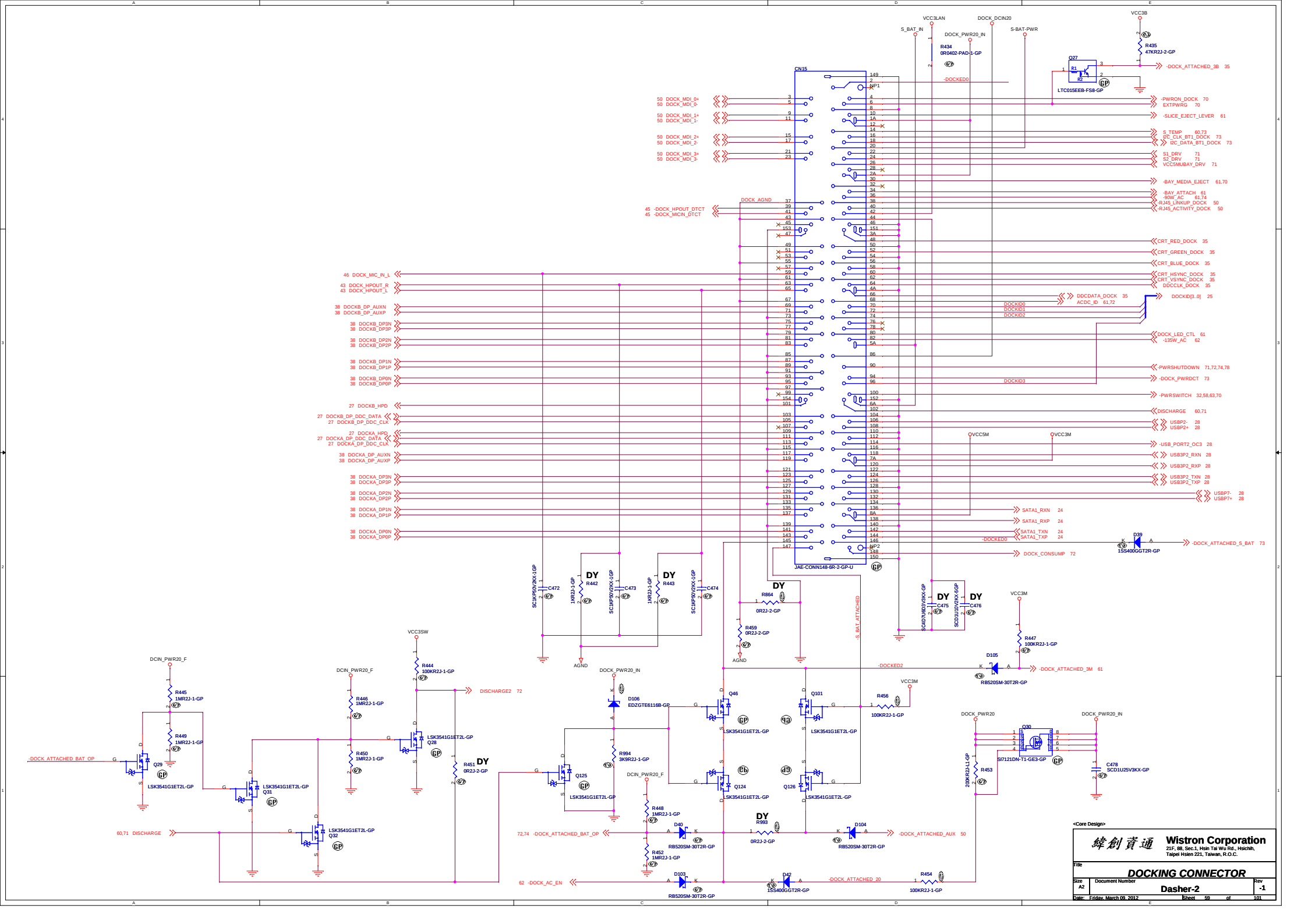
<Core Design>

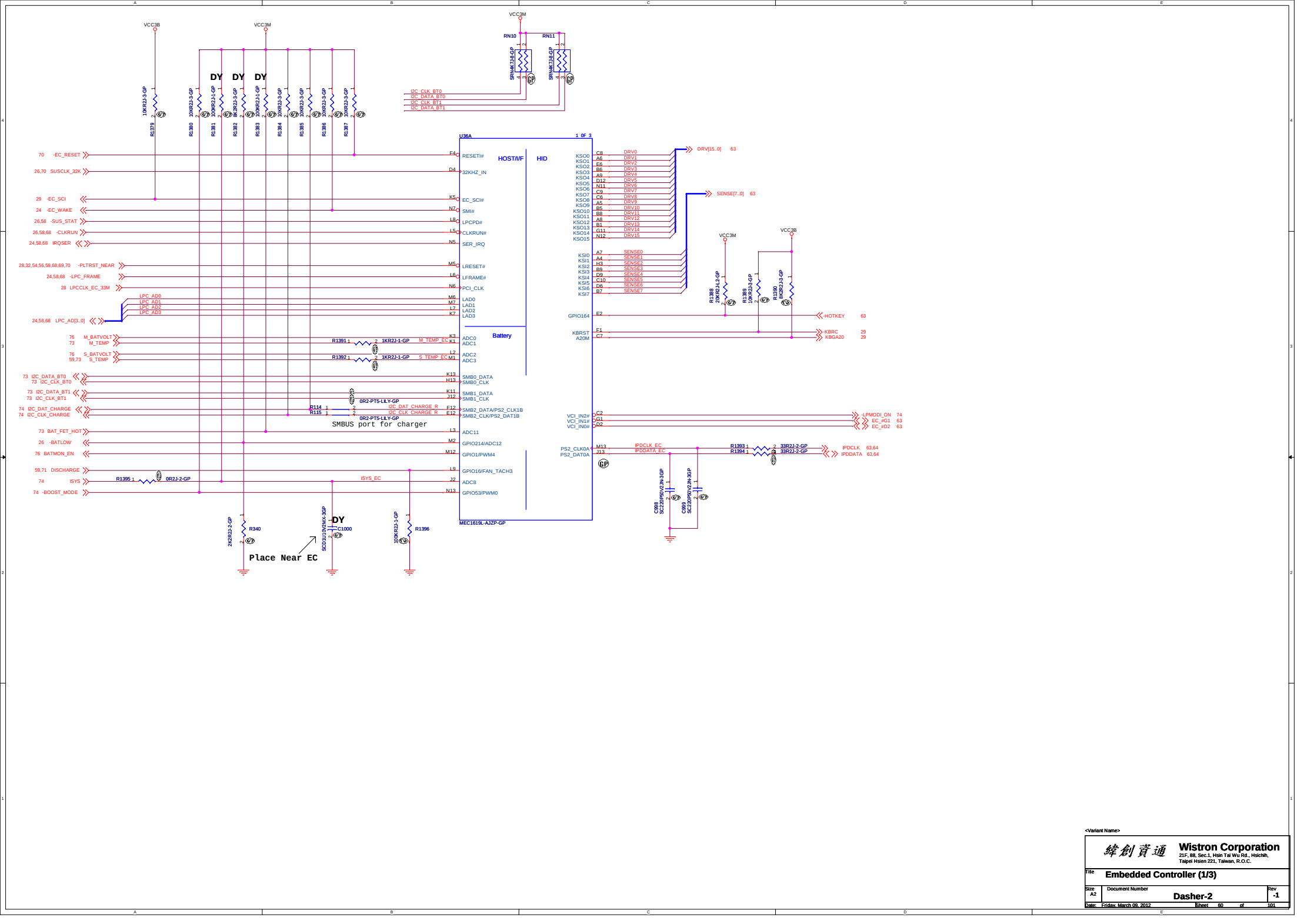
緯創資通 Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title: **SPI&LPC debug card**

Size: A3 | Document Number: **Dasher-2** | Rev: -1

Date: Friday, March 09, 2012 | Sheet 58 of 101


CN15
JAE-CONN148-6R-2-GP-U
VCC3LAN
DOCK_DCIN20
S_BAT_IN
DOCK_PWR20_IN
S-BAT-PWR
VCC3B
R434 0R0402-PAD-1-GP
R435 47KR2J-2-GP
Q27 LTC015EEB-FS8-GP
-DOCKED0
-DOCK_ATTACHED_3B 35
-PWRON_DOCK 70
EXTPWRG 70
-SLICE_EJECT_LEVER 61
S_TEMP 60,73
I2C_CLK_BT1_DOCK 73
I2C_DATA_BT1_DOCK 73
S1_DRV 71
S2_DRV 71
VCC5MUBAY_DRV 71
-BAY_MEDIA_EJECT 61,70
-BAY_ATTACH 61
-90W_AC 61,74
-RJ45_LINKUP_DOCK 50
-RJ45_ACTIVITY_DOCK 50
DOCK_AGND
CRT_RED_DOCK 35
CRT_GREEN_DOCK 35
CRT_BLUE_DOCK 35
CRT_HSYNC_DOCK 35
CRT_VSYNC_DOCK 35
DDCCLK_DOCK 35
DDCDATA_DOCK 35
ACDC_ID 61,72
DOCKID[3..0] 25
DOCKID0
DOCKID1
DOCKID2
DOCKID3
DOCK_LED_CTL 61
-135W_AC 62
-PWRSHUTDOWN 71,72,74,78
-DOCK_PWRDCT 73
-PWRSWITCH 32,58,63,70
DISCHARGE 60,71
USBP2- 28
USBP2+ 28
-USB_PORT2_OC3 28
USB3P2_RXN 28
USB3P2_RXP 28
USB3P2_TXN 28
USB3P2_TXP 28
USBP7- 28
USBP7+ 28
SATA1_RXN 24
SATA1_RXP 24
SATA1_TXN 24
SATA1_TXP 24
D39 1SS400GGT2R-GP
-DOCK_ATTACHED_S_BAT 73
DOCK_CONSUMP 72
VCC5M
VCC3M
-S_BAT_ATTACHED
R864 0R2J-2-GP
R459 0R2J-2-GP
AGND
C472
C473
C474
C475
C476
R442
R443
R447 100KR2J-1-GP
D105 RB520SM-30T2R-GP
-DOCKED2
-DOCK_ATTACHED_3M 61
VCC3SW
R444 100KR2J-1-GP
DISCHARGE2 72
DCIN_PWR20_F
R445 1MR2J-1-GP
R446 1MR2J-1-GP
R449 1MR2J-1-GP
R450 1MR2J-1-GP
R451 0R2J-2-GP
Q29
Q28
Q31
Q32
LSK3541G1ET2L-GP
-DOCK_ATTACHED_BAT_OP
60,71 DISCHARGE
DOCK_PWR20_IN
D106 EDZGTE6116B-GP
Q46
Q101
Q125
Q124
Q126
R456 100KR2J-1-GP
R994 3K9R2J-1-GP
R448 1MR2J-1-GP
R452 1MR2J-1-GP
D40 RB520SM-30T2R-GP
D103 RB520SM-30T2R-GP
D104 RB520SM-30T2R-GP
D42 1SS400GGT2R-GP
R993 0R2J-2-GP
72,74 -DOCK_ATTACHED_BAT_OP
-DOCK_ATTACHED_AUX 50
62 -DOCK_AC_EN
-DOCK_ATTACHED_20
R454 100KR2J-1-GP
DOCK_PWR20
Q30 SI7121DN-T1-GE3-GP
R453 20KR2J-1-GP
C478 SCD1U25V3KX-GP
<Core Design>
Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.
Title
DOCKING CONNECTOR
Size A2
Document Number
Dasher-2
Rev -1
Date: Friday, March 09, 2012
Sheet 59 of 101



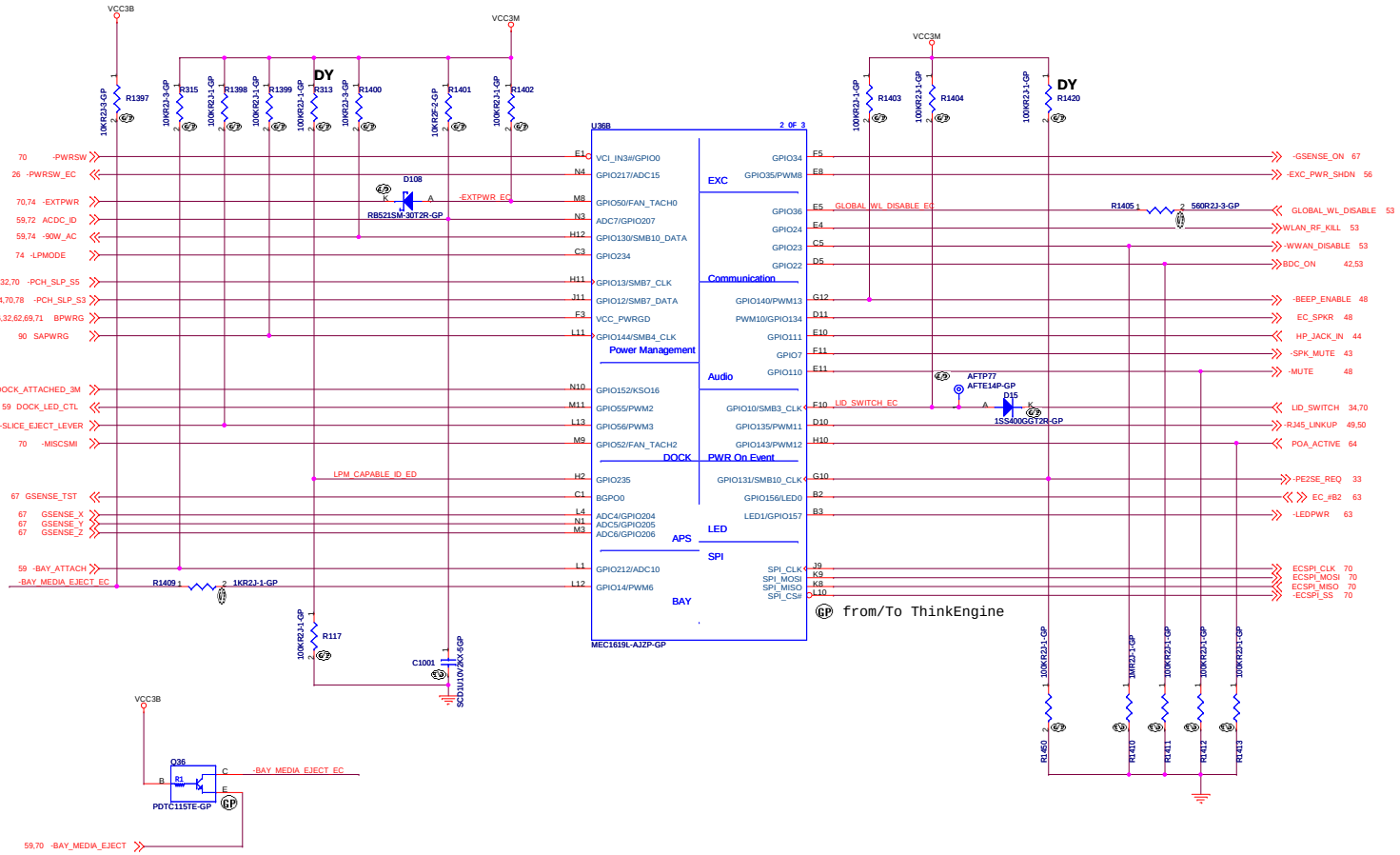
<Variant Name>

Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title: **Embedded Controller (1/3)**

Size	Document Number	Rev
A2	Dasher-2	-1

Date: Friday, March 09, 2012 Sheet 60 of 101



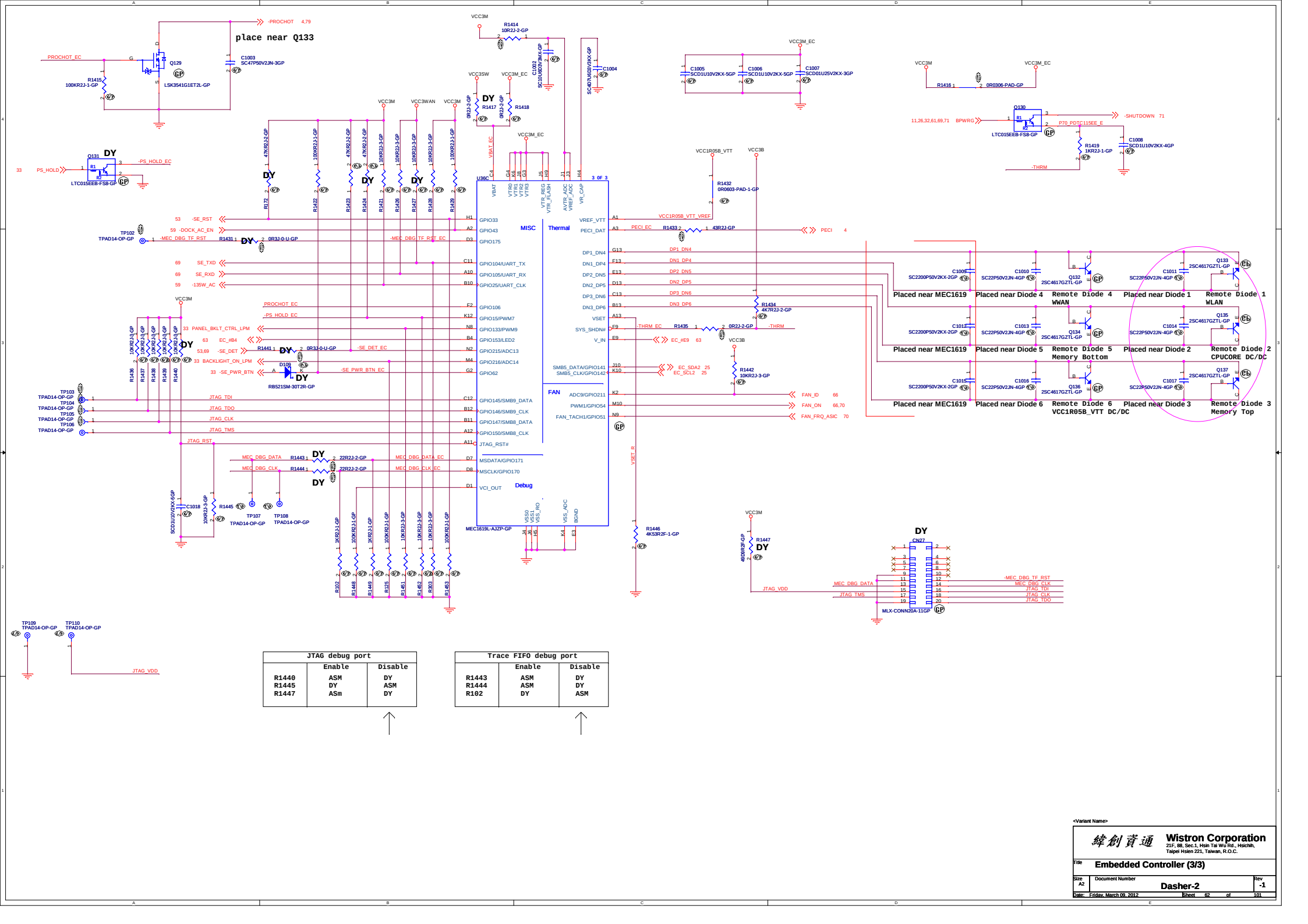
<Core Design>

Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title: Embedded Controller (2/3)

Size: A2 | Document Number: Dasher-2 | Rev: -1

Date: Friday, March 09, 2012 | Sheet 61 of 101



JTAG debug port		
	Enable	Disable
R1440	ASM	DY
R1445	DY	ASM
R1447	ASm	DY

Trace FIFO debug port		
	Enable	Disable
R1443	ASM	DY
R1444	ASM	DY
R102	DY	ASM

<Variant Name>

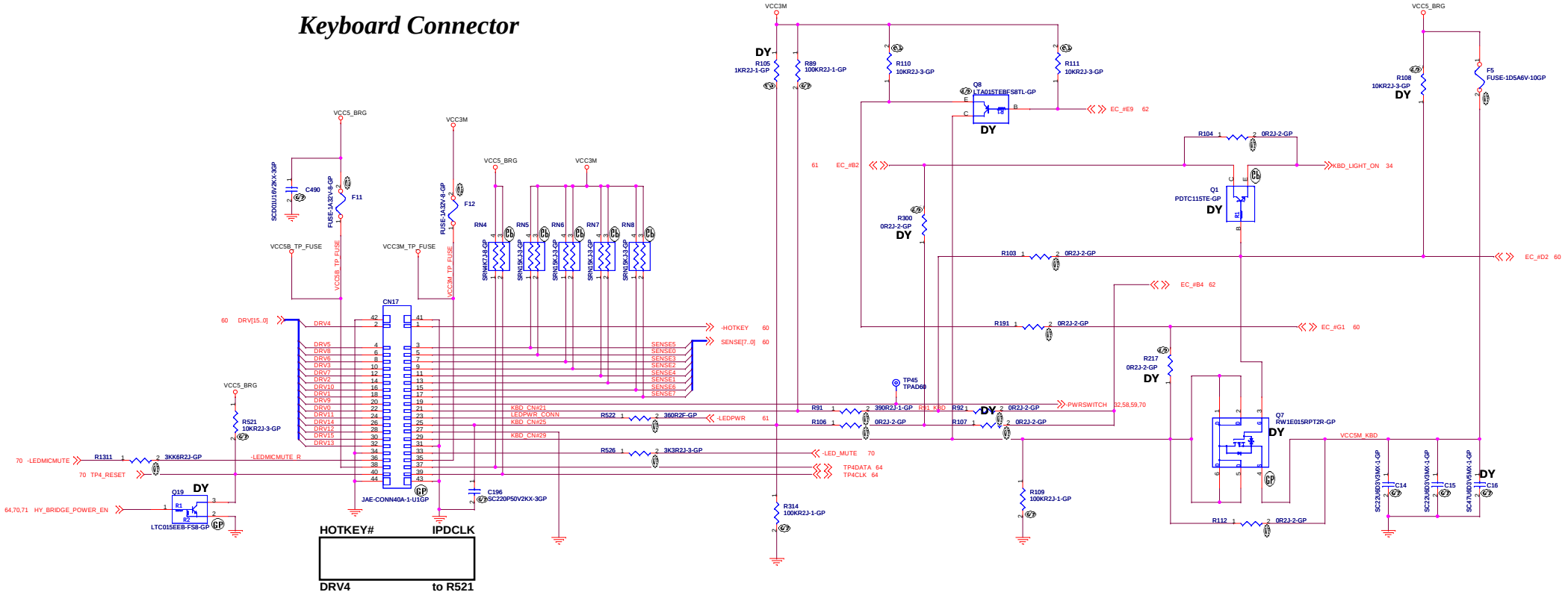
Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title: Embedded Controller (3/3)

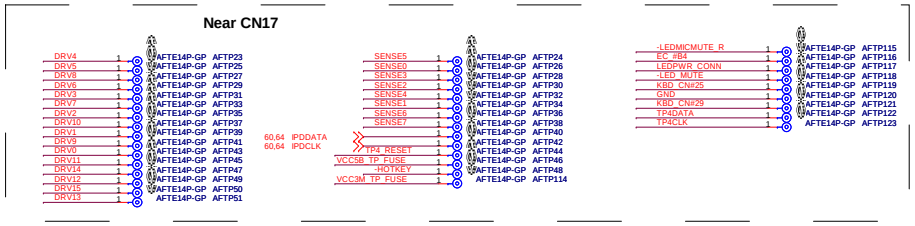
Size	Document Number	Rev
A2	Dasher-2	-1

Date: Friday, March 09, 2012 Sheet 62 of 101

Keyboard Connector



Keyboard Connector Top View



KBD Table1

	case1	case2	case3	case4
CS12 KBD w/o BL	v	v	v	
CS12 KBD w BL		v	v	
CS09			v	v

Parts#	parts	case1	case2	case3	case4
R108	10k_5%_0402	DY	DY	ASM	ASM
R104	0_5%_0402	ASM	ASM	DY	DY
R89	100k_5%_0402	ASM	ASM	ASM	ASM
R105	1k_5%_0402	ASM	DY	ASM	ASM
R111	10k_5%_0402	ASM	ASM	ASM	ASM
R110	10k_5%_0402	DY	ASM	ASM	ASM
R92	0_5%_0402	DY	DY	ASM	ASM
R91	390_5%_0402	DY	ASM	ASM	ASM
R107	0_5%_0402	DY	ASM	DY	DY
R103	0_5%_0402	ASM	ASM	DY	DY
R106	0_5%_0402	DY	ASM	ASM	ASM
R112	0_5%_0402	DY	ASM	DY	DY
R109	100k_5%_0402	DY	ASM	ASM	DY
R191	0_5%_0402	ASM	ASM	DY	ASM
R217	0_5%_0402	DY	DY	ASM	DY
R300	0_5%_0402	DY	DY	ASM	ASM
C16	47uF_6.3V_0805	DY	DY	DY	DY
C15	10uF_6.3V_0603	DY	ASM	ASM	DY
C14	10uF_6.3V_0603	DY	ASM	ASM	DY
Q1	LTC015TE	DY	DY	ASM	ASM
Q8	LTA015TE	DY	DY	ASM	DY
Q7	RW1E015RP	DY	DY	ASM	DY
F5	1.5A/6A Fuse	DY	ASM	DY	DY
	1.5A/6V PolySW	DY	DY	ASM	DY
R314	100K_5%_0402	DY	ASM	DY	DY

KBD Table2

Pre-DV	case4
SDV	case2
FVT	case2
PreSIT	case2
SIT	case2

← Logic

<Core Design>

緯創資通 Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title: KEYBOARD CONNECTOR

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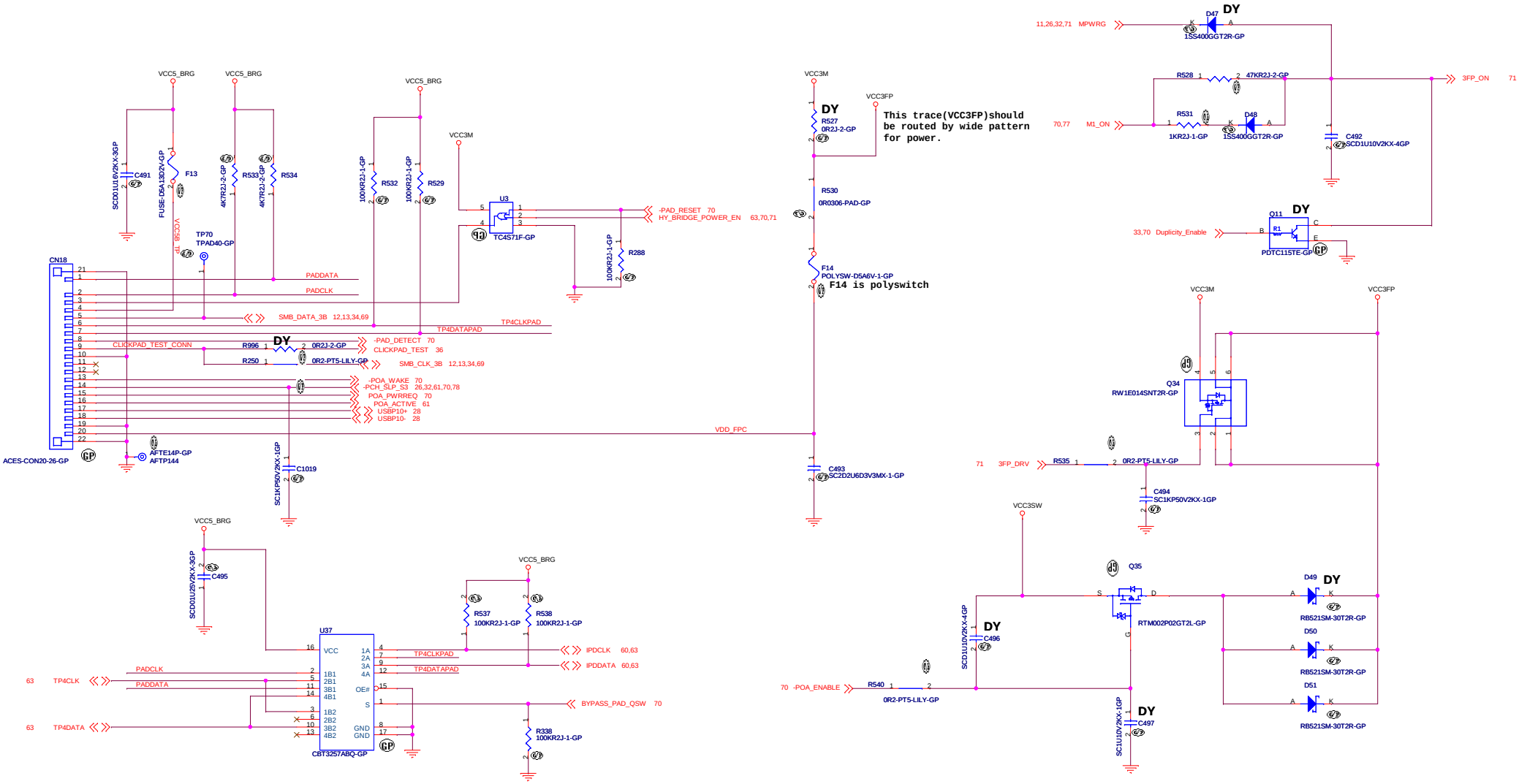
Fingerprint Reader / Touch PAD



Near CN18

PADDATA	1	AFTE14P-GP AFTP124
PADCLK	1	AFTE14P-GP AFTP125
-PAD_RESET	1	AFTE14P-GP AFTP126
VCCSB_TP	1	AFTE14P-GP AFTP127
TP4CLKPAD	1	AFTE14P-GP AFTP128
TP4DATAPAD	1	AFTE14P-GP AFTP129
-PAD_DETECT	1	AFTE14P-GP AFTP130
CLICKPAD_TEST_CONN	1	AFTE14P-GP AFTP131
-POA_WAKE	1	AFTE14P-GP AFTP132
-PCH_SLP_S3	1	AFTE14P-GP AFTP133
POA_PWRREQ	1	AFTE14P-GP AFTP134
POA_ACTIVE	1	AFTE14P-GP AFTP135
VDD_FPC	1	AFTE14P-GP AFTP136

<Core Design>

Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title: **TOUCH PAD CONNECTOR**

Size	Document Number	Rev
Custom	Dasher-2	-1

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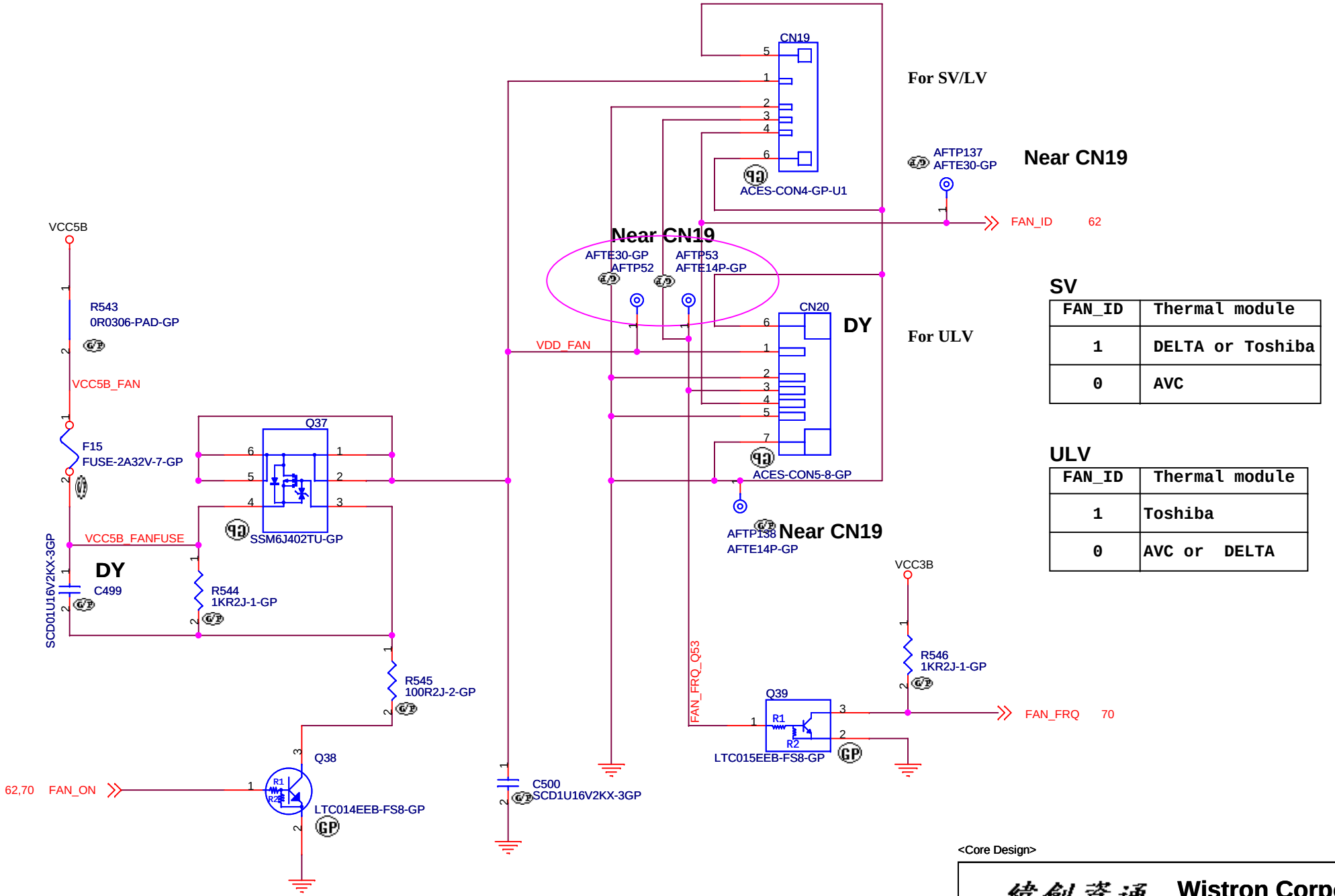
<Core Design>

緯創資通 **Wistron Corporation**
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title: ***BLANK***		
Size: A	Document Number: **Dasher-2**	Rev: **-1**
Date: Tuesday, February 21, 2012	Sheet 65 of 101	



SV

FAN_ID	Thermal module
1	DELTA or Toshiba
0	AVC

ULV

FAN_ID	Thermal module
1	Toshiba
0	AVC or DELTA

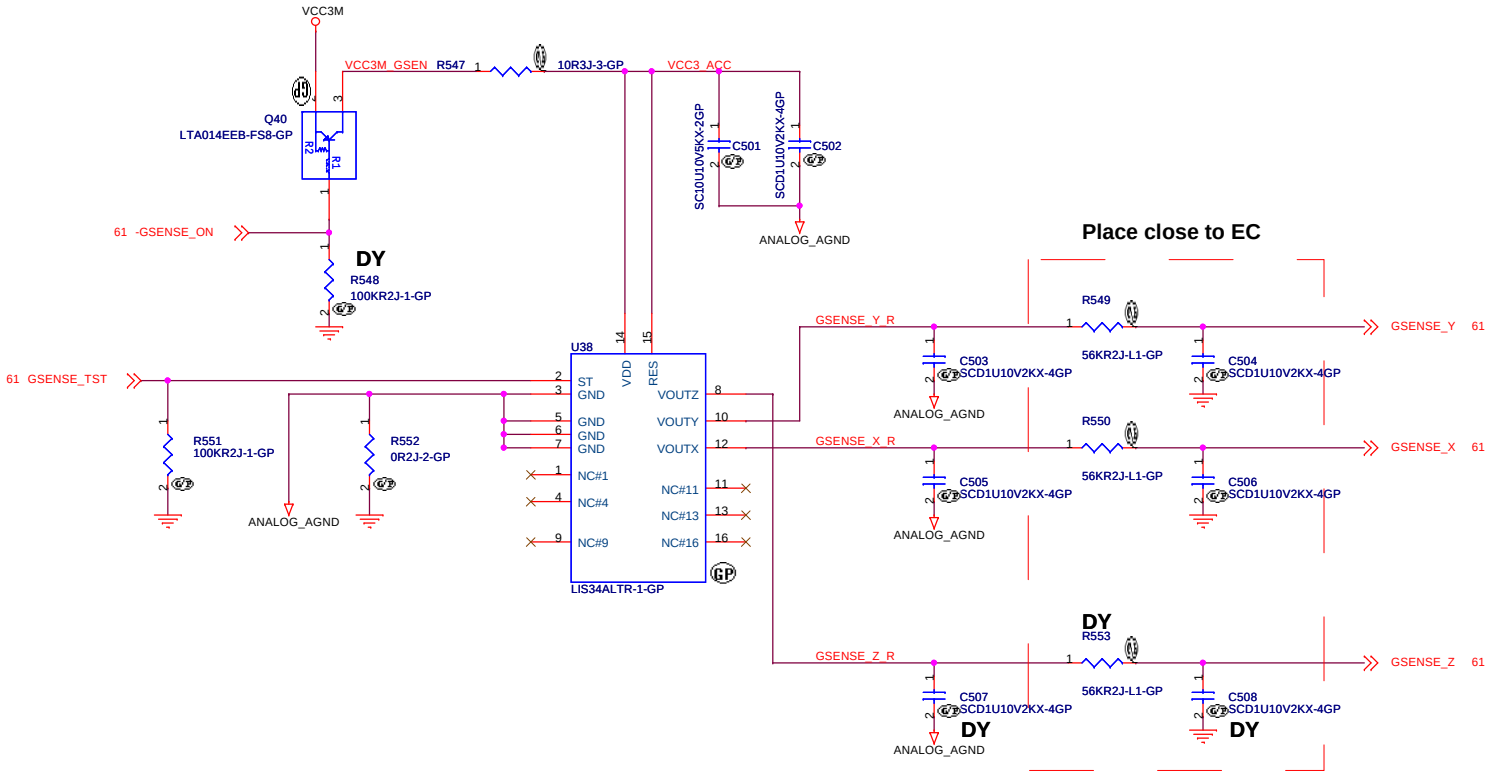
<Core Design>

緯創資通 **Wistron Corporation**
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title: ***FAN Control***

Size	Document Number	Rev
A4	Dasher-2	-1

Date: Friday, March 09, 2012 Sheet 66 of 101



Width = 6 mil & Spacing = 10 mil for three Output traces

U38

Primary	**STMicro LIS34AL**	**41R0828AA**
Second	**Kionix KXTC8-2850**	**74.KXTC8.0BZ**
Third		

Layout Comment :

(1) Avoid routing under DCDC switching area.

<Core Design>

緯創資通 **Wistron Corporation**

21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title: ***G-SENSOR***

Size	Document Number	Rev
A3	Dasher-2	-1

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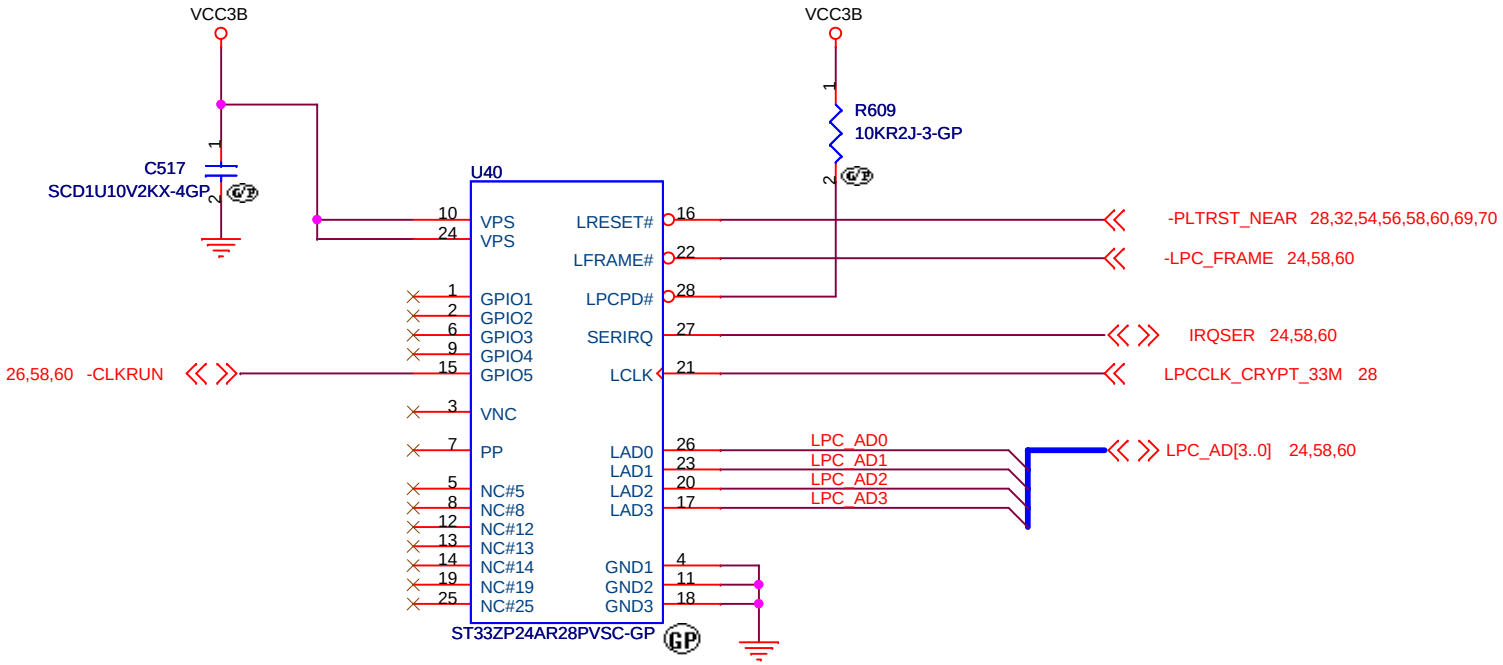

BU	After SDV
ST19NP18ER28PVMO (71.19N18.T0W)	ST33ZP24AR28PVxx xx="OG" for SDV(71.03324.A0W), "RC" for FVT, PreSIT (FW 1.2.C.0)(71.03324.C0W) SC for SIT (not PreSIT) FW 1.2.D.0(71.03324.D0W)

<Core Design>

緯創資通 **Wistron Corporation**
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title: **TPM**

Size	Document Number	Rev
A4	Dasher-2	-1

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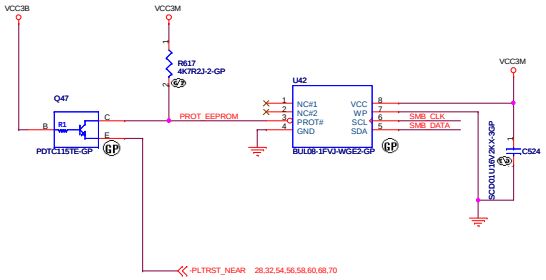
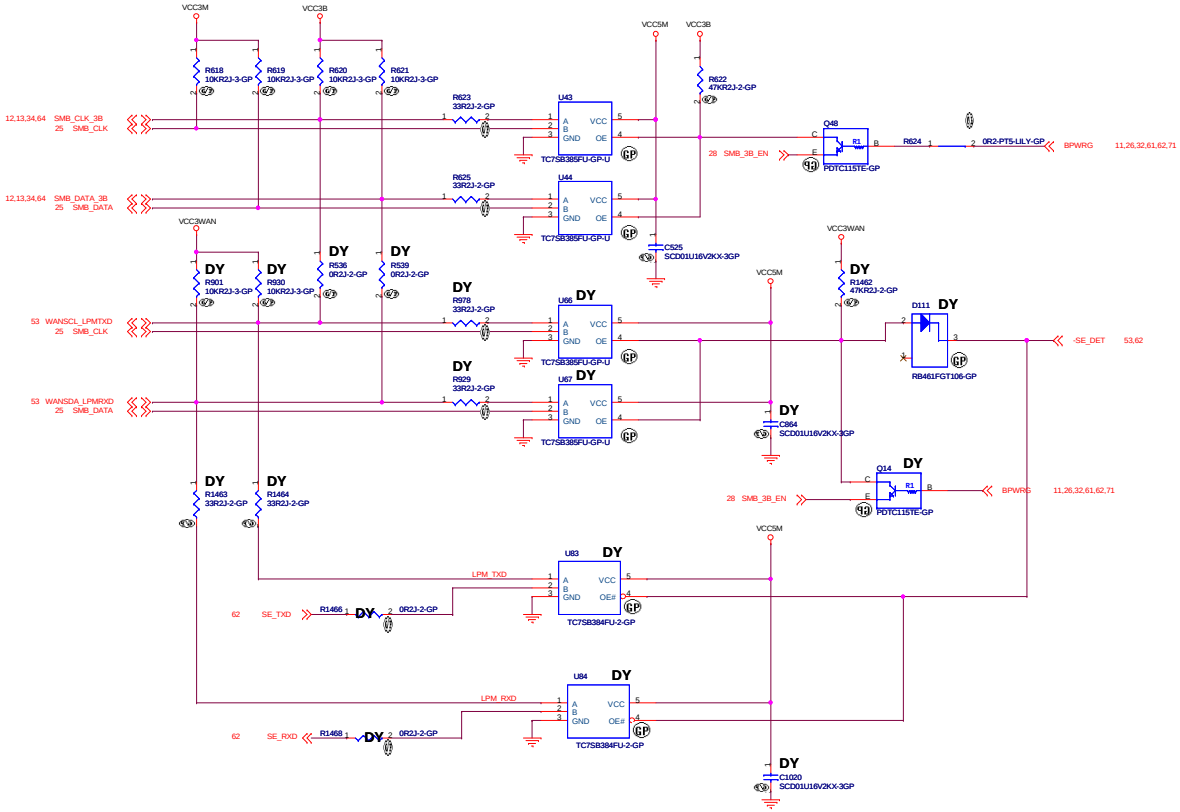
EEPROM



	Vendor	U42	Part Number
1st	ROHM	BUL08-1FVJ-W	72.BUL08.A0Q
2nd	NXP	PCA24S08ADP	72.24S08.A0Q
3rd	Sanyo	LE26CAP08TT	72.26C08.00R



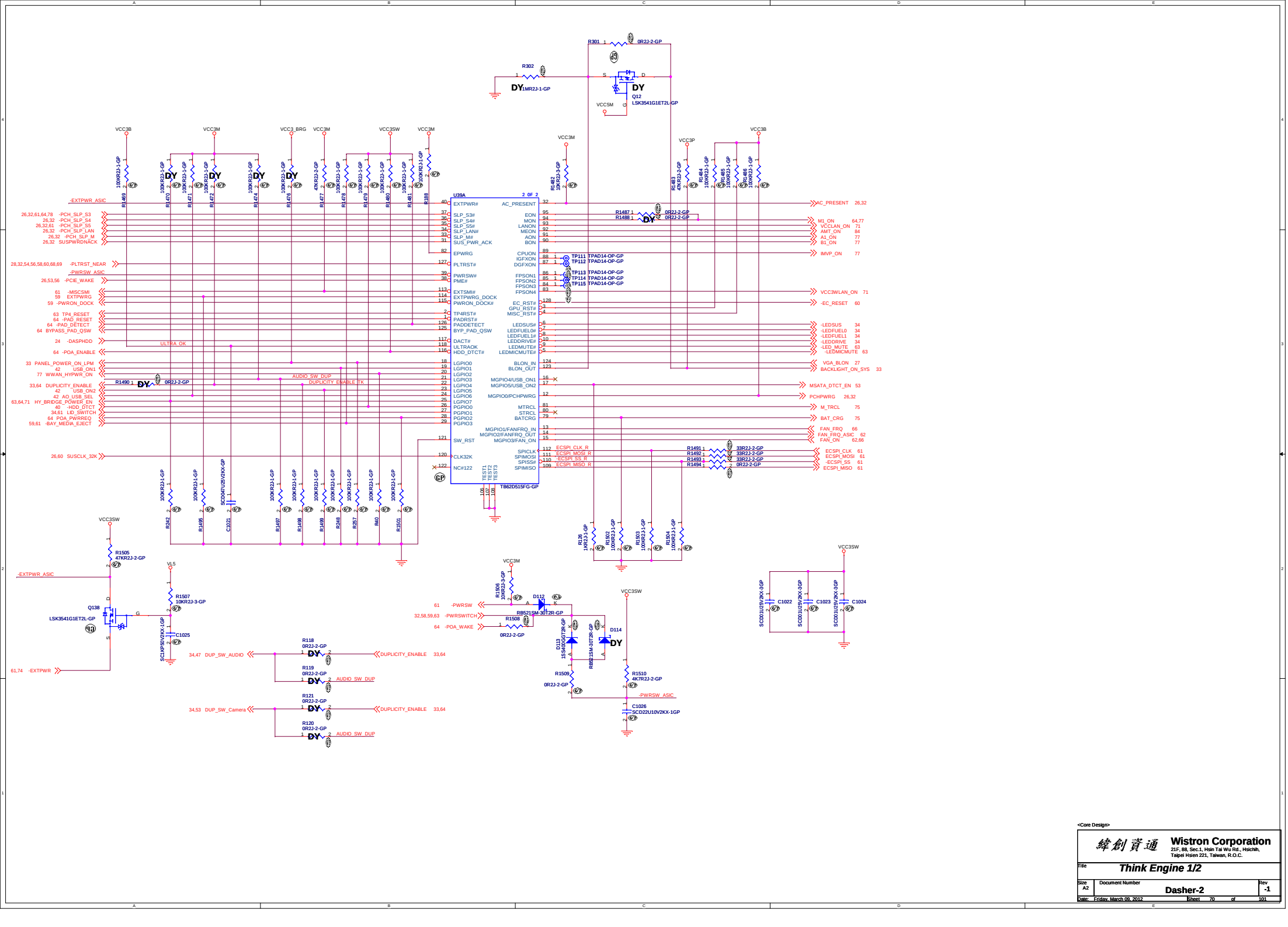
<Core Design>

緯創資通 **Wistron Corporation**
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title: **EEPROM/SMBUS SW**

Size	Document Number	Rev
Custom	Dasher-2	-1

Date: Friday, March 09, 2012 Sheet 69 of 101



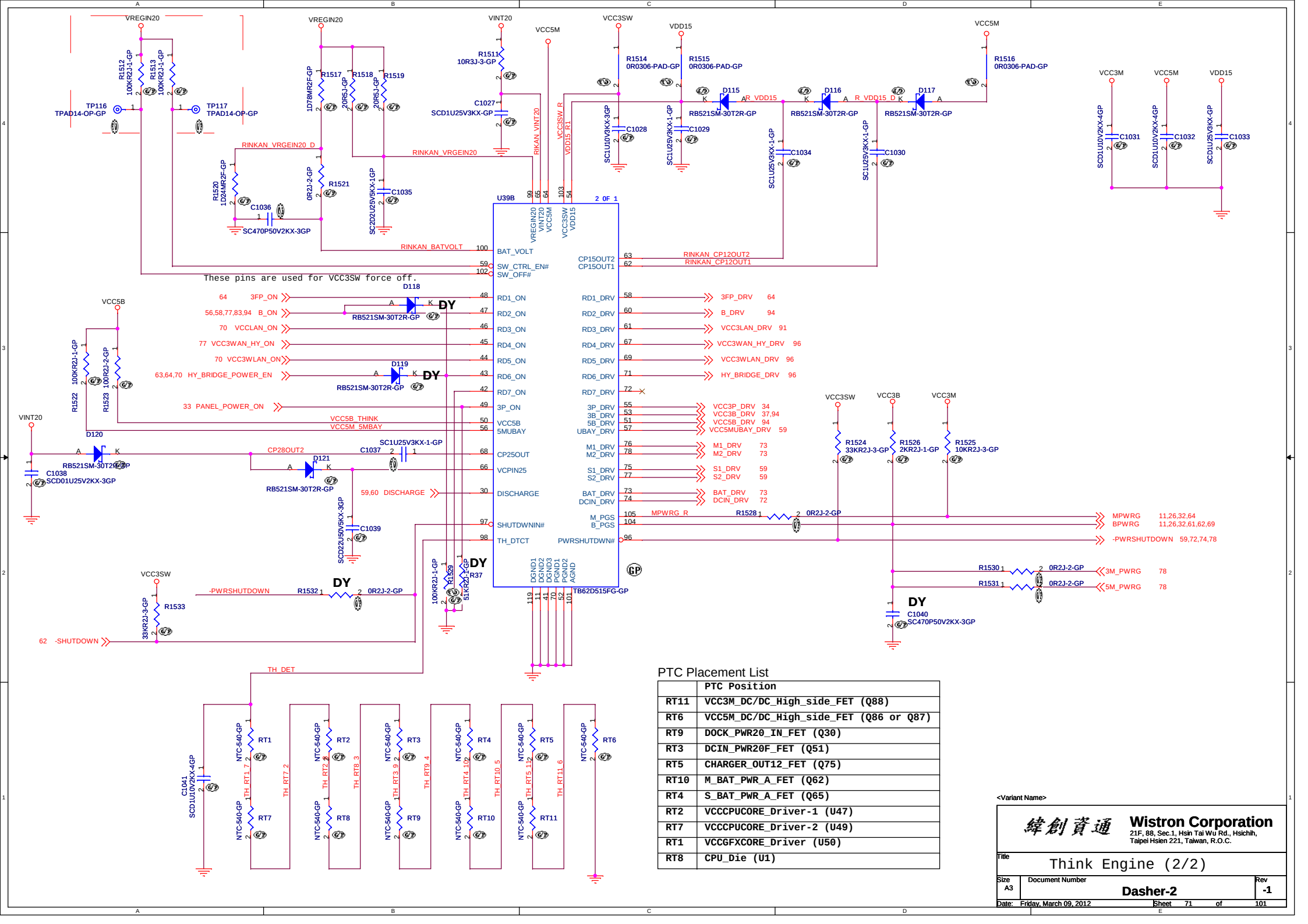
<Core Design>

Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title: Think Engine 1/2

Size: A2 | Document Number: Dasher-2 | Rev: -1

Date: Friday, March 09, 2012 | Sheet 70 of 101



PTC Placement List

	PTC Position
RT11	VCC3M_DC/DC_High_side_FET (Q88)
RT6	VCC5M_DC/DC_High_side_FET (Q86 or Q87)
RT9	DOCK_PWR20_IN_FET (Q30)
RT3	DCIN_PWR20F_FET (Q51)
RT5	CHARGER_OUT12_FET (Q75)
RT10	M_BAT_PWR_A_FET (Q62)
RT4	S_BAT_PWR_A_FET (Q65)
RT2	VCCCPUCORE_Driver-1 (U47)
RT7	VCCCPUCORE_Driver-2 (U49)
RT1	VCCGFXCORE_Driver (U50)
RT8	CPU_Die (U1)

<Variant Name>

緯創資通 Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title	Think Engine (2/2)	
Size A3	Document Number Dasher-2	Rev -1
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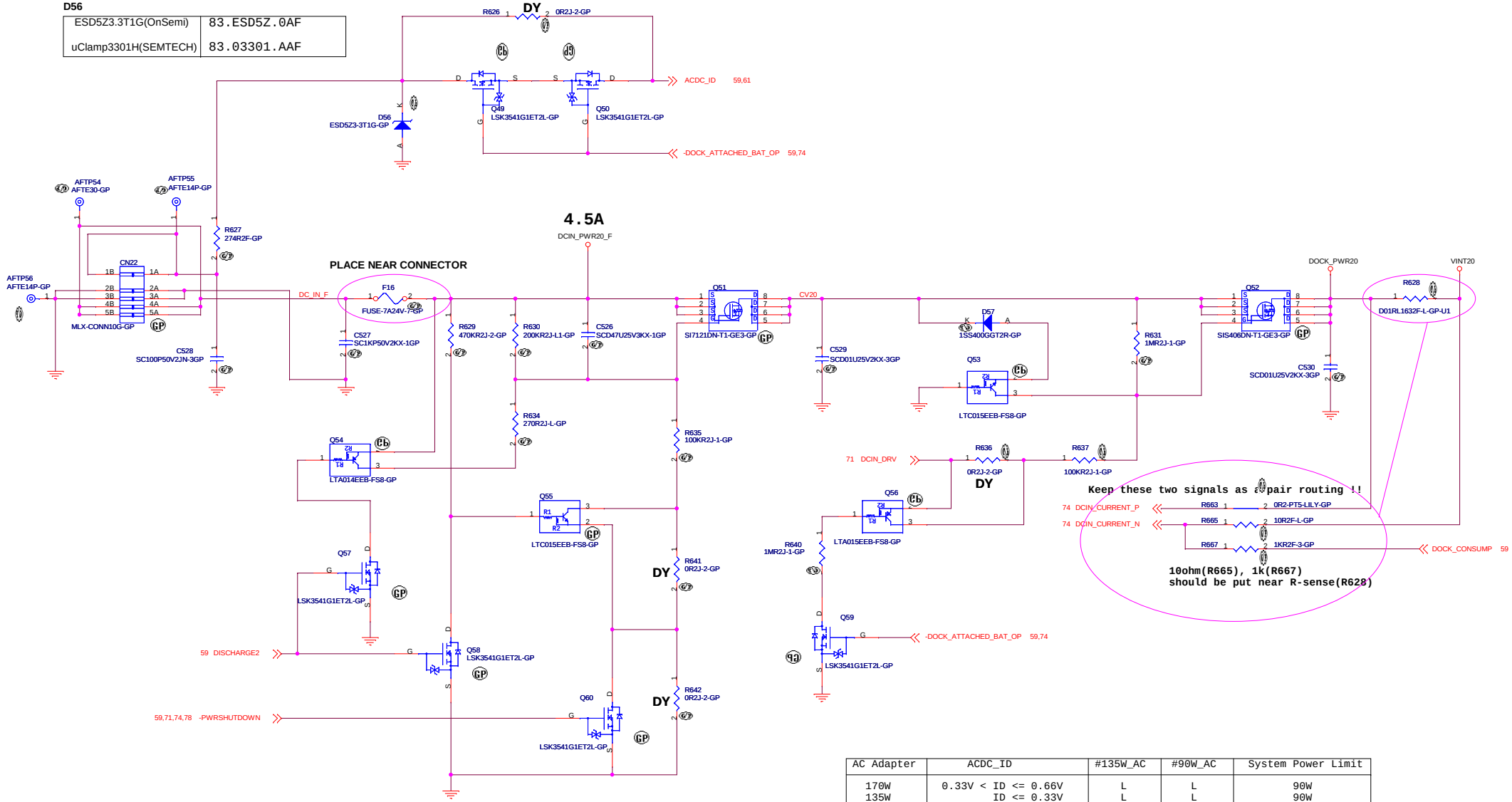
D56	
ESD5Z3.3T1G(OnSemi)	83.ESD5Z.0AF
uClamp3301H(SEMTECH)	83.03301.AAF



AC Adapter	ACDC_ID	#135W_AC	#90W_AC	System Power Limit
170W	0.33V < ID <= 0.66V	L	L	90W
135W	ID <= 0.33V	L	L	90W
90W	2.64V < ID	H	L	90W
65W	1.32V < ID <= 1.98V	H	H	65W

PEAK SHIFT	YES	NO
R641	NO-ASM	ASM
R629	ASM	NO-ASM
Q58	ASM	NO-ASM
Q55	ASM	NO-ASM

Logic

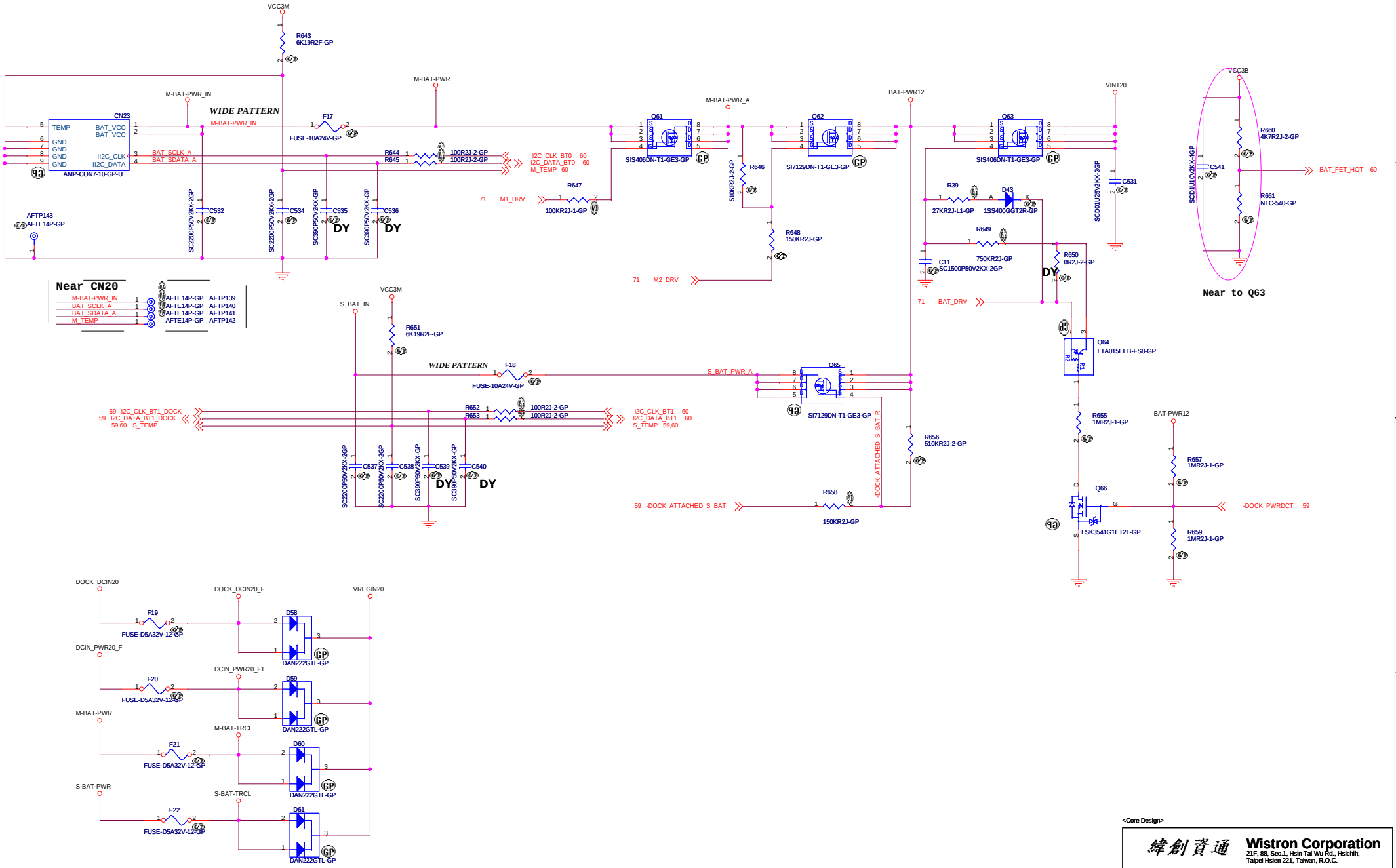
<Core Design>

緯創資通 **Wistron Corporation**
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title: ***DC-IN***

Size	Document Number	Rev
Custom	Dasher-2	-1

Date: Friday, March 09, 2012 Sheet 72 of 101



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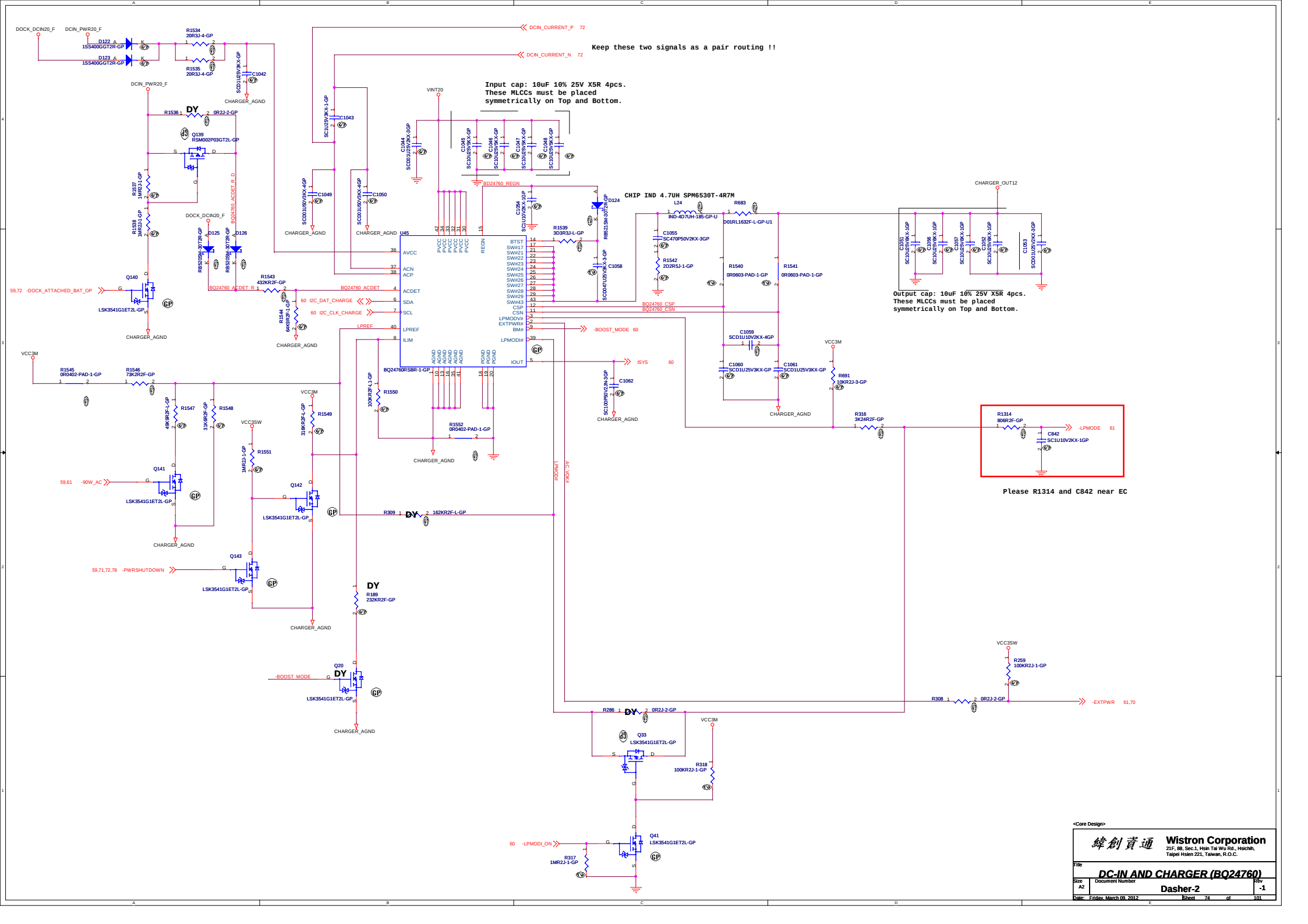
緯創資通 Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title: BATTERY INPUT

Size	Document Number	Rev
Custom	Dasher-2	-1

Date: Friday, March 09, 2012 Sheet 73 of 101



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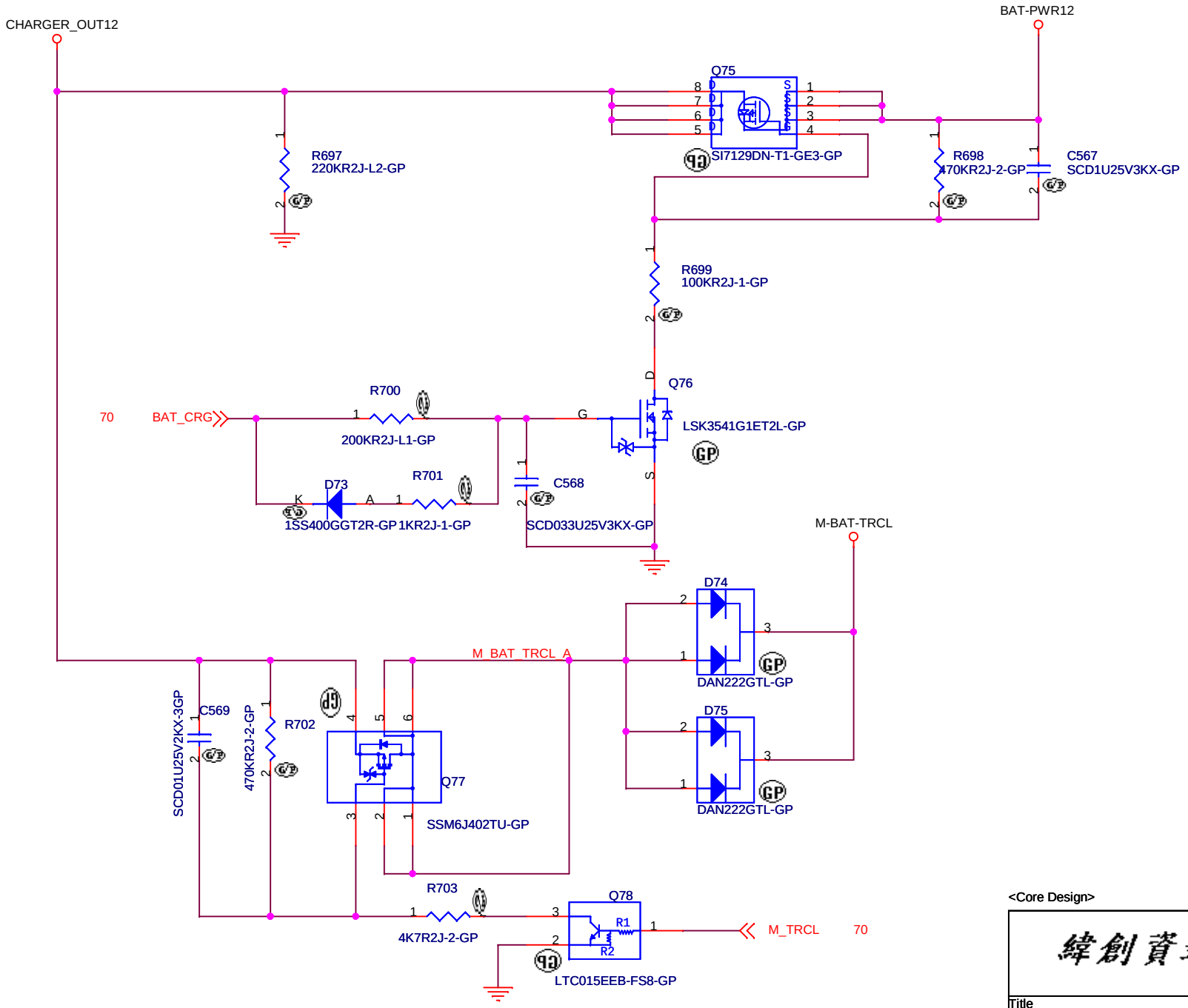
Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title: DC-IN AND CHARGER (BQ24760)

Size	Document Number	Rev
A2	Dasher-2	-1

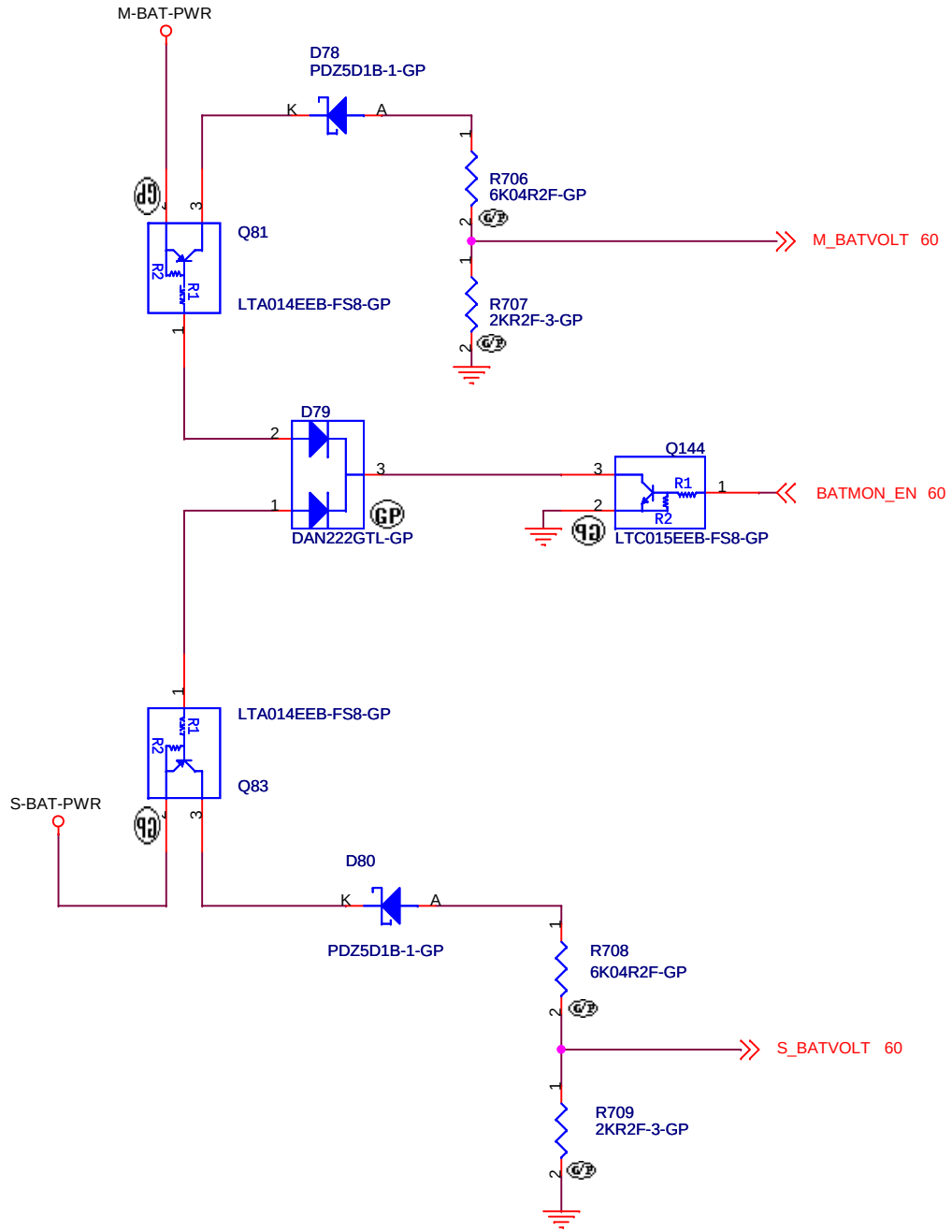
Date: Friday, March 09, 2012 Sheet 74 of 101



<Core Design>

緯創資通	**Wistron Corporation** 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title	***CHARGER SELECT***	
Size A4	Document Number **Dasher-2**	Rev **-1**
Date: Friday, March 09, 2012	Sheet 75 of 101	

VOUT = 0.249 (VBAT - 5)



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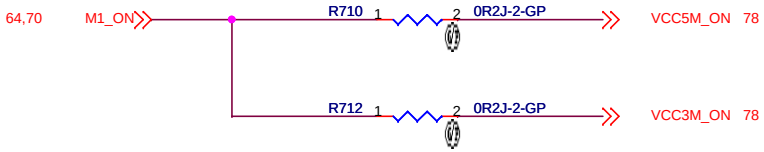
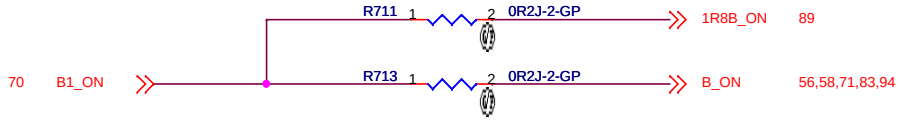
緯創資通 **Wistron Corporation**
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

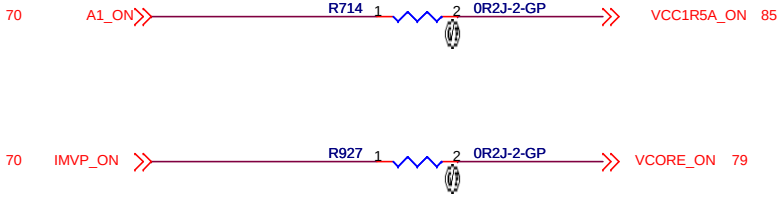
Title: ***BATTERY MONITOR***

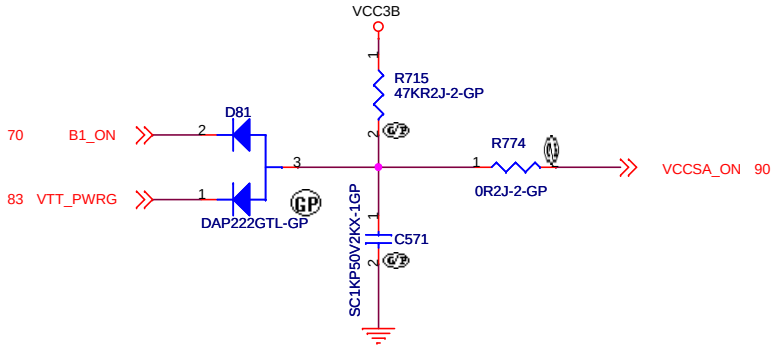
Size	Document Number	Rev
A4	Dasher-2	-1

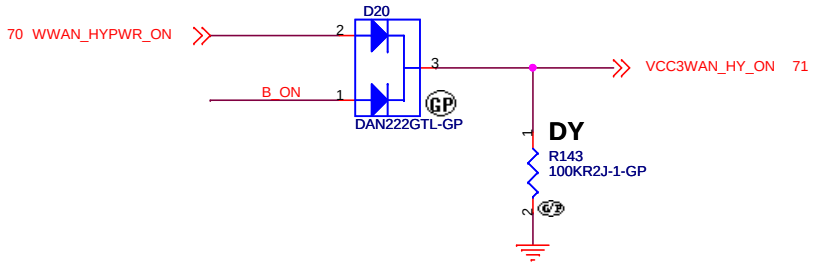
Date: Friday, March 09, 2012 Sheet 76 of 101

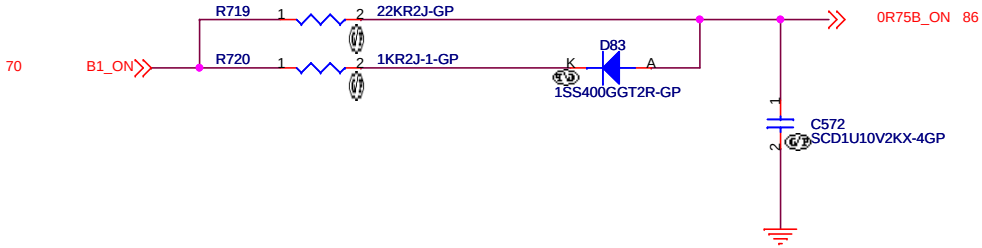












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D20 R143	ASM DY	ASM DY	ASM DY	DY ASM

Logic

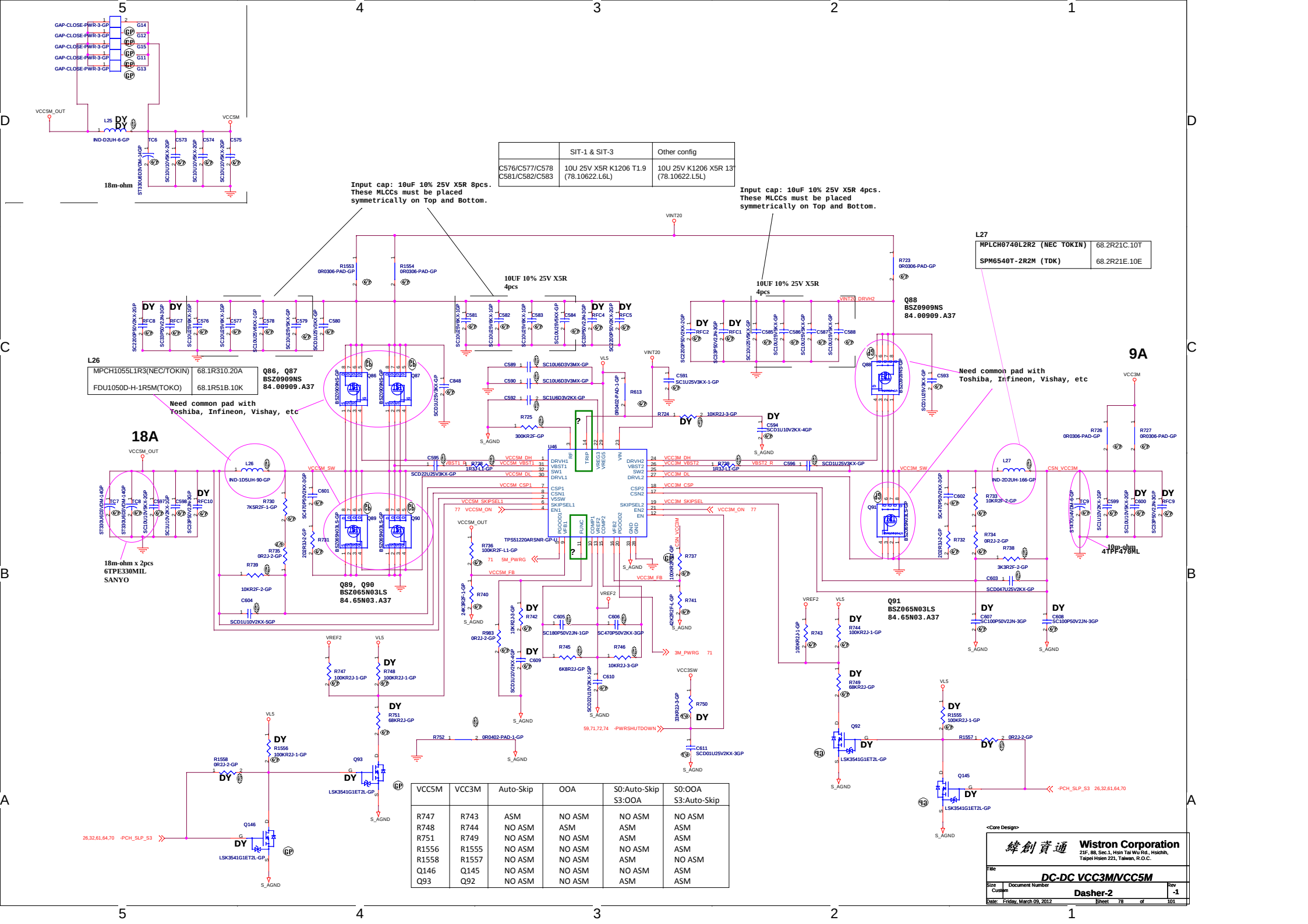
<Core Design>

緯創資通 **Wistron Corporation**
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title: ***POWER SEQUENCE***

Size	Document Number	Rev
Custom	Dasher-2	-1

Date: Friday, March 09, 2012 Sheet 77 of 101



	SIT-1 & SIT-3	Other config
C576/C577/C578 C581/C582/C583	10U 25V X5R K1206 T1.9 (78.10622.L6L)	10U 25V K1206 X5R 13" (78.10622.L5L)

L26	
MPCH1055L1R3(NEC/TOKIN)	68.1R310.20A
FDU1050D-H-1R5M(TOKO)	68.1R51B.10K

L27	
MPLCH0740L2R2 (NEC TOKIN)	68.2R21C.10T
SPM6540T-2R2M (TDK)	68.2R21E.10E

VCC5M	VCC3M	Auto-Skip	OOA	S0:Auto-Skip S3:OOA	S0:OOA S3:Auto-Skip
R747	R743	ASM	NO ASM	NO ASM	NO ASM
R748	R744	NO ASM	ASM	ASM	ASM
R751	R749	NO ASM	NO ASM	ASM	ASM
R1556	R1555	NO ASM	NO ASM	NO ASM	ASM
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Q146	Q145	NO ASM	NO ASM	NO ASM	ASM
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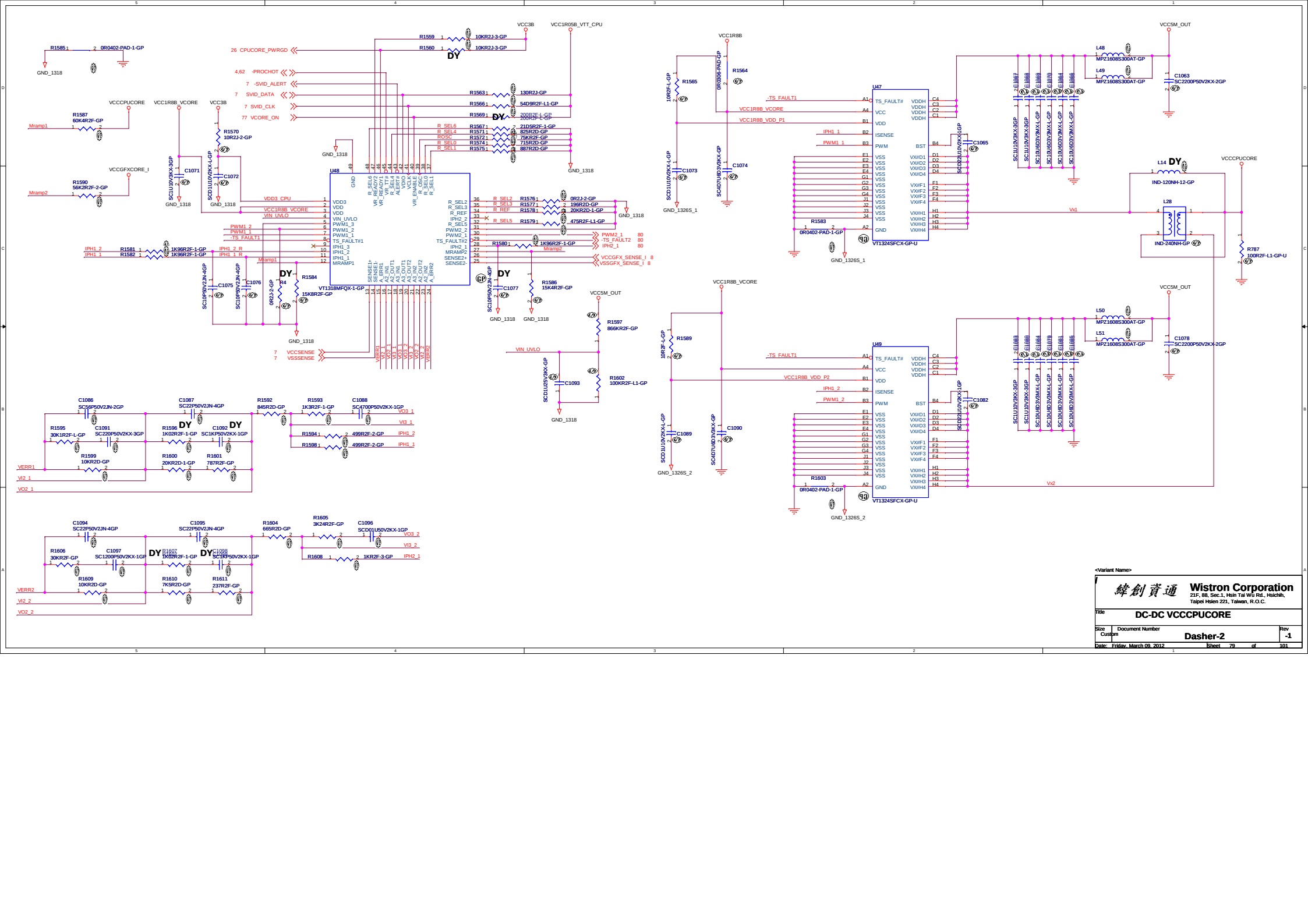

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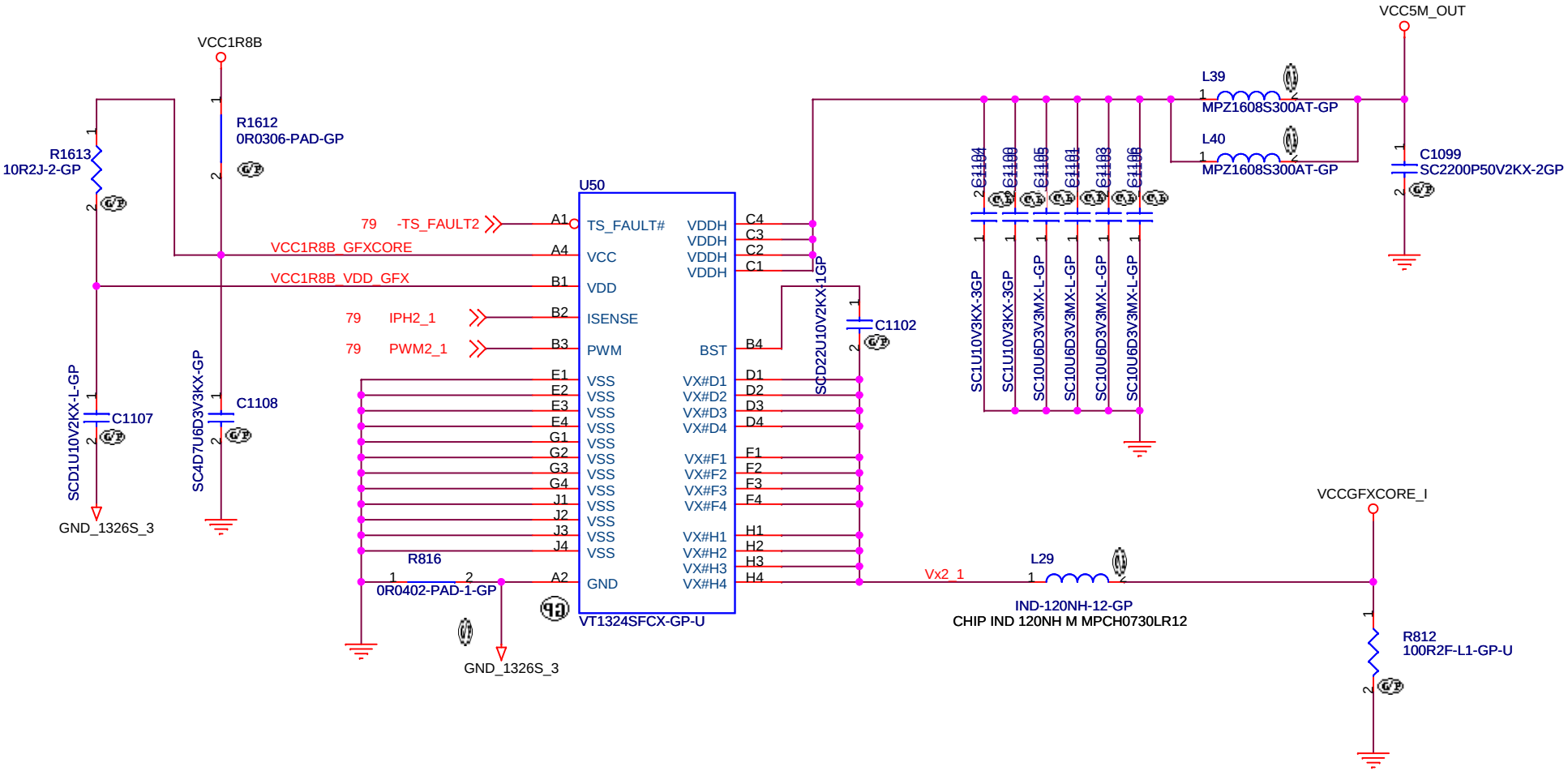
緯創資通	Wistron Corporation 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title	DC-DC VCCCPUCORE	
Size: Custom	Document Number: Dasher-2	Rev: -1
Date: Friday, March 09, 2012	Sheet 79 of 101	



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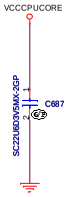
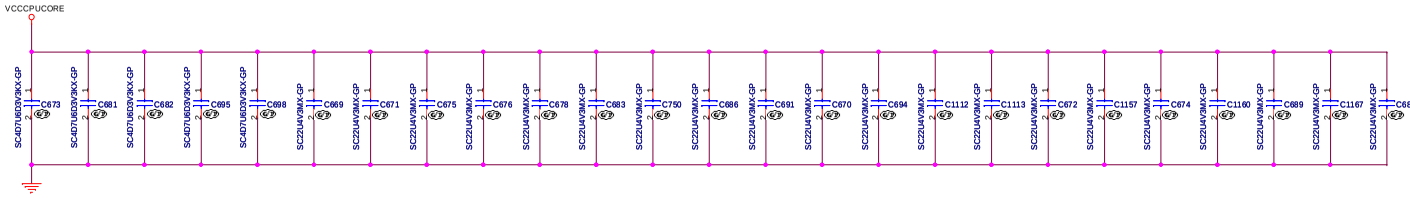
緯創資通 **Wistron Corporation**
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

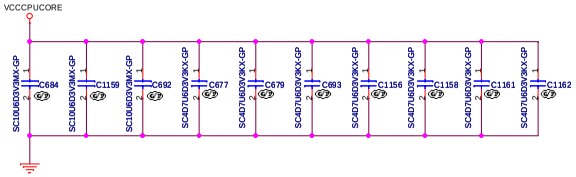
Title	**VCCGFXCORE**	
Size A4	Document Number **Dasher-2**	Rev **-1**
Date: Friday, March 09, 2012	Sheet 80 of 101	



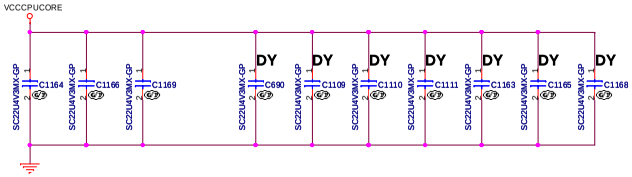
1 x 22uF_0805 MLCC for VCCCPUCORE



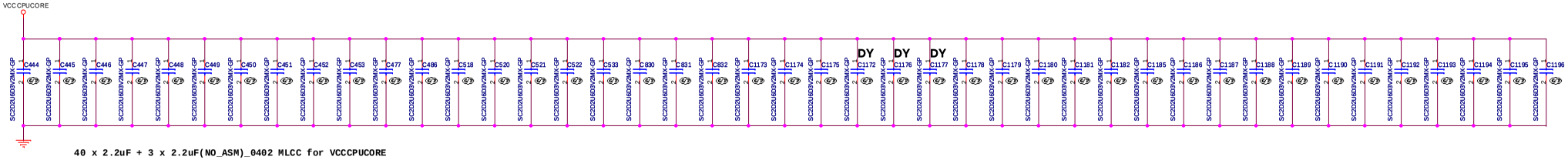
20 x 22uF + 5 x 4.7uF_0603 MLCC for VCCCPUCORE



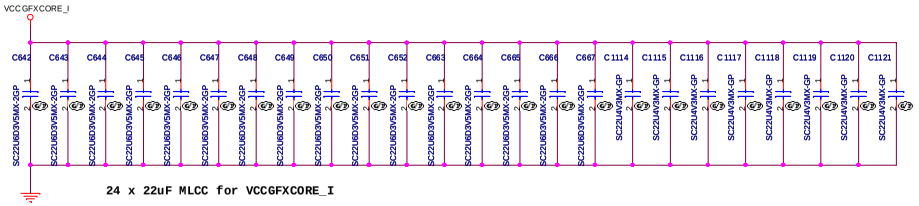
3 x 10uF + 7 x 4.7u_0603 MLCC for VCCCPUCORE



3 x 22uF + 7 x 22uF_0603 MLCC(No ASM) for VCCCPUCORE



40 x 2.2uF + 3 x 2.2uF(NO_ASM)_0402 MLCC for VCCCPUCORE



24 x 22uF MLCC for VCCGFXCORE_I

<Core Design>

Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title: DC-DC VCCCPUCORE

Size: A2 | Document Number: Dasher-2 | Rev: -1

Date: Thursday, March 01, 2012 | Sheet 81 of 101

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<Core Design>

緯創資通 **Wistron Corporation**
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

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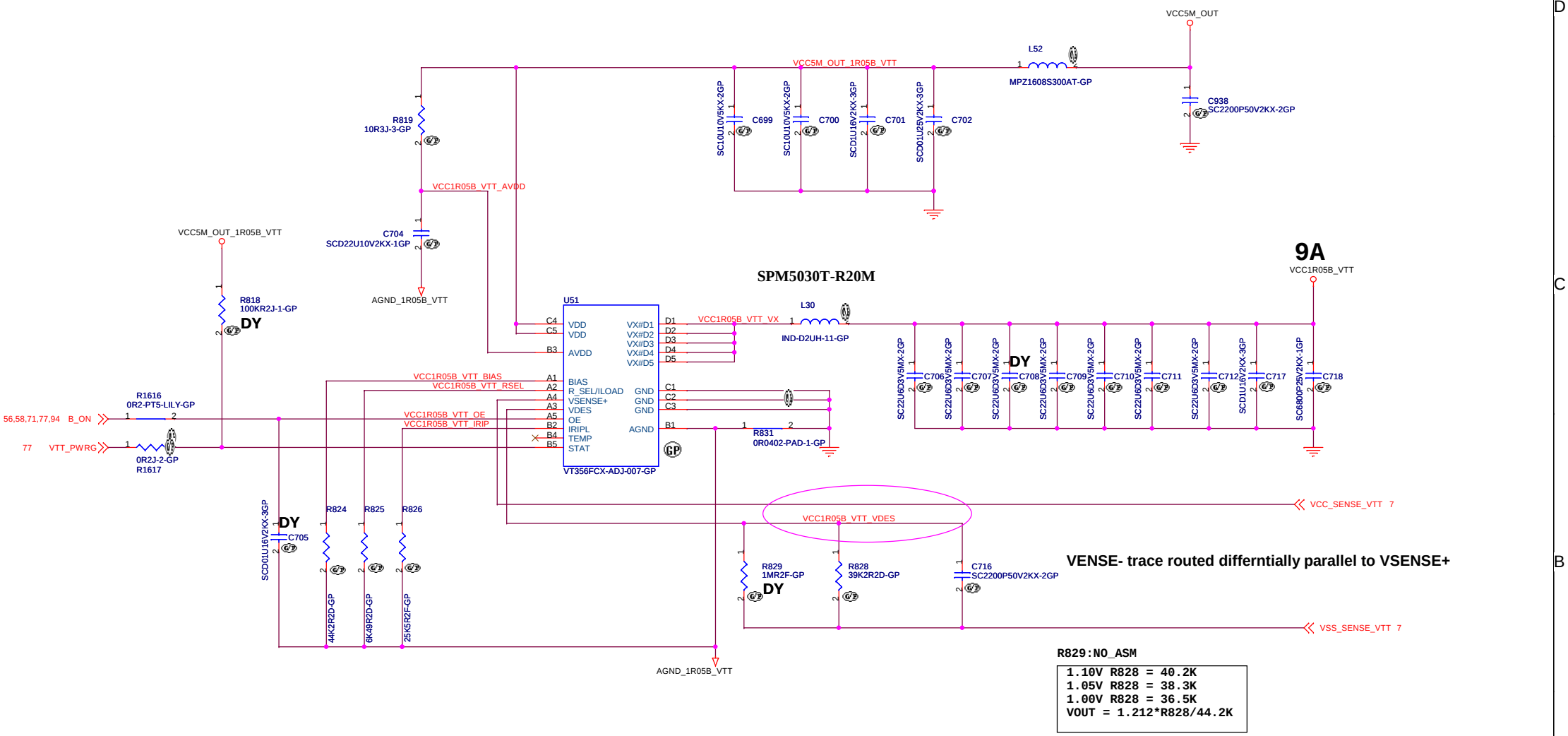
Date: Tuesday, February 21, 2012	Sheet 82 of 101	



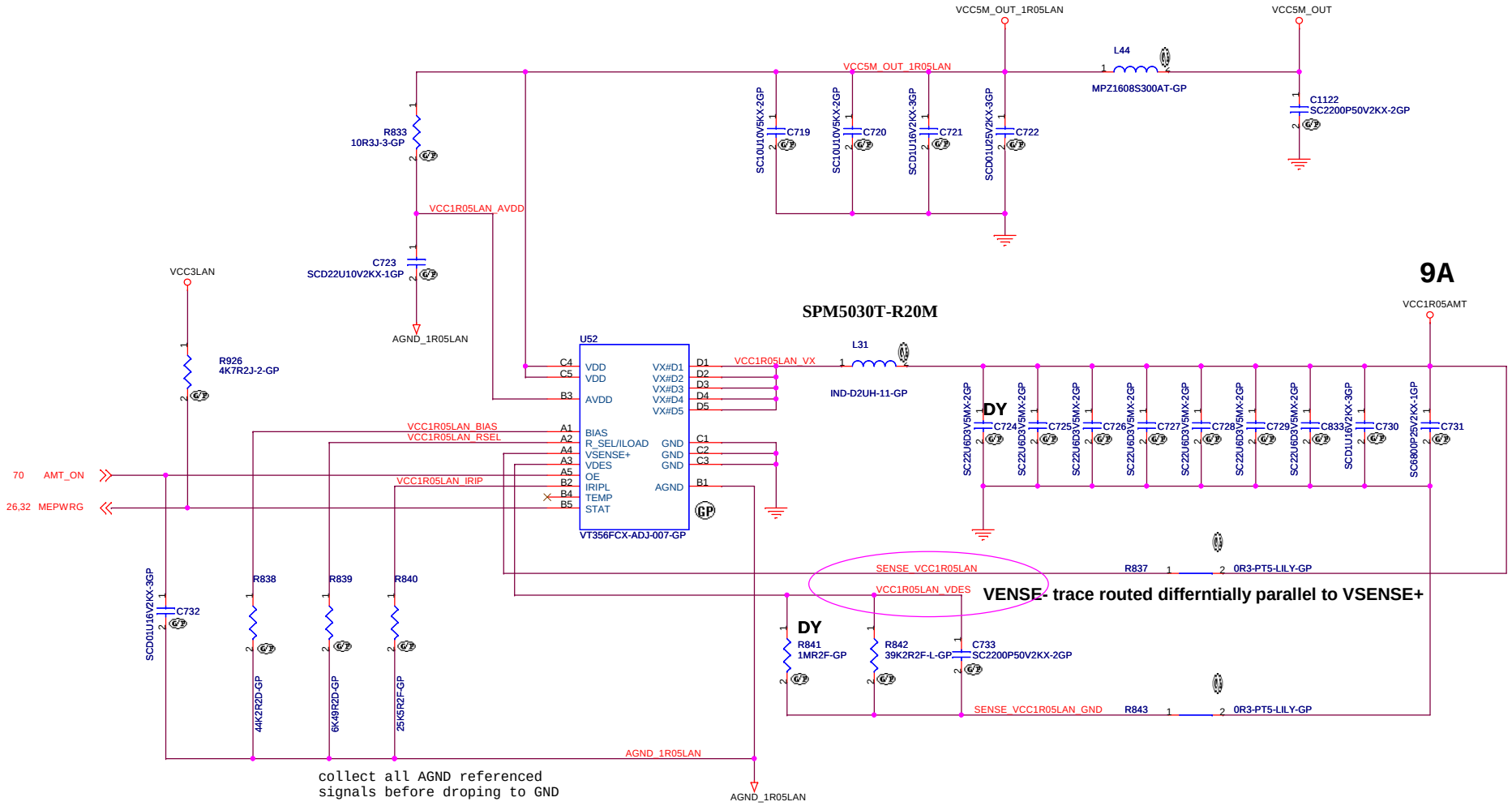
<Core Design>

Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title: DC-DC VCC1R05B_VTT

Size	Document Number	Rev
A3	Dasher-2	-1

Date: Friday, March 09, 2012 Sheet 83 of 101



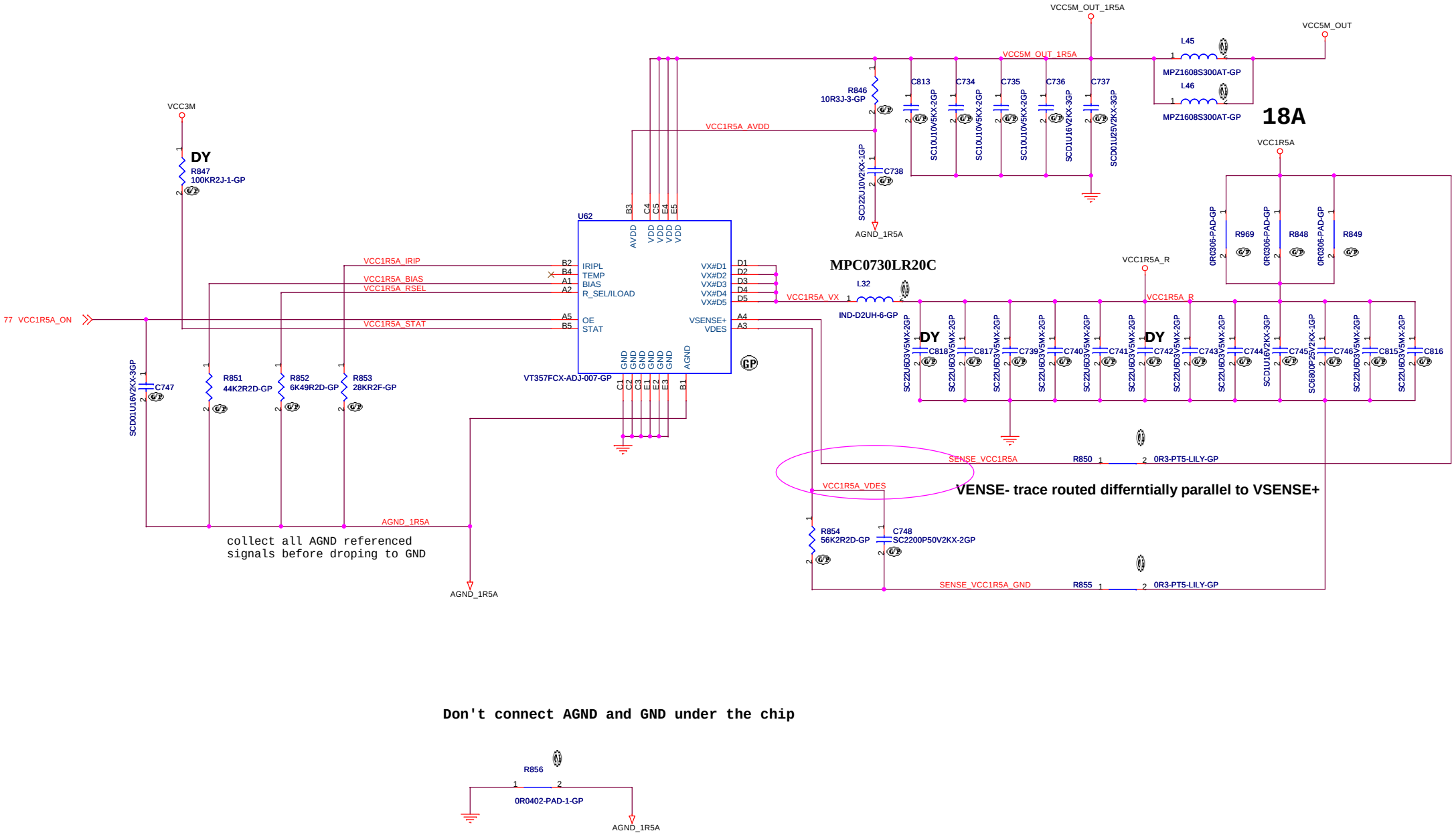
<Core Design>

Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title: DC-DC VCC1R05AMT

Size	Document Number	Rev
A3	Dasher-2	-1

Date: Friday, March 09, 2012 Sheet 84 of 101



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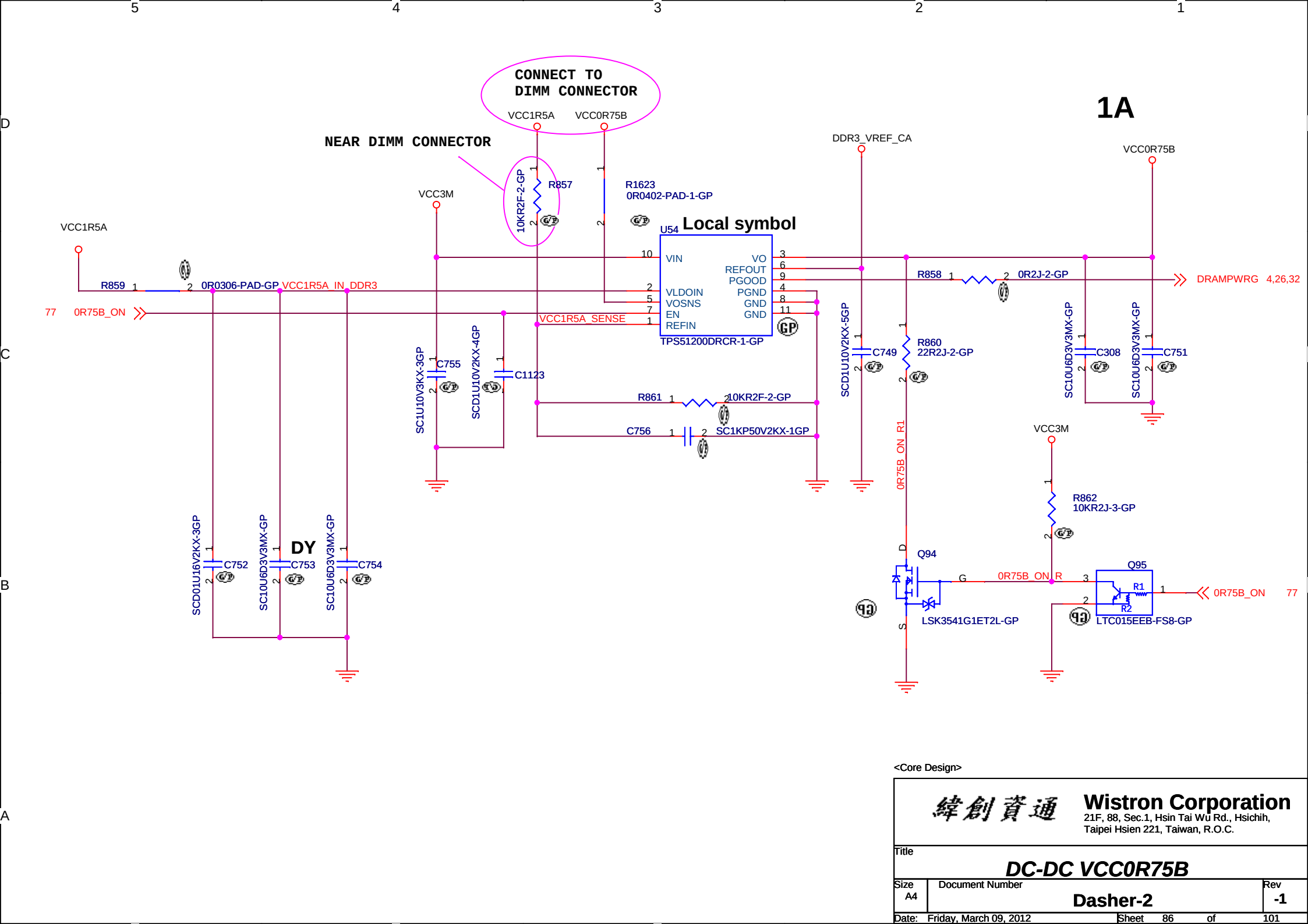
緯創資通 **Wistron Corporation**
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title: ***DC-DC VCC1R5A***

Size	Document Number	Rev
A3	Dasher-2	-1

Date: Friday, March 09, 2012 Sheet 85 of 101



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緯創資通 **Wistron Corporation**
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title	***DC-DC VCC0R75B***	
Size: A4	Document Number: **Dasher-2**	Rev: **-1**
Date: Friday, March 09, 2012	Sheet 86 of 101	

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21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

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Size A4	Document Number **Dasher-2**	Rev **-1**
Date: Tuesday, February 21, 2012	Sheet 87 of 101	

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<Core Design>

緯創資通 Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

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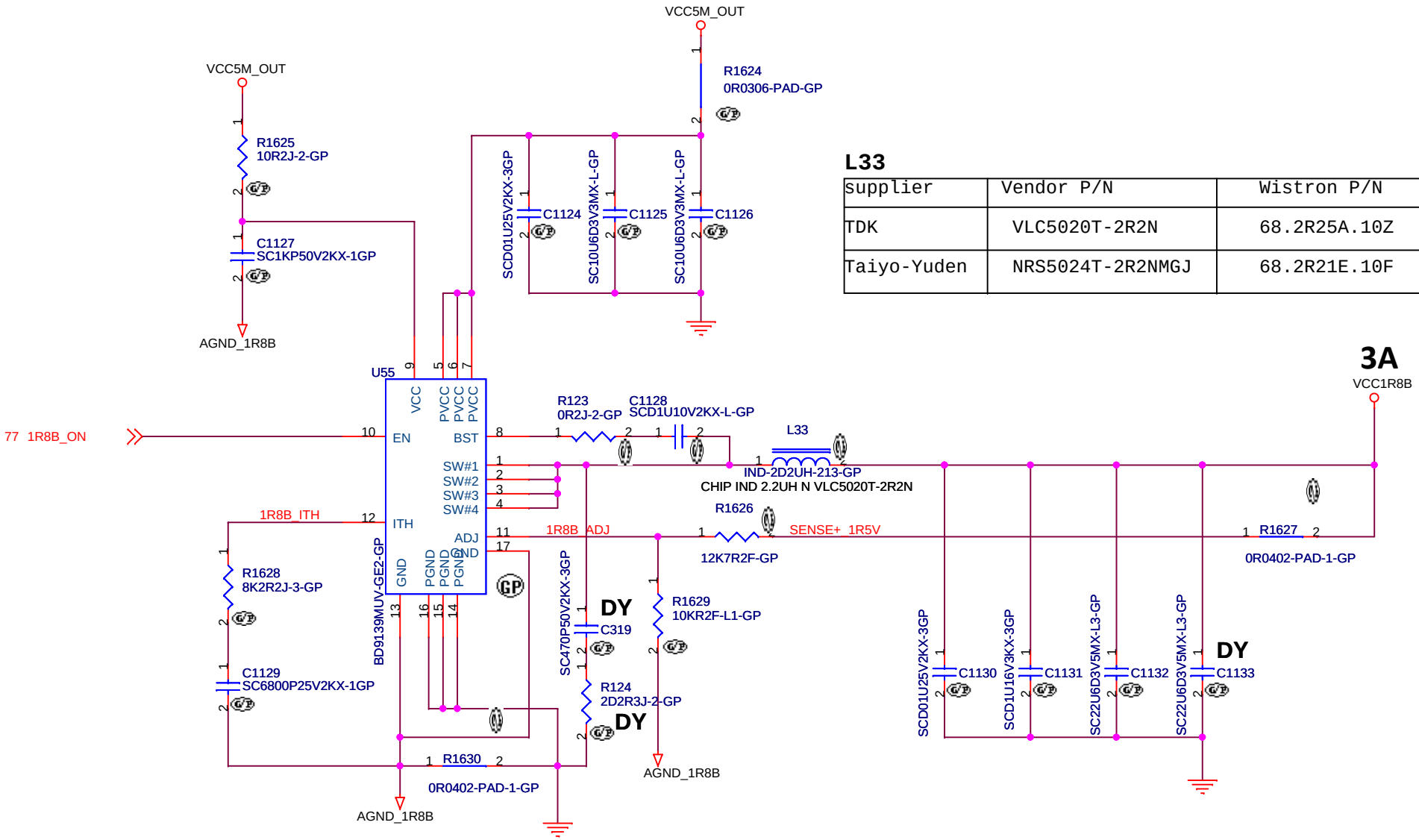
Date: Tuesday, February 21, 2012	Sheet 88 of 101	



L33

supplier	Vendor P/N	Wistron P/N
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Taiyo-Yuden	NRS5024T-2R2NMGJ	68.2R21E.10F

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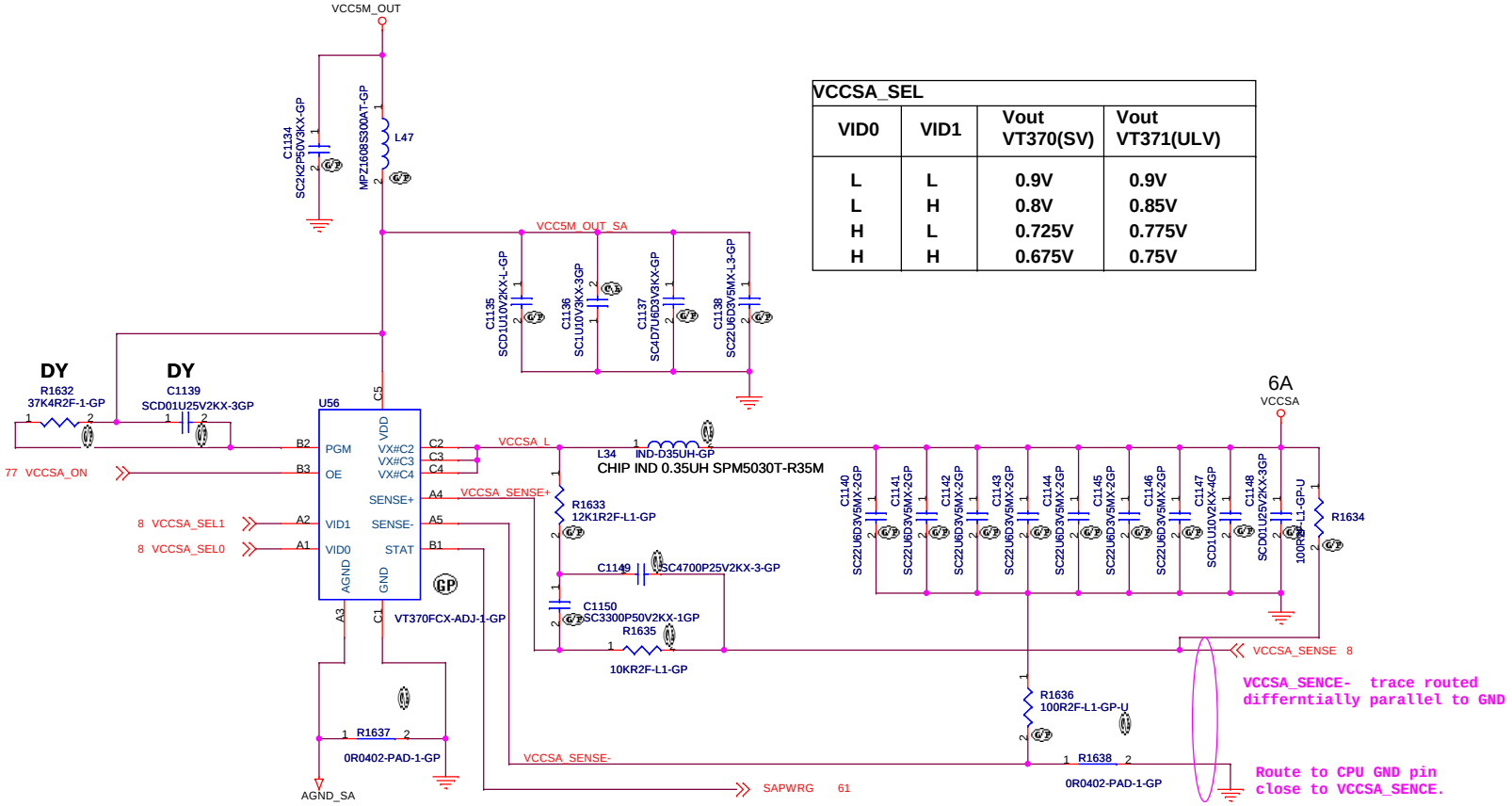
緯創資通 **Wistron Corporation**
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title: ***DC-DC VCC1R8B***

Size	Document Number	Rev
A4	Dasher-2	-1

Date: Friday, March 09, 2012 Sheet 89 of 101



VCCSA_SEL			
VID0	VID1	Vout VT370(SV)	Vout VT371(ULV)
L	L	0.9V	0.9V
L	H	0.8V	0.85V
H	L	0.725V	0.775V
H	H	0.675V	0.75V

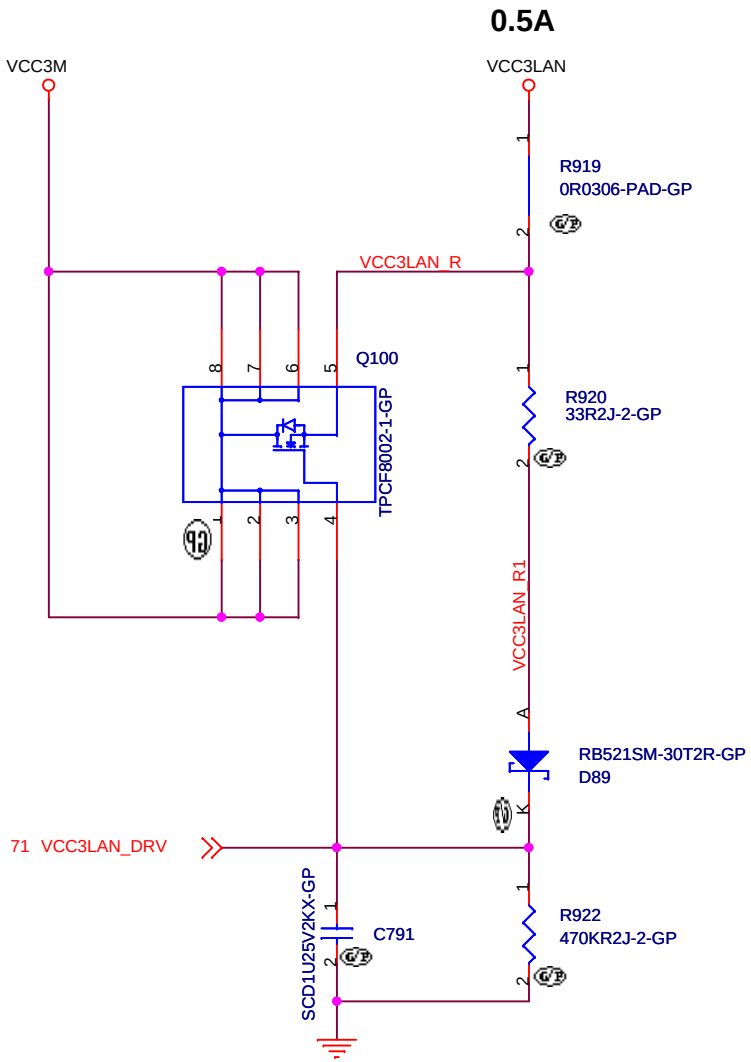
<Variant Name>

緯創資通 Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title: DC/DC VCCSA

Size	Document Number	Rev
A3	Dasher-2	-1

Date: Friday, March 09, 2012 Sheet 90 of 101



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緯創資通 Wistron Corporation 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.		
Title: ***LOAD SW LAN***		
Size: A4	Document Number: **Dasher-2**	Rev: **-1**
Date: Friday, March 09, 2012	Sheet 91 of 101	

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<Variant Name>

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21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

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Size: A4	Document Number: **Dasher-2**	Rev: **-1**
Date: Tuesday, February 21, 2012	Sheet 92 of 101	

5 4 3 2 1

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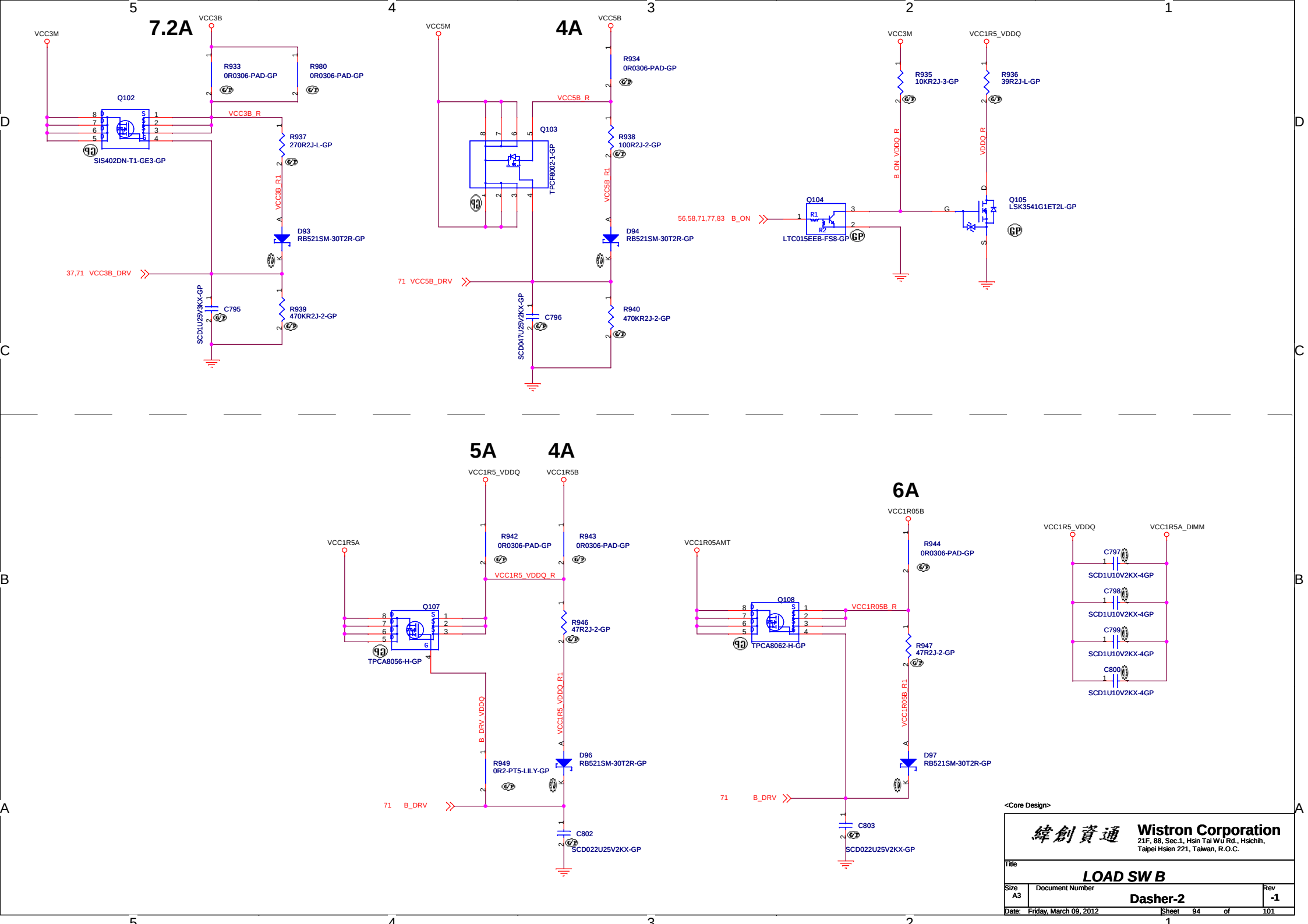
<Core Design>

緯創資通 **Wistron Corporation**
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title	***BLANK***	
Size A4	Document Number **Dasher-2**	Rev **-1**
Date: Tuesday, February 21, 2012	Sheet 93 of 101	



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Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title: LOAD SW B

Size	Document Number	Rev
A3	Dasher-2	-1

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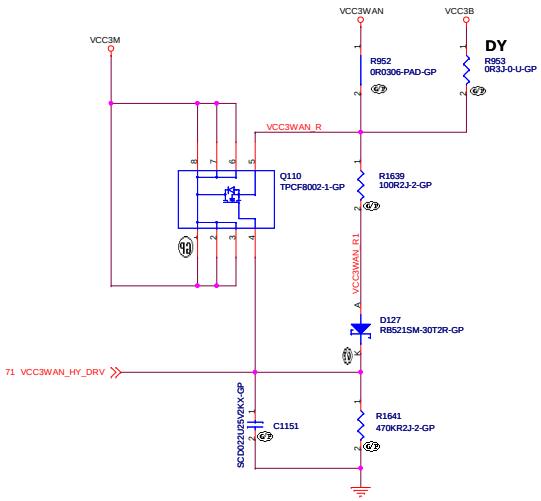
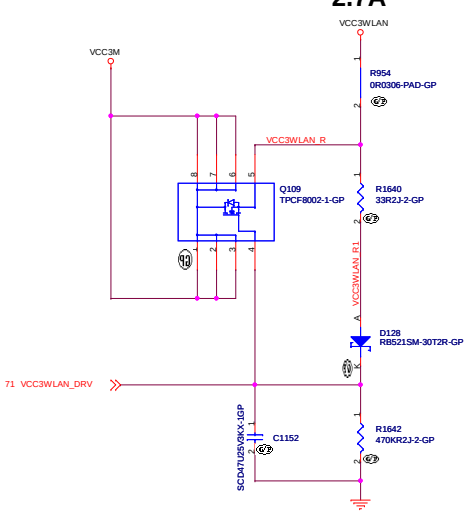
<Core Design>

緯創資通 **Wistron Corporation**
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title	***BLANK***	
Size A4	Document Number **Dasher-2**	Rev **-1**
Date: Tuesday, February 21, 2012	Sheet 95 of 101	





AOAC Duplicity	YES YES	YES No	No YES	No No
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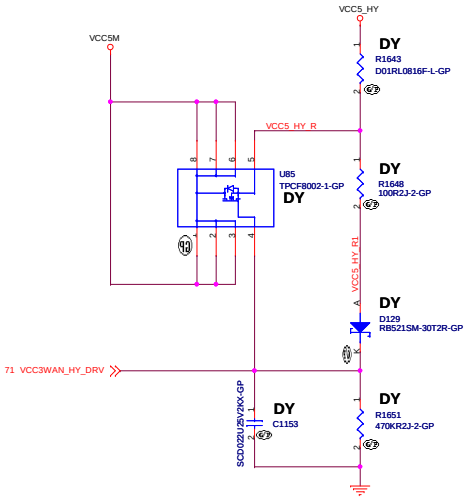
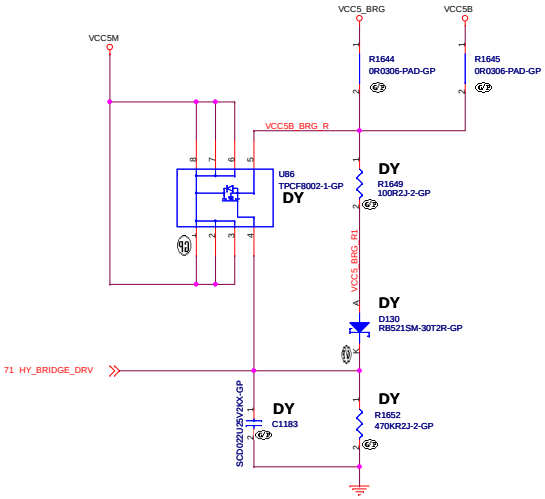
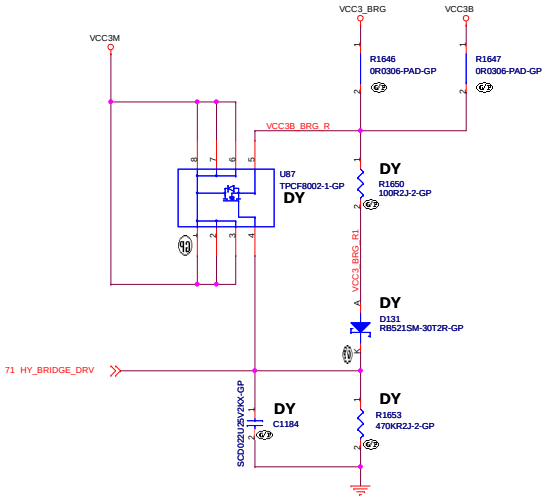
D127	ASM	ASM	ASM	DY
R1639	ASM	ASM	ASM	DY
R1641	ASM	ASM	ASM	DY
C1151	ASM	ASM	ASM	DY
R953	DY	DY	DY	ASM

Logic







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Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title: LOAD SW WWAN & WLAN

Size: Custom | Document Number: Dasher-2 | Rev: -1

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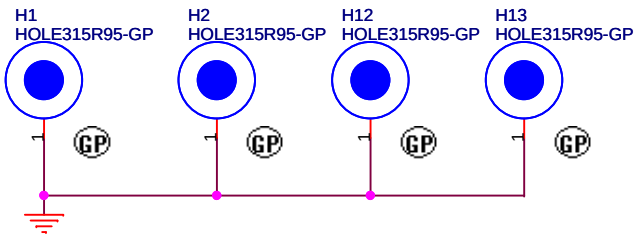
ZZ.00PAD.911



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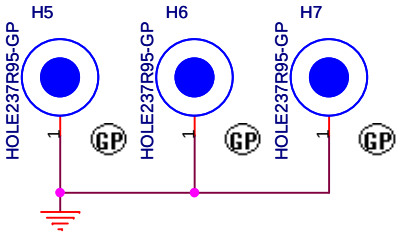

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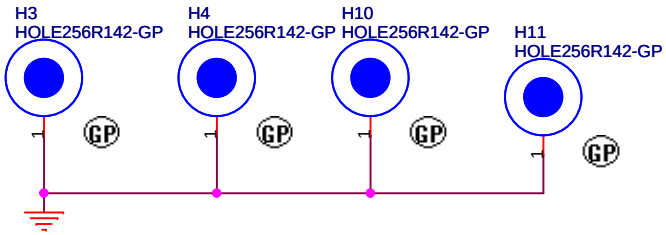

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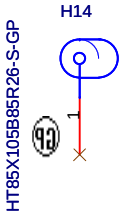

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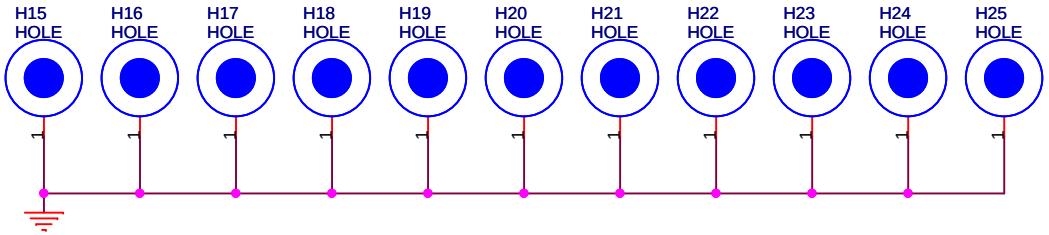
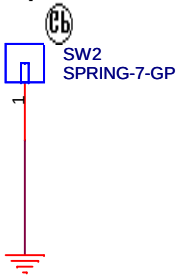
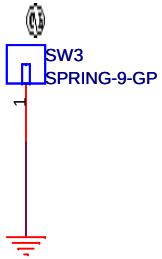


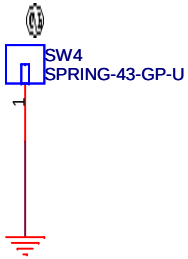
34.49U26.001
GND pad for docking connector



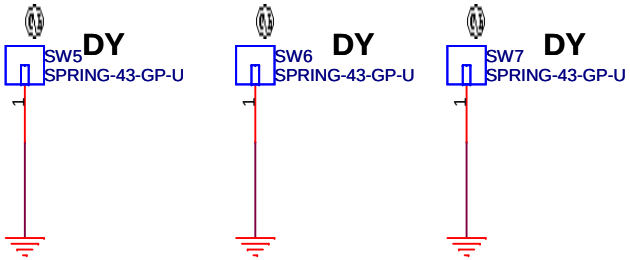
34.49U23.001
GND pad for docking connector



34.15J03.001
spring for EMI



34.15J03.001
spring for RF (SW5~SW7)



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緯創資通 **Wistron Corporation**
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Title: ***HOLES/GND/PADS***

Size	Document Number	Rev
A4	Dasher-2	-1

Date: Tuesday, February 21, 2012 Sheet 97 of 101

Page	Parts	LPM2.0	LPM1.1	Disable
29	R289	ASM	ASM	DY
	R260	DY	DY	ASM
33	U69	ASM	ASM	DY
	C942	ASM	ASM	DY
	C943	ASM	ASM	DY
	C944	ASM	ASM	DY
	C945	ASM	ASM	DY
	R1340	ASM	ASM	DY
	R1341	ASM	ASM	DY
	Q127	ASM	ASM	DY
	Q128	ASM	ASM	DY
	C318	ASM	ASM	DY
	U68	ASM	ASM	DY
	C939	ASM	ASM	DY
	C940	ASM	ASM	DY
	C941	ASM	ASM	DY
	U70	ASM	ASM	DY
	U71	ASM	ASM	DY
	U72	ASM	ASM	DY
	C946	ASM	ASM	DY
	C947	ASM	ASM	DY
	C948	ASM	ASM	DY
	R1338	DY	DY	ASM
	R1339	DY	DY	ASM
	R1342	DY	DY	ASM
	R1330	DY	DY	ASM
	R1331	DY	DY	ASM
	R1332	DY	DY	ASM
	R1333	DY	DY	ASM
	R1334	DY	DY	ASM
	R1335	DY	DY	ASM
	R1336	DY	DY	ASM
	R1337	DY	DY	ASM
	CN26	ASM	ASM	DY
34	U73	ASM	ASM	DY
	U74	ASM	ASM	DY
	U75	ASM	ASM	DY
	R1343	DY	DY	ASM
	R1344	DY	DY	ASM
	R1345	DY	DY	ASM
	R1346	DY	DY	ASM

LOGIC

Page	Parts	LPM2.0	LPM1.1	Disable
47	U78	ASM	ASM	DY
	U79	ASM	ASM	DY
	U80	ASM	ASM	DY
	U81	ASM	ASM	DY
	C994	ASM	ASM	DY
	C995	ASM	ASM	DY
	C19	ASM	ASM	DY
	R1366	DY	DY	ASM
	R1367	DY	DY	ASM
	R1368	DY	DY	ASM
	R1369	DY	DY	ASM
	R1370	DY	DY	ASM
	R1371	DY	DY	ASM
	R1372	DY	DY	ASM
53	U82	ASM	ASM	DY
	R404	DY	DY	ASM
	R405	DY	DY	ASM
	R1374	ASM	ASM	DY
	R1373	DY	DY	ASM
61	R1450	DY	DY	ASM
	R1420	ASM	ASM	DY
	R313	ASM	ASM	DY
	R117	DY	DY	ASM
62	Q131	ASM	ASM	DY
	D109	ASM	ASM	DY
	R1441	ASM	ASM	DY
	R1424	ASM	ASM	DY
	R1452	DY	DY	ASM
	R172	ASM	ASM	DY
	R1453	DY	DY	ASM
63	Q19	ASM	ASM	DY
64	Q11	DY	DY	DY
69	U83	ASM	ASM	DY
	U84	ASM	ASM	DY
	R1463	ASM	ASM	DY
	R1464	ASM	ASM	DY
	R1466	ASM	ASM	DY
	R1468	ASM	ASM	DY
	R901	DY	DY	Ref to P101
	R930	DY	DY	Ref to P101
	R536	DY	DY	Ref to P101
	R539	DY	DY	Ref to P101
	C1020	ASM	ASM	DY
	D111	Ref P101	Ref P101	DY
	U42	ASM	ASM	Ref to P101
	Q47	ASM	ASM	Ref to P101
	R617	ASM	ASM	Ref to P101
	C524	ASM	ASM	Ref to P101
	Q48	ASM	ASM	Ref to P101

LOGIC

Page	Parts	LPM2.0	LPM1.1	Disable
70	R1490	ASM	ASM	DY
	R118	DY	ASM	DY
	R119	ASM	DY	DY
	R120	ASM	DY	DY
	R121	DY	ASM	DY
71	D118	ASM	ASM	DY
	D119	ASM	ASM	DY
	R37	ASM	ASM	DY
77	D20	ASM	ASM	Ref to P77
	R143	DY	DY	Ref to P77
96	R953	DY	DY	Ref p96 AOAC table
	Q110	ASM	ASM	Ref p96 AOAC table
	R1639	ASM	ASM	Ref p96 AOAC table
	R1641	ASM	ASM	Ref p96 AOAC table
	D127	ASM	ASM	Ref p96 AOAC table
	C1151	ASM	ASM	Ref p96 AOAC table
	U85	ASM	ASM	DY
	R1643	ASM	ASM	DY
	R1648	ASM	ASM	DY
	R1651	ASM	ASM	DY
	D129	ASM	ASM	DY
	C1153	ASM	ASM	DY
	R1645	DY	DY	ASM
	U86	ASM	ASM	DY
	R1649	ASM	ASM	DY
	R1652	ASM	ASM	DY
	D130	ASM	ASM	DY
	C1183	ASM	ASM	DY
	R1647	DY	DY	ASM
	U87	ASM	ASM	DY
	R1650	ASM	ASM	DY
	R1653	ASM	ASM	DY
	D131	ASM	ASM	DY
	C1184	ASM	ASM	DY

LOGIC

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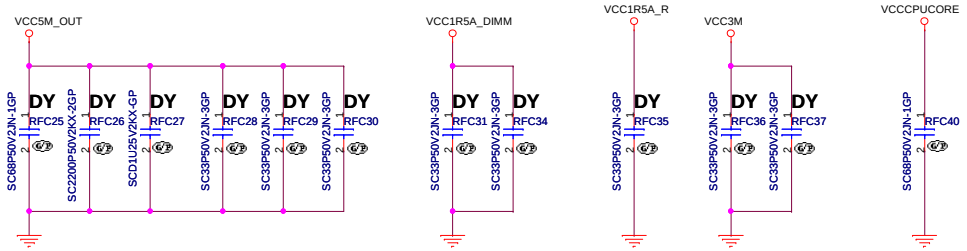
Title: ***LPM SELECT TABLE***

Size	Document Number	Rev
A3	Dasher-2	-1

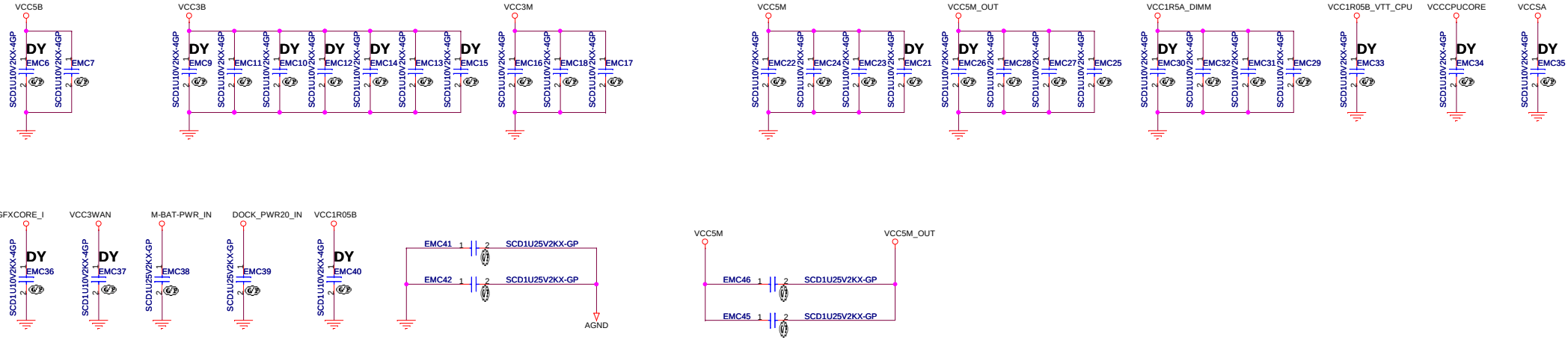
Date: Tuesday, February 28, 2012 Sheet 98 of 101

RF decoupling caps

named as RFCxxx



Long power trace EMI decoupling caps



		SV	ULV
Master IC	U48	VT1318M	VT1318M
# of slave for CPU		2	1
Slave for CPU	U47	VT1324S	VT1324S
	U49	VT1324S	no stuff
Inductor for CPU	L28	BPW10040	no stuff
	L14	no stuff	MPCH0730LR12
# of slave for GPU		1	1
Slave for GPU	U50	VT1324S	VT1324S
Inductor for GPU	L29	MPCH0730LR12	MPCH0730LR12

			SV		ULV	
R_SEL[0]	pin 38	R1574	715	0.5%	280	1.0%
R_SEL[1]	pin 37	R1575	887	0.5%	825	0.5%
R_SEL[2]	pin 36	R1576	0	5.0%	0	5.0%
R_SEL[3]	pin 35	R1577	196	0.5%	196	0.5%
R_SEL[4]	pin 44	R1571	825	0.5%	825	0.5%
R_SEL[5]	pin 32	R1579	475	1.0%	402	0.5%
R_SEL[6]	pin 48	R1567	21.5	1.0%	21.5	1.0%
R_REF	pin 34	R1578	20K	0.5%	20K	0.5%
ROSC	pin 39	R1572	75K	1.0%	61.9K	1.0%

			SV		ULV	
LL_R1_CPU	pin 15 - pin 16	R1599	10K	0.5%	10K	0.5%
LL_R2_CPU	pin 16 - pin 17	R1600	20K	0.5%	7.5K	0.5%
	pin 16 - pin 17	R1601	787	1.0%	453	1.0%
LL_C1_CPU	pin 15 - pin 16	C1086	56pF		33pF	
LL_C2_CPU	pin 16 - pin 17	C1087	22pF		22pF	
LL_RLEAD_CPU	pin 15 - pin 16	R1595	30.1K	1.0%	15K	1.0%
LL_CLEAD_CPU	pin 15 - pin 16	C1091	220pF		680pF	
LL_RLAG_CPU	pin 16 - pin 17	R1596	no stuff		no stuff	
LL_CLAG_CPU	pin 16 - pin 17	C1092	no stuff		no stuff	
RDES_CPU	pin 17 - pin 18	R1592	845	0.5%	487	0.5%
RINT_CPU	pin 18 - pin 19	R1593	1.3K	1.0%	2.74K	1.0%
CINT_CPU	pin 18 - pin 19	C1088	4700PF		3300pF	
RPH11	IPH1_1 - pin 18	R1598	499	1.0%	750	1.0%
RPH12	IPH1_2 - pin 18	R1594	499	1.0%	no stuff	1.0%
IPHF11_R	pin 11	R1582	1.96K	1.0%	1.96K	1.0%
IPHF11_C	pin 11	C1076	10pF		no stuff	
IPHF12_R	pin 10	R1581	1.96K	1.0%	no stuff	1.0%
IPHF12_C	pin 10	C1075	10pF		no stuff	
R_MRAMP1	pin 12	R1584	15.8K	1.0%	13K	1.0%
R_MRAMP1_PU	pin 12	R1587	60.4K	1.0%	43.2K	1.0%
R_PWM2	Pin 6	R4	no stuff		0	5.0%
LL_R1_GPU	pin 24 - pin 23	R1609	10K	0.5%	10K	0.5%
LL_R2_GPU	pin 23 - pin 22	R1610	7.5K	0.5%	7.87K	0.5%
	pin 23 - pin 22	R1611	237	1.0%	0	5.0%
LL_C1_GPU	pin 24 - pin 23	C1094	22pF		22pF	
LL_C2_GPU	pin 23 - pin 22	C1095	22pF		22pF	
LL_RLEAD_GPU	pin 24 - pin 23	R1606	30K	1.0%	30K	1.0%
LL_CLEAD_GPU	pin 24 - pin 23	C1097	1200pF		1000pF	
LL_RLAG_GPU	pin 23 - pin 22	R1607	no stuff		no stuff	
LL_CLAG_GPU	pin 23 - pin 22	C1098	no stuff		no stuff	
RDES_GPU	pin 22 - pin 21	R1604	665	0.5%	665	0.5%
RINT_GPU	pin 21 - pin 20	R1605	3.24K	1.0%	3.24K	1.0%
CINT_GPU	pin 21 - pin 20	C1096	10nF		10nF	
RPH21	IPH2_1 - pin 21	R1608	1K	1.0%	1K	1.0%
IPHF21_R	pin 28	R1580	1.96K	1.0%	1.96K	1.0%
IPHF21_C	pin 28	C1077	no stuff		no stuff	
R_MRAMP2	pin 27	R1586	15.4K	1.0%	15.4K	1.0%
R_MRAMP2_PU	pin 27	R1590	56.2K	1.0%	56.2K	1.0%

			SV	ULV
		C1090	4.7uF	no stuff
		R1589	10 1.0%	no stuff
		C1089	0.1uF	no stuff
		C1082	0.22uF	no stuff
		C1083	1uF	no stuff
		C1080	1uF	no stuff
		C1084	10uF	no stuff
		C1079	10uF	no stuff
		C1081	10uF	no stuff
		C1085	10uF	no stuff
		L50	MPZ1608S300A	no stuff
		L51	MPZ1608S300A	no stuff
		C1078	2200pF	no stuff

Vcore Coutput

	SV		ULV	
C687	22uF	0805	22uF	0805
C669	22uF	0603	22uF	0603
C671	22uF	0603	22uF	0603
C673	4.7uF	0603	22uF	0603
C675	22uF	0603	22uF	0603
C676	22uF	0603	22uF	0603
C678	22uF	0603	22uF	0603
C695	4.7uF	0603	22uF	0603
C681	4.7uF	0603	22uF	0603
C682	4.7uF	0603	22uF	0603
C683	22uF	0603	22uF	0603
C750	22uF	0603	22uF	0603
C686	22uF	0603	22uF	0603
C691	22uF	0603	22uF	0603
C670	22uF	0603	22uF	0603
C694	22uF	0603	22uF	0603
C698	4.7uF	0603	22uF	0603
C1112	22uF	0603	22uF	0603
C1113	22uF	0603	22uF	0603
C672	22uF	0603	22uF	0603
C1157	22uF	0603	22uF	0603
C674	22uF	0603	22uF	0603
C1160	22uF	0603	22uF	0603
C689	22uF	0603	22uF	0603
C1167	22uF	0603	22uF	0603
C680	22uF	0603	22uF	0603
C679	4.7uF	0603	10uF	0603
C677	4.7uF	0603	10uF	0603
C1156	4.7uF	0603	10uF	0603
C684	10uF	0603	10uF	0603
C693	4.7uF	0603	10uF	0603
C1159	10uF	0603	10uF	0603
C692	10uF	0603	10uF	0603
C1158	4.7uF	0603	10uF	0603
C1161	4.7uF	0603	10uF	0603
C1162	4.7uF	0603	10uF	0603
C690	N/A	0603	N/A	0603
C1109	N/A	0603	N/A	0603
C1110	N/A	0603	N/A	0603
C1111	N/A	0603	N/A	0603
C1163	N/A	0603	N/A	0603
C1164	22uF	0603	N/A	0603
C1165	N/A	0603	N/A	0603
C1166	22uF	0603	N/A	0603
C1168	N/A	0603	N/A	0603
C1169	22uF	0603	N/A	0603
C444	2.2uF	0402	2.2uF	0402
C445	2.2uF	0402	2.2uF	0402
C446	2.2uF	0402	2.2uF	0402
C447	2.2uF	0402	2.2uF	0402
C448	2.2uF	0402	2.2uF	0402
C449	2.2uF	0402	2.2uF	0402
C450	2.2uF	0402	N/A	0402
C451	2.2uF	0402	2.2uF	0402
C452	2.2uF	0402	2.2uF	0402
C453	2.2uF	0402	2.2uF	0402

	SV		ULV	
C477	2.2uF	0402	2.2uF	0402
C486	2.2uF	0402	2.2uF	0402
C518	2.2uF	0402	2.2uF	0402
C520	2.2uF	0402	2.2uF	0402
C521	2.2uF	0402	2.2uF	0402
C522	2.2uF	0402	N/A	0402
C533	2.2uF	0402	2.2uF	0402
C830	2.2uF	0402	2.2uF	0402
C831	2.2uF	0402	2.2uF	0402
C832	2.2uF	0402	2.2uF	0402
C1172	N/A	0402	N/A	0402
C1173	2.2uF	0402	N/A	0402
C1174	2.2uF	0402	N/A	0402
C1175	2.2uF	0402	N/A	0402
C1176	N/A	0402	N/A	0402
C1177	N/A	0402	N/A	0402
C1178	2.2uF	0402	2.2uF	0402
C1179	2.2uF	0402	2.2uF	0402
C1180	2.2uF	0402	2.2uF	0402
C1181	2.2uF	0402	2.2uF	0402
C1182	2.2uF	0402	2.2uF	0402
C1185	2.2uF	0402	2.2uF	0402
C1186	2.2uF	0402	2.2uF	0402
C1187	2.2uF	0402	2.2uF	0402
C1188	2.2uF	0402	2.2uF	0402
C1189	2.2uF	0402	2.2uF	0402
C1190	2.2uF	0402	2.2uF	0402
C1191	2.2uF	0402	2.2uF	0402
C1192	2.2uF	0402	2.2uF	0402
C1193	2.2uF	0402	2.2uF	0402
C1194	2.2uF	0402	2.2uF	0402
C1195	2.2uF	0402	2.2uF	0402
C1196	2.2uF	0402	2.2uF	0402

<Core Design>

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Title: ***VT1318M TABLE***

Size: A2 | Document Number: **Dasher-2** | Rev: -1

Date: Tuesday, March 06, 2012 | Sheet 100 of 101

	Capability											
Config	1	2	3	4	5	6	7	8	9	10	11	12
AOAC	Yes	Yes	Yes	Yes	No	No	No	No	Yes	No	Yes	No
Anti-Theft	Yes	Yes	No	No	Yes	Yes	No	No	Yes	Yes	No	No
External EEPROM	Yes	No	Yes	No	Yes	No	Yes	No	Yes	Yes	Yes	Yes
Duplicity	No	No	No	No	No	No	No	No	Yes	Yes	Yes	Yes
U66	ASM	ASM	DY	DY	DY	DY	DY	DY	ASM	ASM	DY	DY
U67	ASM	ASM	DY	DY	DY	DY	DY	DY	ASM	ASM	DY	DY
C864	ASM	ASM	DY	DY	DY	DY	DY	DY	ASM	ASM	DY	DY
R978	ASM	ASM	DY	DY	DY	DY	DY	DY	ASM	ASM	DY	DY
R929	ASM	ASM	DY	DY	DY	DY	DY	DY	ASM	ASM	DY	DY
R901	ASM	ASM	DY	DY	DY	DY	DY	DY	ASM	ASM	DY	DY
R930	ASM	ASM	DY	DY	DY	DY	DY	DY	ASM	ASM	DY	DY
R1462	ASM	ASM	DY	DY	DY	DY	DY	DY	ASM	ASM	DY	DY
Q14	ASM	DY	DY	DY	DY	DY	DY	DY	ASM	ASM	DY	DY
Q48	ASM	DY	ASM	DY	ASM	DY	ASM	DY	ASM	ASM	ASM	ASM
R624	ASM	DY	ASM	DY	ASM	DY	ASM	DY	ASM	ASM	ASM	ASM
R536	DY	DY	DY	DY	ASM	ASM	DY	DY	DY	DY	DY	DY
R539	DY	DY	DY	DY	ASM	ASM	DY	DY	DY	DY	DY	DY
U42	ASM	DY	ASM	DY	ASM	DY	ASM	DY	ASM	ASM	ASM	ASM
Q47	ASM	DY	ASM	DY	ASM	DY	ASM	DY	ASM	ASM	ASM	ASM
R617	ASM	DY	ASM	DY	ASM	DY	ASM	DY	ASM	ASM	ASM	ASM
C524	ASM	DY	ASM	DY	ASM	DY	ASM	DY	ASM	ASM	ASM	ASM
D111	DY	DY	DY	DY	DY	DY	DY	DY	ASM	ASM	DY	DY
R1463	DY	DY	DY	DY	DY	DY	DY	DY	ASM	ASM	ASM	ASM
R1464	DY	DY	DY	DY	DY	DY	DY	DY	ASM	ASM	ASM	ASM
U83	DY	DY	DY	DY	DY	DY	DY	DY	ASM	ASM	ASM	ASM
U84	DY	DY	DY	DY	DY	DY	DY	DY	ASM	ASM	ASM	ASM
C1020	DY	DY	DY	DY	DY	DY	DY	DY	ASM	ASM	ASM	ASM
R1466	DY	DY	DY	DY	DY	DY	DY	DY	ASM	ASM	ASM	ASM
R1468	DY	DY	DY	DY	DY	DY	DY	DY	ASM	ASM	ASM	ASM

Logic

Duplicity	Y	Y	N	N
Battery Authentification	Y	N	Y	N
--> External EEPROM	Y	Y	Y	N

Logic

<Core Design>

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Title: SMBus Logic Table

Size: A3 | Document Number: Dasher-2 | Rev: -1

Date: Tuesday, February 21, 2012 | Sheet 101 of 101